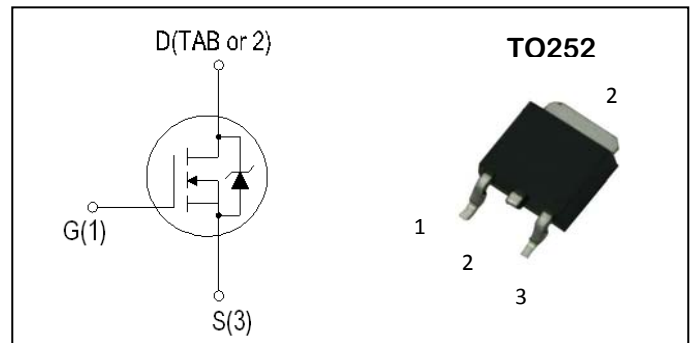


N-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

V_{DSS}	I_D	$R_{DS(ON)}$ (m Ω)
60V	40A	18m Ω



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Ratings	Unit
Common Ratings				
V_{DSS}	Drain-Source Voltage		60	V
V_{GSS}	Gate-Source Voltage		± 20	
T_J	Maximum Junction Temperature		150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range		-55 to 175	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	40	A
Mounted on Large Heat Sink				
I_{DM}	300 μs Pulse Drain Current Tested(1)	$T_C=25^\circ\text{C}$	90	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	40	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	20	W

1. Pulse width limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	Ratings	Unit
R_{thJC}	Thermal resistance junction-case max	2.0	$^\circ\text{C}/\text{W}$
R_{thJA}	Thermal resistance junction-ambient max	40	$^\circ\text{C}/\text{W}$

Electrical Characteristics (TA=25°C Unless Otherwise Noted)

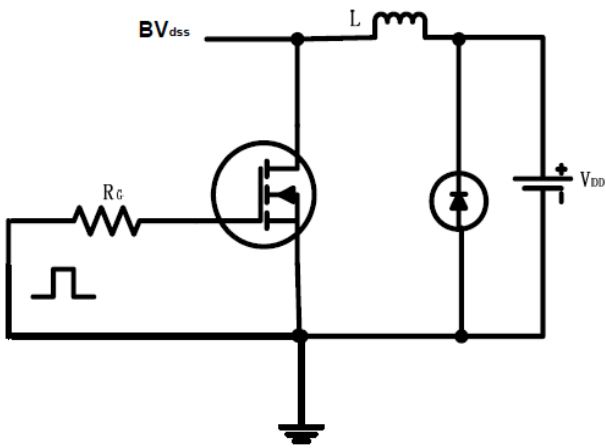
Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
On/off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250uA	60	--	--	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 48V, V _{GS} =0V	--	--	1	uA
		V _{DS} =48V, V _{GS} =0V T _J =85°C	--	--	5	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250uA	1	--	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	--	--	±100	nA
R _{DS(ON)}	Drain-SourceOn-stateResistance(2)	V _{GS} = 10V, I _{DS} =15A	--	16.0	18.0	mΩ
g _{FS}	Forward transconductance(2)	V _{DS} = 10V, I _{DS} =15A	--	14	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} = 25V, Frequency=1.0MHz	--	540	--	pF
C _{oss}	Output Capacitance		--	74	--	
C _{rss}	Reverse Transfer Capacitance		--	34	--	
Switching Characteristics						
t _{d(ON)}	Turn-on Delay Time(1)	V _{DD} =30V, I _D = 5A, V _{GS} = 10V, R _{GEN} =3Ω	--	6	--	ns
t _r	Turn-on Rise Time(1)		--	4.6	--	
t _{d(OFF)}	Turn-off Delay Time(1)		--	22	--	
t _f	Turn-off Fall Time(1)		--	4	--	
Q _g	Total Gate Charge(1)	V _{DS} =30V, V _{GS} = 10V, I _{DS} =12A	--	9	--	nC
Q _{gs}	Gate-Source Charge(1)		--	1.6	--	
Q _{gd}	Gate-Drain Charge(1)		--	1.8	--	
Diode Characteristics						
V _{SD}	Diode Forward Voltage(2)	I _{SD} = 1A, V _{GS} = 0	--	--	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =15A, dI _{SD} /dt=50A/μs	--	42.0	--	ns
q _{rr}	Reverse Recovery Charge		--	31.0	--	nC

NOTES:

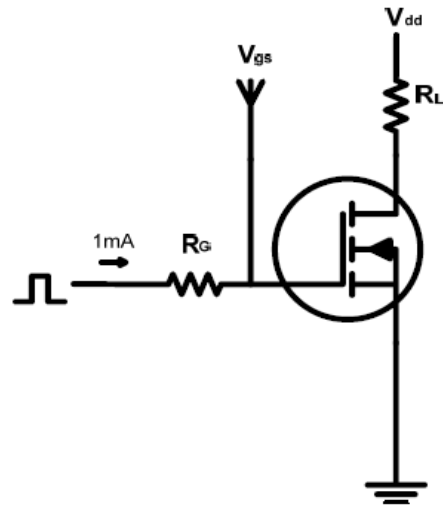
1. Independent of operating temperature.
2. Pulse Test : Pulse width ≤ 300 μs, Duty cycle ≤ 2%

Test circuits and Waveforms

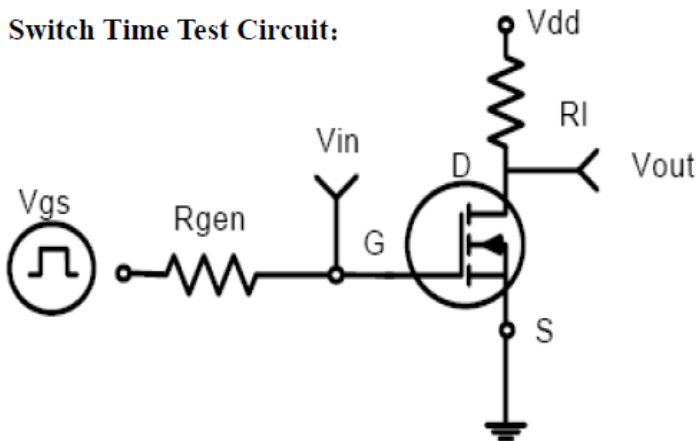
EAS test circuits:



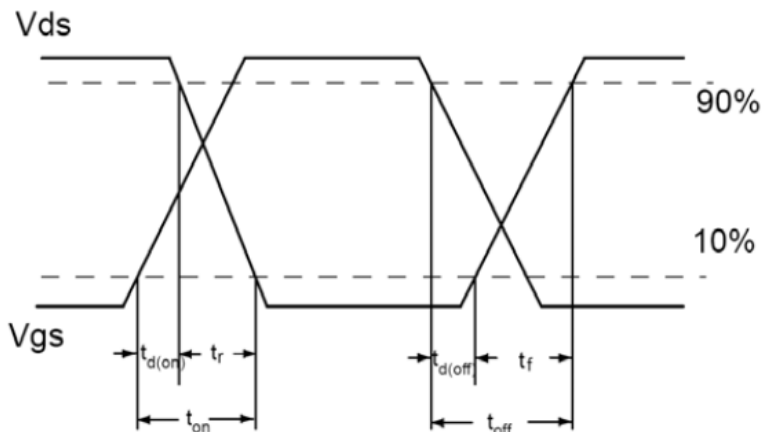
Gate charge test circuit:



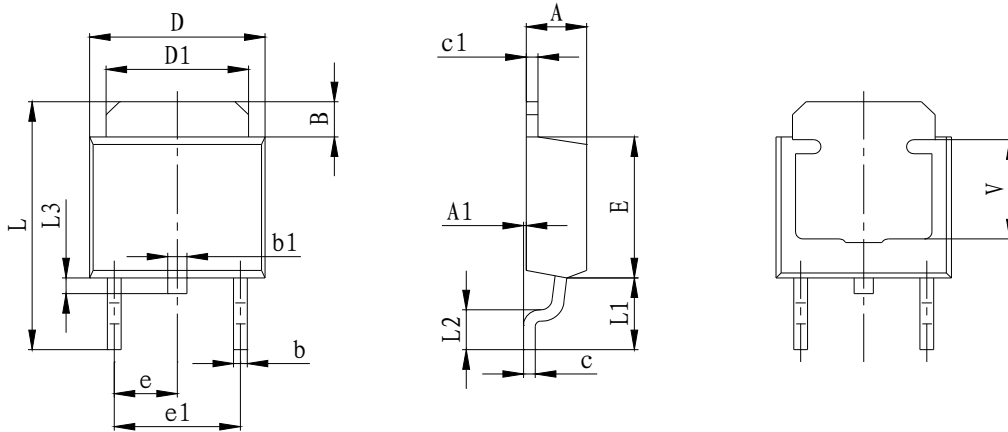
Switch Time Test Circuit:



Switch Waveforms:



PACKAGE MECHANICAL DATA
TO-252 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	