

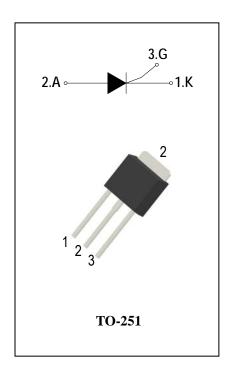
SCRs

General Description

Available either in sensitive or standard gate triggering levels, the 12A SCR series is suitable to fit all modes of control found inapplications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, in-rush current limiting circuits, capacitive discharge ignition, voltage regulation circuits...

Features

- ◆ Repetitive Peak Off-State Voltage: 600V and 800V
- ◆ R.M.S On-State Current (IT(RMS)= 12 A)
- ♦ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	Ti=25°C	ADS12A60D	600	V
V_{RRM}	Repetitive peak reverse voltage	Tj=25°C	ADS12A80D	800	V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave , To	8	Α	
I _{T(RMS)}	R.M.S On-State Current	Half Sine Wave , Tc = 105°C		12	Α
I _{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		140	А
l ² t	I ² t for Fusing	Tj =25°C,tp =10ms		98	A ² S
P _{GM}	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width ≤ 20μs		5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W
I _{GM}	Peak Gate Current	Tj =125°C, Pulse Width ≤ 20μs		4	Α
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T _{STG}	Storage Temperature			- 40 ~ 150	°C





ADS12A60D/80D

Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS12A60D/80D		Unit	
				Т	S	Blank	
		$V_{DRM} = V_{RRM}, R_{GK} = 1K\Omega$			5		uA
I _{DRM}	Peak Forward Reverse	Tj = 25°C	Max				u, (
I _{RRM}	Blocking Current	$V_{DRM} = V_{RRM}, R_{GK} = 1K\Omega$ Tj = 125°C	Max.				mA
V _{TM}	Peak On-State Voltage	I _{TM} = 24A, t _p = 380 μs	Max.	1.55		V	
$V_{\sf GD}$	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $R_{GK} = 1 \text{K}\Omega$ $Tj = 125 ^{\circ}\text{C}$	Min.	0.2		٧	
V_{GT}	Gate Trigger Voltage	V 40V B 000	Max.	1.5		V	
I _{GT}	Gate Trigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	0.2	15	30	mA
I _H	Holding Current	$I_T = 0.5A$ $R_{GK} = 1K\Omega$	Max.	5	30	40	mA
ΙL	Latching Current	$I_G = 1.2 I_{GT} R_{GK} = 1K\Omega$	Max.	7	50	60	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3V_{DRM}$ gate open $R_{GK} = 1K\Omega$ $Tj = 125^{\circ}C$	Min.	200	500	600	V/µs
R _{th(j-c)}	Junction to case (AC)		Max.		1.8		°C/W
R _{th(j-a)}	Junction to ambient		Max.		100	-	°C/W

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ADV

FIG.1: Maximum average power dissipation (Single phase half wave)

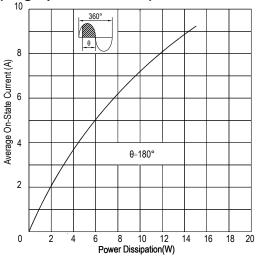
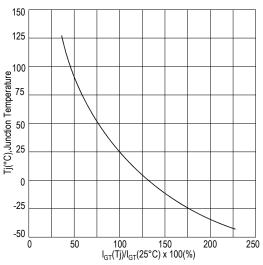


FIG.3: Gate trigger current VS Junction temperature



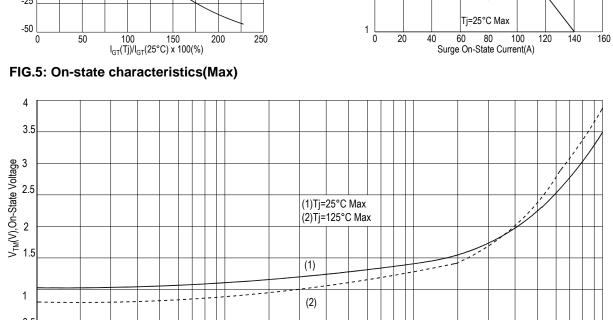


FIG.2: Average on-state current VS Allowable case Temperature(Single phase half wave)

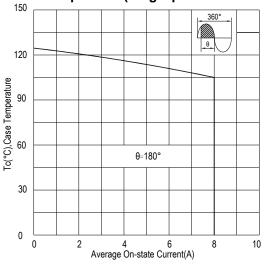
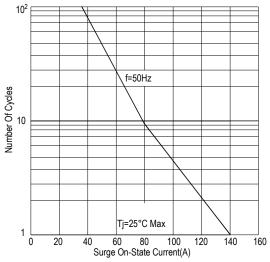


FIG.4: Rated surge on-state current (Non-Repetitive)



10²

I_{TM}(A),On-State Current

10¹

10⁰



ADS12A60D/80D

FIG.6:Holding current and Latching current VS Junction temperature

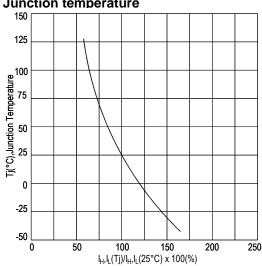


FIG.8: Gate trigger current VS Junction temperature for type T gate triggering

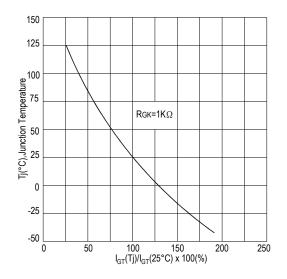


FIG.7: Gate trigger voltage VS Junction

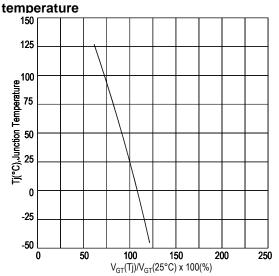
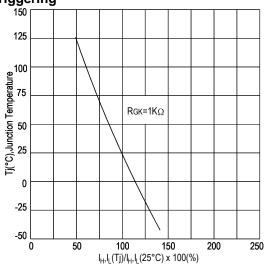


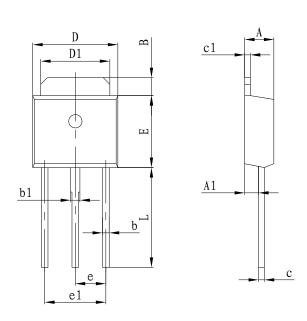
FIG.8:Holding current and Latching current
VS Junction temperature for type T gate
triggering





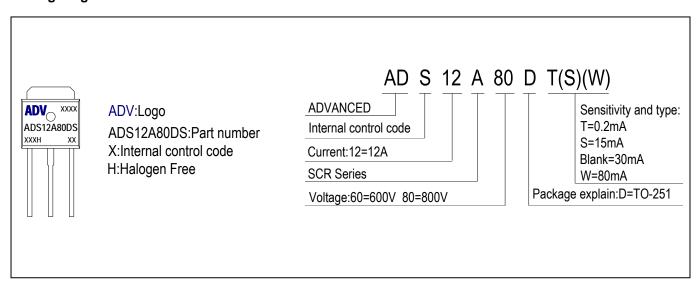
PACKAGE MECHANICAL DATA

TO-251 Package Dimension



	Dimensions In		Dimensions In		
Symbol	Millimeters		Inches		
	Min	Max	Min	Max	
Α	2.200	2.400	0.087	0.094	
A1	0.900	1.100	0.035	0.043	
В	1.350	1.650	0.053	0.065	
b	0.500	0.700	0.020	0.028	
b1	0.700	0.900	0.028	0.035	
С	0.430	0.620	0.017	0.024	
c1	0.480	0.620	0.019	0.024	
D	6.350	6.700	0.252	0.264	
D1	5.100	5.400	0.200	0.213	
Е	6.000	6.200	0.236	0.244	
е	2.300TYP		0.091TYP		
e1	4.500	4.700	0.177	0.185	
L	8.900	9.400	0.350	0.370	

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity			
ADS12A60D#	TO-251	ADS12A60D#	Tube	80pcs			
ADS12A80D#	TO-251	ADS12A80D#	Tube	80pcs			
Note:# = Gate Trigger Current Sensitivity and type							



ADS12A60D/80D

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