

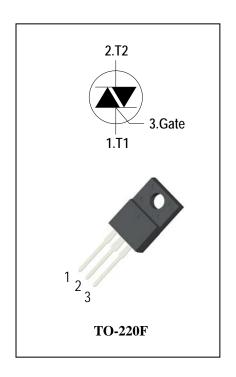
### 3 Quadrants High temperature Triacs

### **General Description**

High current density due to mesa technology , guaranteed maximum junction temperature 150° C. The ADS12CH triac series is suitable for general purpose AC switching. They can beused as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers. The heatsink can be reduced,compared to traditional triacs, according to the high performance at given junction temperatures.

#### **Features**

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (IT(RMS)= 12A)
- ♦ High Commutation dv/dt
- ◆ High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



### **Absolute Maximum Ratings**

Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$	Denetitive Deals Off Chate Veltage	T: 05°0	ADS12CH60F	600	V
$V_{RRM}$	Repetitive Peak Off-State Voltage	Tj = 25°C	ADS12CH80F	800	V
I <sub>T(RMS)</sub>	R.M.S On-State Current	T <sub>C</sub> = 120 °C	12	Α	
I <sub>TSM</sub>	Surge On-State Current	tp=20ms(50Hz)/tp=16.7	120/126	А	
l <sup>2</sup> t	I <sup>2</sup> t for fusing	tp=10ms		78	$A^2s$
-11/-14	Critical rate of rise of on-state	cal rate of rise of on-state F = 120 Hz Tj = 150°C		50	Δ /
dI/dt	current $I_G = 2 \times I_{GT}$ , tr $\leq 100 \text{ ns}$		50	A/µs	
I <sub>GM</sub>	Peak Gate Current	tp = 20 μs Tj = 150°C		4	Α
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=150°C)			1	W
$P_GM$	Peak Gate Power Dissipation(tp=20us,Tj=150°C)			10	W
T <sub>j</sub>	Operating Junction Temperature			- 40 ~ 150	°C
T <sub>STG</sub>	Storage Temperature			- 40 ~ 150	°C





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### **ADS12CH60F/80F**

### **Electrical Characteristics**( Tj = 25°C unless otherwise specified )

Symbol	Items Conditions		Conditions		ADS12CH60F/80F			Unit
					S	Blank	В	
I <sub>DRM</sub>	Peak Forward Reverse Blocking		$V_{DRM} = V_{RRM}$ , $Tj = 25$ °C	May	5		uA	
I <sub>RRM</sub>	Current		$V_{DRM} = V_{RRM}$ , $Tj = 150$ °C	Max.	3.8			mA
V <sub>TM</sub>	Peak On-S	tate Voltage	I <sub>TM</sub> = 17A, t <sub>p</sub> = 380 μs	Max.	1.5			٧
$V_{GD}$	Q1-Q2-Q3	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 150^{\circ}\text{C}$	Min.	0.2		V	
$V_{GT}$	Q1-Q2-Q3	Gate Trigger Voltage	1/ 40V D 000	Max.	1.5		V	
I <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Current	$V_D = 12V$ , $R_L = 33\Omega$ Max.	Max.	10	35	50	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.1A	Max.	20	50	75	mA
ΙL	Q1-Q3	Latabia a Commant	1 - 401	Max.	20	80	90	mA
	Q2	Latching Current	$I_G = 1.2 I_{GT}$		35	90	110	
dV/dt	Critical Rate of Rise of Off-State  Voltage		$V_D = 2/3V_{DRM}$ gate open Tj = 150°C	Min.	200	1000	1500	V/µs
(dV/dt)c	Critical Rate of Change of Commutating Voltage		$V_D$ =400V Tj = 150°C (dl/dt)c=-5.3A/ms	Min.	1	16	21	V/µs
R <sub>th(j-c)</sub>	Junction to case (AC)		Max.	2.3		°C/W		
R <sub>th(j-a)</sub>	Junction to ambient		Max.	60		°C/W		

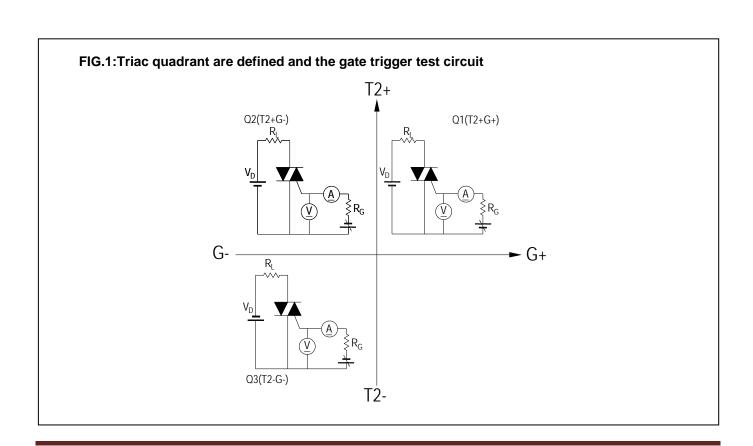


FIG.2: Maximum on-state power dissipation

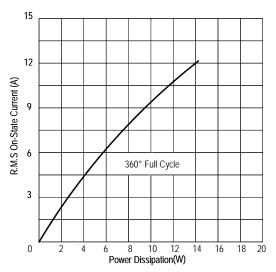


FIG.4: Maximum transient thermal impedance

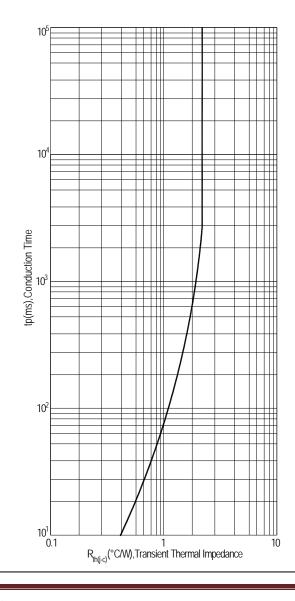


FIG.3: Typical RMS on-state current VS Allowable case Temperature

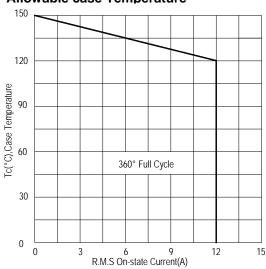


FIG.5: Rated surge on-state current (Non-Repetitive)

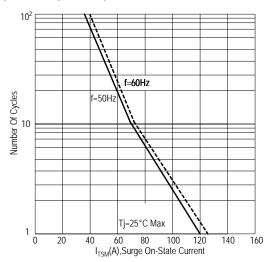
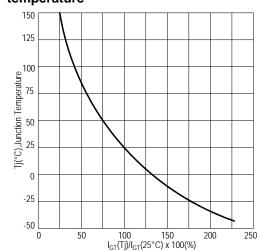


FIG.6: Gate trigger current VS Junction temperature





## **ADS12CH60F/80F**

FIG.7:Holding current and Latching current VS Junction temperature

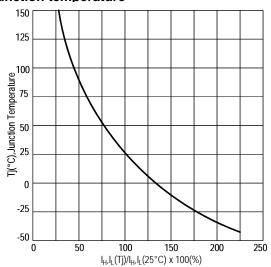


FIG.8: Gate trigger voltage VS Junction temperature

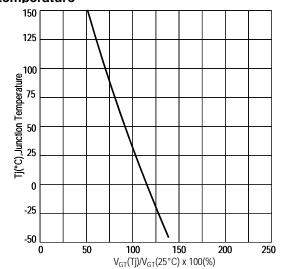
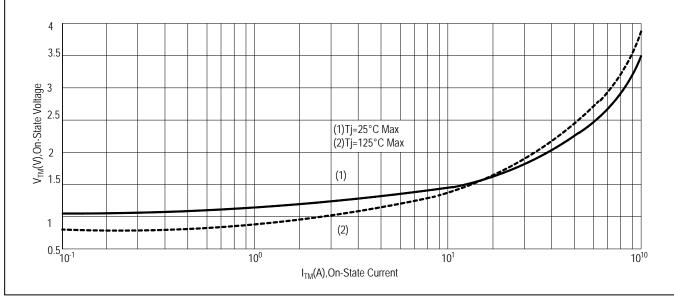


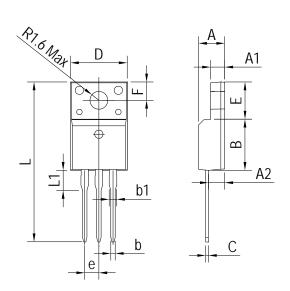
FIG.9: On-state characteristics(Max)



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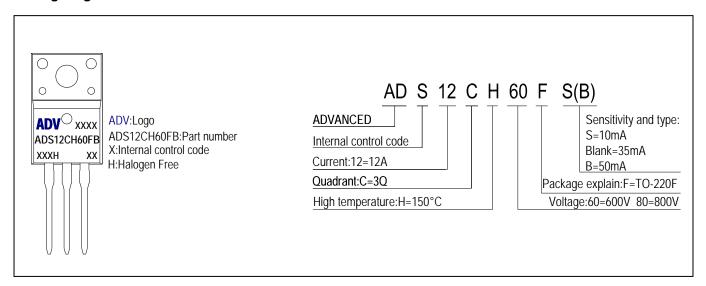


# PACKAGE MECHANICAL DATA TO-220F Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	4.300	4.800	0.169	0.189	
A1	2.400	2.700	0.094	0.106	
A2	2.500	3.000	0.098	0.118	
В	8.800	9.300	0.346	0.367	
b	0.600	0.950	0.023	0.037	
b1	1.100	1.700	0.043	0.067	
С	0.500	0.750	0.020	0.030	
D	9.700	10.360	0.382	0.408	
Е	6.400	6.800	0.252	0.268	
е	2.540 TYP		0.100 TYP		
F	3.300 REF		0.130 REF		
L	L 28.000 30.000		1.102	1.181	
L1	2.900	3.630	0.114	0.143	

#### **Making Diagram**



### **Ordering information**

Part number	Package	Marking	Packing	Quantity		
ADS12CH60F#	TO-220F	ADS12CH60F#	Tube	50pcs		
ADS12CH80F#	TO-220F	ADS12CH80F#	Tube	50pcs		
Note:# = Gate Trigger Current Sensitivity and type						



### **ADS12CH60F/80F**

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