## ADS16A60G/80G

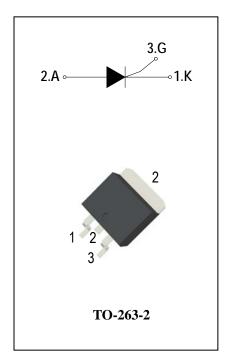
## ADV SCRs

### **General Description**

The 16A SCR series of silicon controlled rectifiers, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

#### Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current ( IT(RMS)= 16A )
- ♦ These are Pb-Free Devices



### **Absolute Maximum Ratings**

Symbol	Items	Conditions		Ratings	Unit
V <sub>DRM</sub>	Repetitive Peak Off-State Voltage	Ti=25°C	ADS16A60G	600	V
V <sub>RRM</sub>	Repetitive peak reverse voltage	Tj=25°C	ADS16A80G	800	V
I <sub>T(AV)</sub>	Average On-State Current	Half Sine Wave , Tc = 110°C		10	А
I <sub>T(RMS)</sub>	R.M.S On-State Current	Half Sine Wave , Tc = 110°C		16	А
I <sub>TSM</sub>	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		190	А
l <sup>2</sup> t	I <sup>2</sup> t for Fusing	Tj =25°C,tp =10ms		180	A <sup>2</sup> S
P <sub>GM</sub>	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width $\leq 20\mu_S$		5	W
P <sub>G(AV)</sub>	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W
I <sub>GM</sub>	Peak Gate Current	Tj =125°C, Pulse Width $\leq 20\mu s$		4	А
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T <sub>STG</sub>	Storage Temperature			- 40 ~ 150	°C



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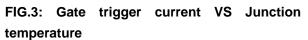
## Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS16A60G/80G		Unit
				s	Blank	
		$V_{DRM} = V_{RRM}$		5		uA
I <sub>DRM</sub>	Peak Forward Reverse	Tj = 25°C	Max.			
I <sub>RRM</sub>	Blocking Current	V <sub>DRM</sub> = V <sub>RRM</sub> Tj = 125°C	Max.	2		mA
V <sub>TM</sub>	Peak On-State Voltage	I <sub>TM</sub> = 32A, tp = 380 μs	Max.	1.6		V
$V_{\text{GD}}$	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ Tj = 125°C	Min.	0.2		V
$V_{\text{GT}}$	Gate Trigger Voltage		Max.	1.3		V
I <sub>GT</sub>	Gate Trigger Current	$V_D = 12V$ , $R_L = 33\Omega$	Max.	15	30	mA
I <sub>H</sub>	Holding Current	I <sub>T</sub> = 0.5A	Max.	30	40	mA
ΙL	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	50	60	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3V_{DRM}$ gate open Tj = 125°C	Min.	500	600	V/µs
R <sub>th(j-c)</sub>	Junction to case (AC)		Max.	1.1		°C/W
R <sub>th(j-a)</sub>	Junction to ambient(Copper surface under tab:S=1cm <sup>2</sup> )		Max.	45		°C/W

## <u>ADV</u>

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FIG.1: Maximum average power dissipation (Single phase half wave) 360° θ 8 Average On-State Current (A) 6 4 θ=180° 2 0 2 4 6 8 10 12 14 16 18 20 Power Dissipation(W)



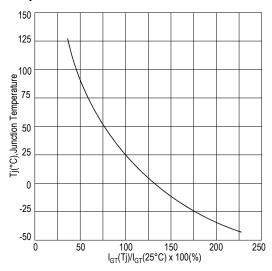
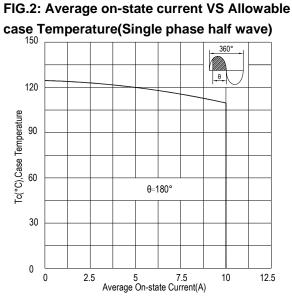
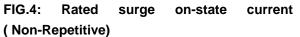
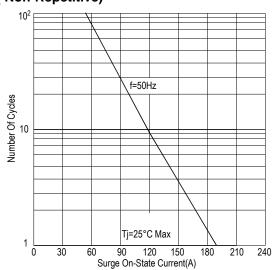
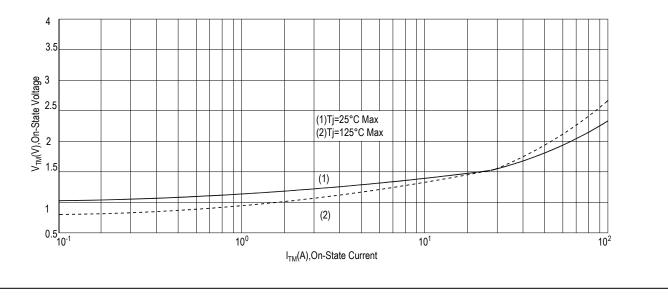


FIG.5: On-state characteristics(Max)



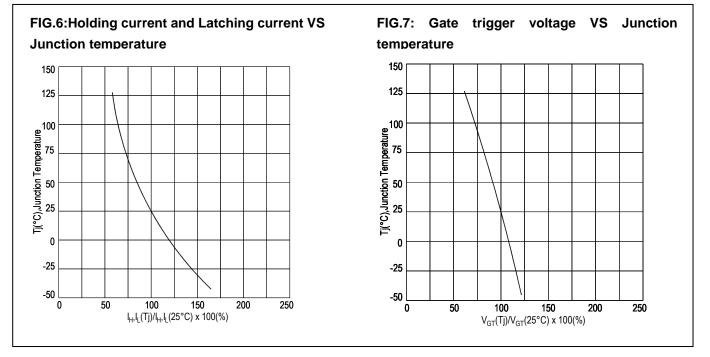






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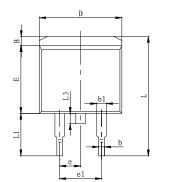
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# ADV

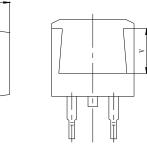
## ADS16A60G/80G

## PACKAGE MECHANICAL DATA TO-263-2 Package Dimension



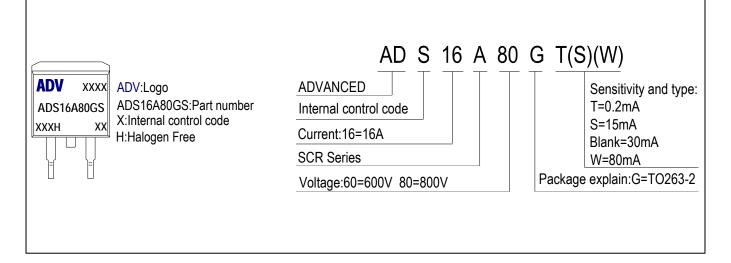
A1

2



Sumb	Dimensions		Dimensions		
Symb	In Millimeters		In Inches		
ol	Min	Max	Min	Max	
А	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
с	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540 TYP		0.100 TYP		
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.600 REF		0.220 REF		

#### Making Diagram



#### Ordering information

Part number	Package	Marking	Packing	Quantity	
	TO-263-2	ADS16A60G#	Tube	50pcs	
ADS16A60G#			Embossed tape	800pcs	
	TO-263-2	ADS16A80G#	Tube	50pcs	
ADS16A80G#			Embossed tape	800pcs	
Note:# = Gate Trigger Current Sensitivity and type					

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