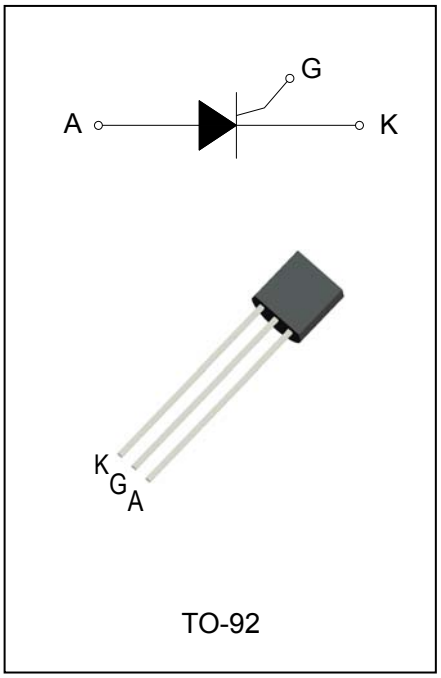
SCRs



General Description

Sensitive triggering SCR is suitable for the application where gate current limited such as small motor control， Earth leakage circuit breakers or Ground Fault Circuit Interrupters (GFCI) ， Solid state relays，General purpose switching，Small engine ignition。

Features

- Repetitive Peak Off-State Voltage : 600V and 800V
- R.M.S On-State Current ($I_{T(RMS)}$= 1A)
- These are Pb-Free Devices

Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	Tj=25°C	ADS1A60	600	V
V_{RRM}	Repetitive peak reverse voltage		ADS1A80	800	V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave , Tc = 50°C		0.7	A
$I_{T(RMS)}$	R.M.S On-State Current	Half Sine Wave , Tc = 50°C		1	A
I_{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)		12	A
dI/dt	Critical rate of rise of on-state current	Tj =110°C, tr≤ 100ns		50	A/μs
I^2t	I^2t for Fusing	Tj =25°C,tp =10ms		0.72	A^2S
P_{GM}	Forward Peak Gate Power Dissipation	Tj =110°C, Pulse Width ≤ 1.0μs		0.5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		0.1	W
I_{GM}	Peak Gate Current	Tj =110°C, Pulse Width ≤ 1.0μs		0.3	A
Tj	Operating Junction Temperature			- 40 ~ 110	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C

ADV
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Electrical Characteristics (T_j = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS1A60/80	Unit
I_{DRM} I_{RRM}	Peak Forward Reverse Blocking Current	V_{DRM} = V_{RRM}, R_{GK} = 1KΩ Tj = 25°C	Max.	5	uA
		V_{DRM} = V_{RRM}, R_{GK} = 1KΩ Tj = 110°C		0.1	mA
V_{TM}	Peak On-State Voltage	I_{TM} = 2A, tp = 380 μs	Max.	1.7	V
V_{GD}	Non-Trigger Gate Voltage	V_D = V_{DRM} R_L = 3.3 kΩ R_{GK} = 1KΩ Tj = 110°C	Min.	0.2	V
V_{GT}	Gate Trigger Voltage	V_D = 6V ， R_L = 100Ω	Max.	0.8	V
I_{GT}	Gate Trigger Current		Max.	200	uA
I_H	Holding Current	I_T = 0.05A R_{GK} = 1KΩ	Max.	3	mA
I_L	Latching Current	I_G = 1mA R_{GK} = 1KΩ	Max.	5	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	V_D = 2/3V_{DRM} gate open R_{GK} = 1KΩ Tj = 110°C	Min.	50	V/μs
$R_{th(j-c)}$	Junction to case		Max.	70	°C/W
$R_{th(j-a)}$	Junction to ambient		Max.	150	°C/W

FIG.1: Maximum average power dissipation (Single phase half wave)

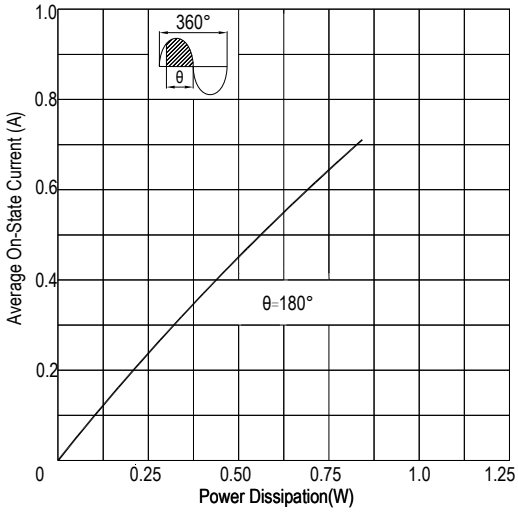


FIG.2: Average on-state current VS Allowable case Temperature(Single phase half wave)

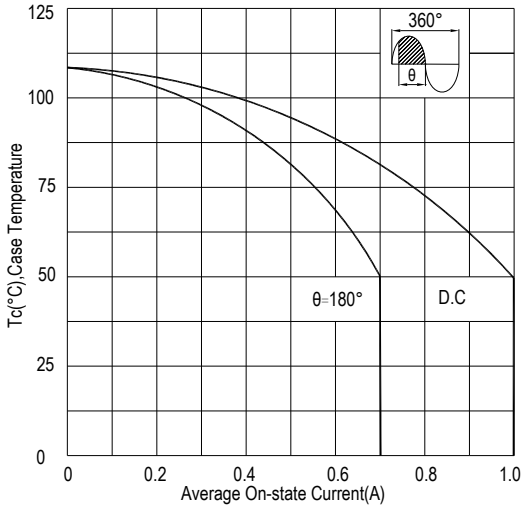


FIG.3: Gate trigger current VS Junction temperature

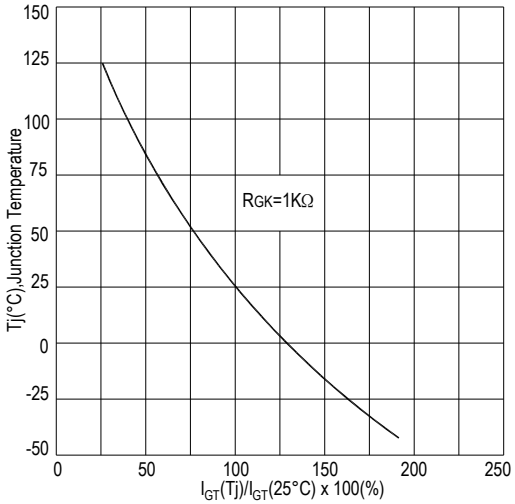


FIG.4: Rated surge on-state current (Non-Repetitive)

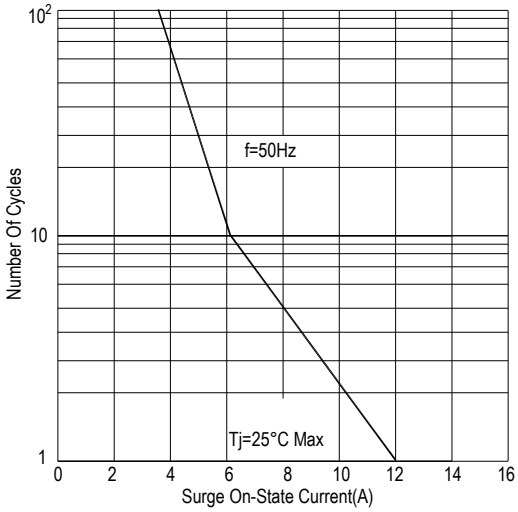


FIG.5: On-state characteristics(Max)

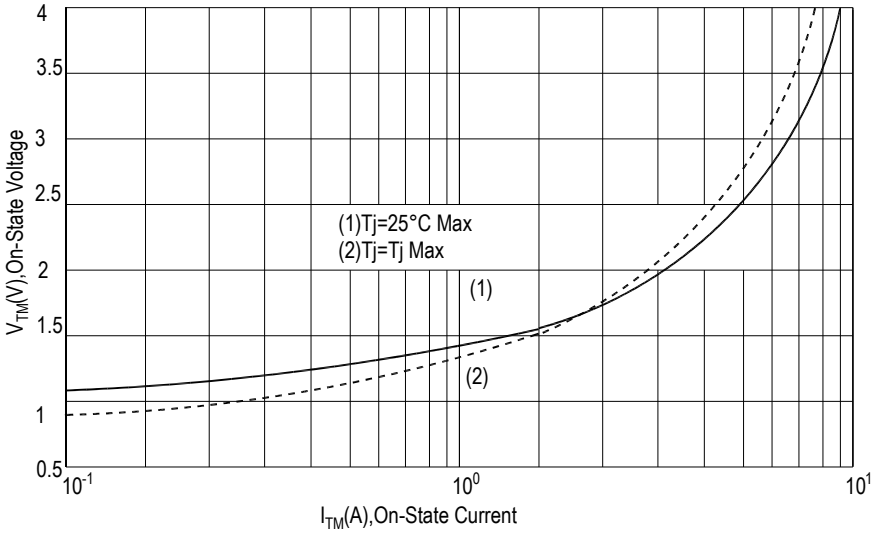


FIG.6:Holding current and Latching current VS Junction temperature

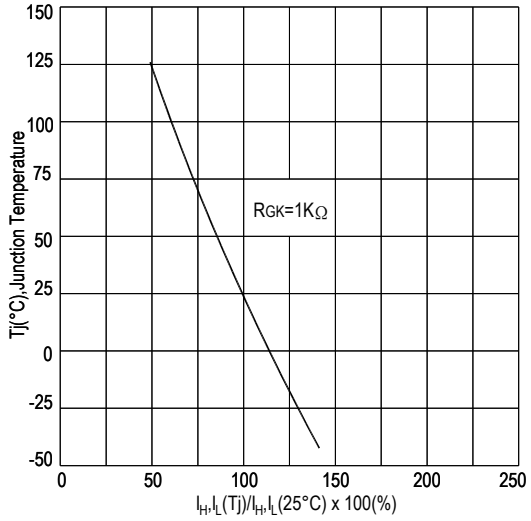
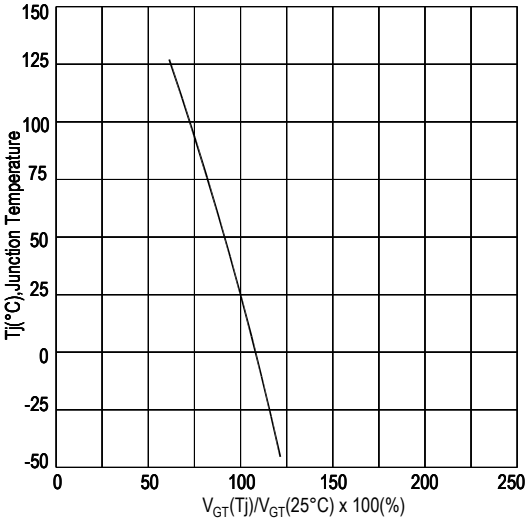
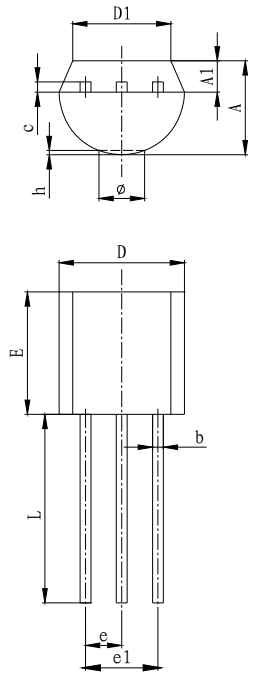


FIG.7: Gate trigger voltage VS Junction temperature



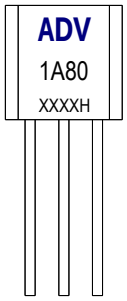
PACKAGE MECHANICAL DATA

TO-92 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	3.180	4.190	0.125	0.165
A1	1.100	1.400	0.043	0.055
b	0.380	0.550	0.015	0.022
c	0.360	0.510	0.014	0.020
D	4.400	5.200	0.173	0.205
D1	3.430		0.135	
E	4.300	5.330	0.169	0.210
e	1.270 TYP		0.050 TYP	
e1	2.420	2.660	0.095	0.105
L	12.70	-	0.500	-
Φ		1.600		0.063
h	0.000	0.380	0.000	0.015

Making Diagram



ADV:Logo
1A80:Part number(ADS1A80)
X:Internal control code
H:Halogen Free

AD S 1 A 60 #

- ADVANCED
- Internal control code
- Current:1=1A
- SCR series
- Voltage:60=600V 80=800V
- Package explain:Blank=TO-92

Ordering information

Part number	Package	Marking	Packing	Quantity
ADS1A60	TO-92	1A60	Vinyl sack	1000pcs
ADS1A80	TO-92	1A80	Vinyl sack	1000pcs

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