

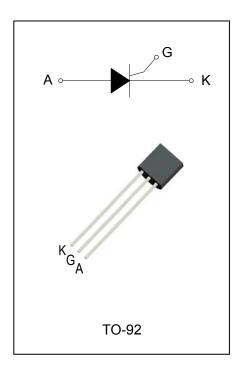
SCRs

General Description

Sensitive triggering SCR is suitable for the application where gate current limited such as small motor control, Earth leakage circuit breakers or Ground Fault Circuit Interrupters (GFCI), Solid state relays, General purpose switching, Small engine ignition.

Features

- ◆ Repetitive Peak Off-State Voltage: 900V
- ◆ R.M.S On-State Current (IT(RMS)= 1.25 A)
- ♦ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit		
V_{DRM}	Repetitive Peak Off-State Voltage	Ti=25°C	ADC1400	900	V		
V_{RRM}	Repetitive peak reverse voltage	Tj=25°C	ADS1A90	900	V		
I _{T(AV)}	Average On-State Current	Half Sine Wave , Tc = 50°C		0.9	Α		
I _{T(RMS)}	R.M.S On-State Current	Half Sine Wave , Tc = 50°C		1.25	А		
I _{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)		20	А		
dI/dt	Critical rate of rise of on-state current	Tj =110°C, tr≤ 100ns		50	A/µs		
l ² t	I ² t for Fusing	Tj =25°C,tp =10ms		2	A ² S		
P_{GM}	Forward Peak Gate Power Dissipation	Tj =110°C, Pulse Width $\leq 1.0 \mu s$		0.5	W		
P _{G(AV)}	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		0.1	W		
I _{GM}	Peak Gate Current	Tj =110°C, Pulse Width ≤ 1.0μs		0.2	А		
Tj	Operating Junction Temperature			- 40 ~ 110	°C		
T _{STG}	Storage Temperature			- 40 ~ 150		- 40 ~ 150	°C





Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS1A90	Unit
I _{DRM}	Peak Forward Reverse	$V_{DRM} = V_{RRM}, R_{GK} = 1K\Omega$ $Tj = 25^{\circ}C$		5	uA
I _{RRM}	Blocking Current	$V_{DRM} = V_{RRM}, R_{GK} = 1K\Omega$ $Tj = 110^{\circ}C$	Max.	0.1	mA
V_{TM}	Peak On-State Voltage	I_{TM} = 4A, t_p = 380 μ s	Max.	1.5	V
$V_{\sf GD}$	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $R_{GK} = 1 \text{K}\Omega$ $Tj = 110 ^{\circ}\text{C}$	Min.	0.2	V
V_{GT}	Gate Trigger Voltage	V 0V D 4000	Max.	0.8	٧
I _{GT}	Gate Trigger Current	$V_D = 6V$, $R_L = 100\Omega$	Max.	200	uA
l _Η	Holding Current	$I_T = 0.05A$ $R_{GK} = 1K\Omega$	Max.	4	mA
ΙL	Latching Current	$I_G = 1 \text{mA}$ $R_{GK} = 1 \text{K}\Omega$	Max.	5	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3V_{DRM}$ gate open $R_{GK} = 1K\Omega$ Tj = 110°C	Min.	50	V/µs
R _{th(j-c)}	Junction to case		Max.	70	°C/W
$R_{th(j-a)}$	Junction to ambient		Max.	150	°C/W



FIG.1: Maximum average power dissipation (Single phase half wave)

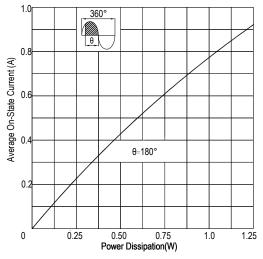


FIG.3: Gate trigger current VS Junction temperature

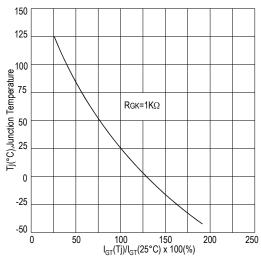


FIG.5: On-state characteristics(Max)

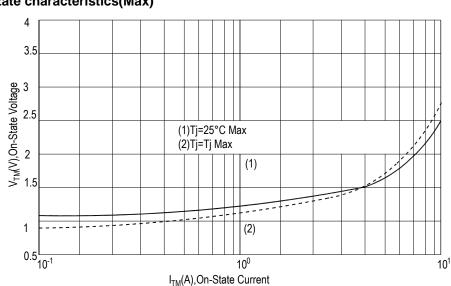


FIG.2: Average on-state current VS Allowable case Temperature(Single phase half wave)

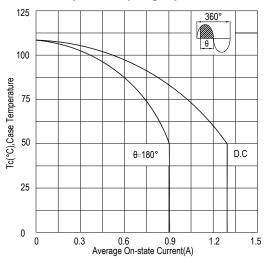
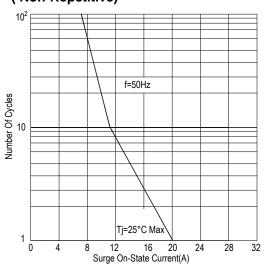
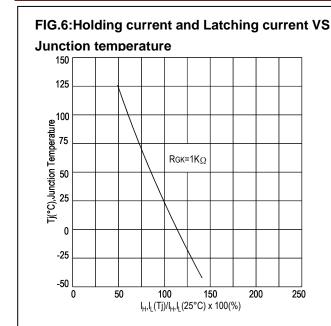
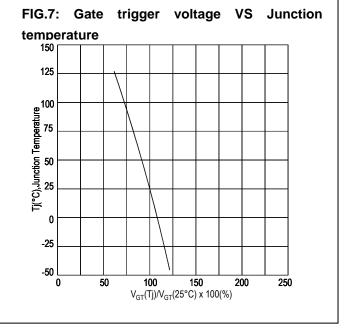


FIG.4: Rated surge on-state current (Non-Repetitive)





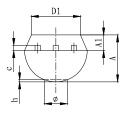


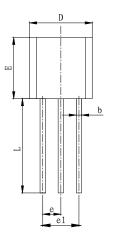




PACKAGE MECHANICAL DATA

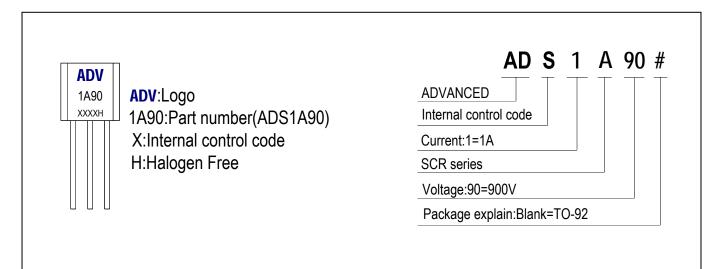
TO-92 Package Dimension





	Dimensions In		Dimensions In		
Symbol	Millimeters		Inches		
	Min	Max	Min	Max	
Α	3.180	4.190	0.125	0.165	
A1	1.100	1.400	0.043	0.055	
b	0.380	0.550	0.015	0.022	
С	0.360	0.510	0.014	0.020	
D	4.400	5.200	0.173	0.205	
D1	3.430		0.135		
Е	4.300	5.330	0.169	0.210	
е	1.270 TYP		0.050 TYP		
e1	2.420	2.660	0.095	0.105	
L	12.70	-	0.500	-	
Ф		1.600		0.063	
h	0.000	0.380	0.000	0.015	

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity
ADS1A90	TO-92	1A90	Vinyl sack	1000pcs



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