

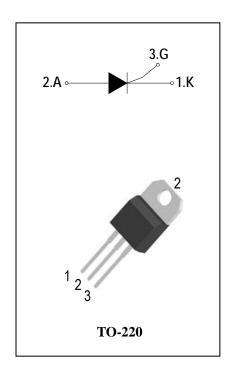
SCRs

General Description

The 25A SCR series of silicon controlled rectifiers, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

Features

- ◆ Repetitive Peak Off-State Voltage: 1000V and 1200V
- ◆ R.M.S On-State Current (IT(RMS)= 25 A)
- ♦ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit		
V_{DRM}	Repetitive Peak Off-State Voltage	Ti=25°C	ADS25A100	1000	V		
V_{RRM}	Repetitive peak reverse voltage	Tj=25°C	ADS25A120	1200	V		
I _{T(AV)}	Average On-State Current	Half Sine Wave , To	16	Α			
I _{T(RMS)}	R.M.S On-State Current	Half Sine Wave , Tc = 100°C		25	Α		
I _{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		300	А		
I ² t	I ² t for Fusing	Tj =25°C,tp =10ms		450	A^2S		
Р _{GМ}	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width ≤ 20μs		5	W		
$P_{G(AV)}$	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W		
I _{GM}	Peak Gate Current	Tj =125°C, Pulse Width ≤ 20μs		4	Α		
Tj	Operating Junction Temperature			- 40 ~ 125	°C		
T _{STG}	Storage Temperature					- 40 ~ 150	°C





ADS25A100/120

Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS25A100/120		Unit
				S	Blank	<u> </u>
		$V_{DRM} = V_{RRM}$		10		uA
I _{DRM}	Peak Forward Reverse	Tj = 25°C	Max.	10		
I _{RRM}	Blocking Current	$V_{DRM} = V_{RRM}$ $Tj = 125^{\circ}C$	IVIAX.	4		mA
V _{TM}	Peak On-State Voltage	I_{TM} = 50A, t_p = 380 μ s	Max.	1.6		V
V_{GD}	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 125^{\circ}\text{C}$	Min.	0.2		V
V_{GT}	Gate Trigger Voltage	V 40V B 000	Max.	1.3		V
I _{GT}	Gate Trigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	15	30	mA
I _H	Holding Current	I _T = 0.5A	Max.	30	40	mA
ال	Latching Current	I _G = 1.2 I _{GT}	Max.	50	50	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_D = 2/3V_{DRM}$ gate open $Tj = 125^{\circ}C$	Min.	1000	1500	V/µs
R _{th(j-c)}	Junction to case (AC)		Max.	1.0		°C/W
R _{th(j-a)}	Junction to ambient		Max.	60		°C/W

FIG.2: Average on-state current VS Allowable

ADV

FIG.1: Maximum average power dissipation (Single phase half wave)

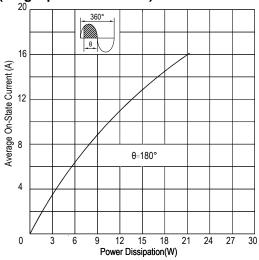


FIG.3: Gate trigger current VS Junction temperature

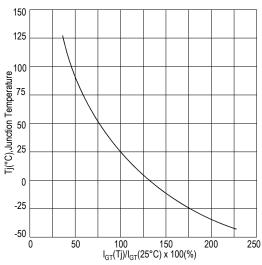


FIG.5: On-state characteristics(Max)

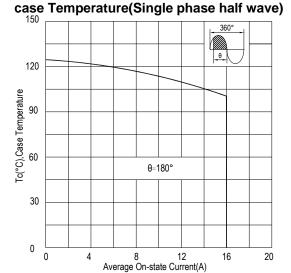
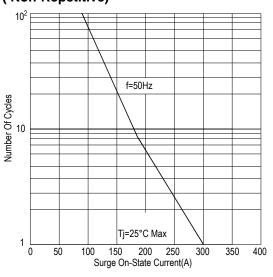
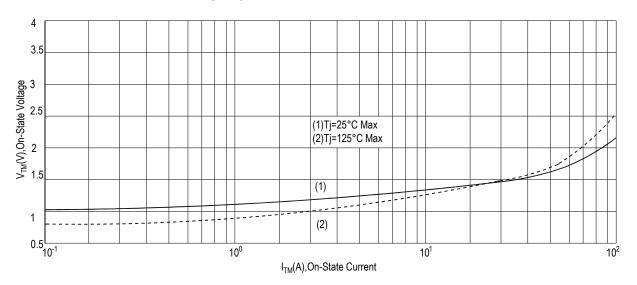


FIG.4: Rated surge on-state current (Non-Repetitive)







-50

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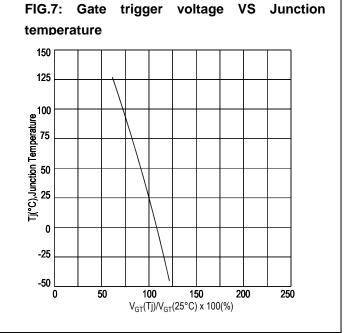
FIG.6:Holding current and Latching current VS

Junction temperature

100

I_H,I_L(Tj)/I_H,I_L(25°C) x 100(%)

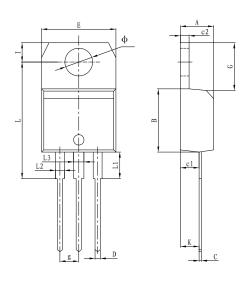
250





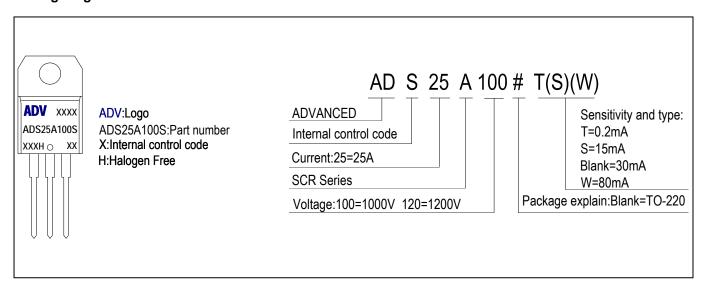
PACKAGE MECHANICAL DATA

TO-220 Package Dimension



	Dimensions		Dimensions		
Symbol	In Millimeters		In Inches		
	Min	Max	Min	Max	
Α	4.40	4.60	0.173	0.181	
В	9.00	9.30	0.354	0.366	
С	0.40	0.60	0.015	0.023	
c1	2.00	2.60	0.078	0.102	
c2	1.23	1.32	0.048	0.051	
D	0.70	1.00	0.027	0.039	
E	10.00	10.40	0.393	0.409	
g	2.40	2.70	0.094	0.106	
G	6.20	6.80	0.244	0.267	
I	2.65	2.95	0.104	0.116	
L	15.80	16.80	0.622	0.661	
L1	3.75		0.147		
L2	1.14	1.70	0.044	0.066	
L3	1.14	1.70	0.044	0.066	
Ф	3.60	3.90	0.141	0.153	
К	2.60TYP		0.102TYP		

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADS25A100#	TO-220	ADS25A100#	Tube	50pcs		
ADS25A120#	TO-220	ADS25A120#	Tube	50pcs		
Note:# = Gate Trigger Current Sensitivity and type						



ADS25A100/120

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