ADS25A60/80

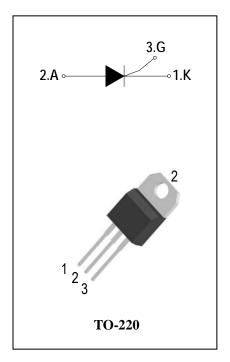
ADV SCRs

General Description

The 25A SCR series of silicon controlled rectifiers, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current (IT(RMS)= 25 A)
- ♦ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit		
V _{DRM}	Repetitive Peak Off-State Voltage	Ti-25°C	ADS25A60	600	V		
V _{RRM}	Repetitive peak reverse voltage	Tj=25°C	ADS25A80	800	V		
I _{T(AV)}	Average On-State Current	Half Sine Wave , Tc = 100°C		16	А		
I _{T(RMS)}	R.M.S On-State Current	Half Sine Wave , Tc = 100°C		25	А		
I _{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		300	А		
l ² t	I ² t for Fusing	Tj =25°C,tp =10ms		450	A ² S		
P _{GM}	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width $\leq 20\mu s$		5	W		
P _{G(AV)}	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W		
I _{GM}	Peak Gate Current	Tj =125°C, Pulse Width \leqslant 20 $\mu_{ m S}$		4	А		
Tj	Operating Junction Temperature			- 40 ~ 125	°C		
T _{STG}	Storage Temperature			- 4		- 40 ~ 150	°C



Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS25A60/80		Unit	
				S	Blank		
		$V_{DRM} = V_{RRM}$		1	0	uA	
I _{DRM}	Peak Forward Reverse	Tj = 25°C	Max	10		uA	
I _{RRM}	Blocking Current	V _{DRM} = V _{RRM} Tj = 125°C	Max.	4		mA	
V _{TM}	Peak On-State Voltage	I _{TM} = 50A, t _p = 380 μs	Max.	1.6		V	
V _{GD}	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 k\Omega$ Tj = 125°C	Min.	0.2		V	
V _{GT}	Gate Trigger Voltage	N KOL D AND		1.3		V	
I _{GT}	Gate Trigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	15	30	mA	
I _H	Holding Current	I _T = 0.5A	Max.	30	40	mA	
١L	Latching Current	I _G = 1.2 I _{GT}	Max.	50	50	mA	
dV/dt	Critical Rate of Rise of Off-State Voltage	V _D = 2/3V _{DRM} gate open Tj = 125°C	Min.	1000	1500	V/µs	
R _{th(j-c)}	Junction to case (AC)		Max.	1.0		°C/W	
R _{th(j-a)}	Junction to ambient		Max.	60		°C/W	

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ADS25A60/80

FIG.1: Maximum average power dissipation (Single phase half wave) 360° θ 16 Average On-State Current (A) 12 8 θ=180° 4 0 3 6 9 12 15 18 21 24 27 30 Power Dissipation(W)

FIG.3: Gate trigger current VS Junction temperature

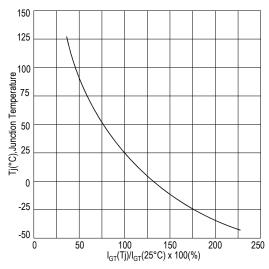
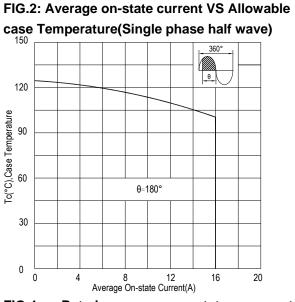
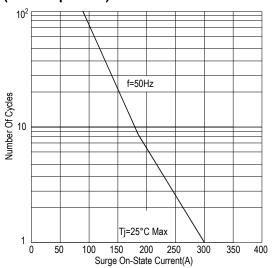
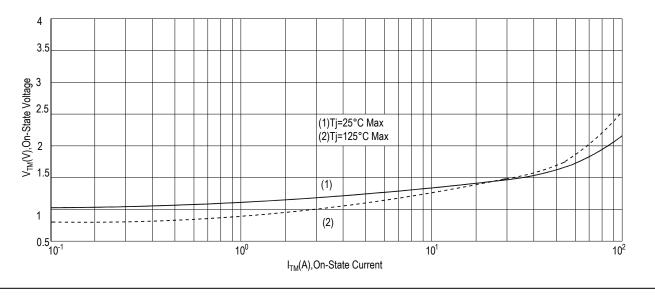


FIG.5: On-state characteristics(Max)

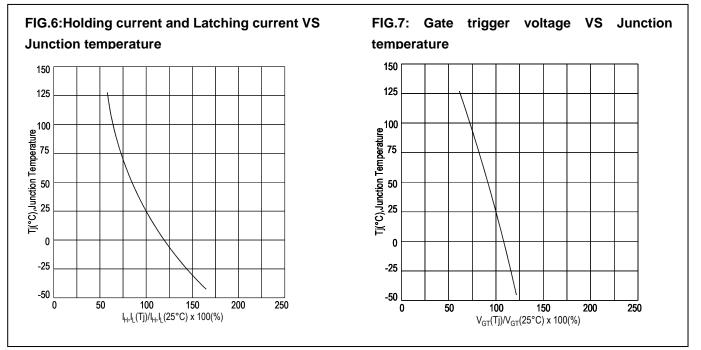








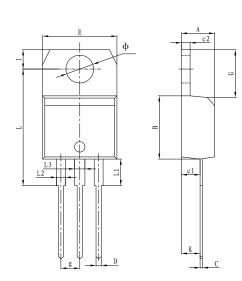
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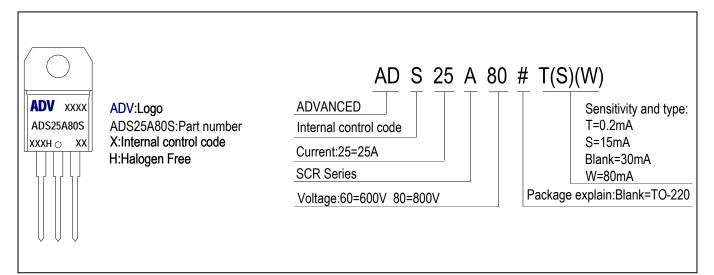
ADS25A60/80

PACKAGE MECHANICAL DATA TO-220 Package Dimension



	Dimensions			Dimensions		
Symbol	In Millimeters		In Inches			
	Min	Max	Min	Max		
А	4.40	4.60	0.173	0.181		
В	9.00	9.30	0.354	0.366		
С	0.40	0.60	0.015	0.023		
c1	2.00	2.60	0.078	0.102		
c2	1.23	1.32	0.048	0.051		
D	0.70	1.00	0.027	0.039		
E	10.00	10.40	0.393	0.409		
g	2.40	2.70	0.094	0.106		
G	6.20	6.80	0.244	0.267		
I	2.65	2.95	0.104	0.116		
L	15.80	16.80	0.622	0.661		
L1	3.75		0.147			
L2	1.14	1.70	0.044	0.066		
L3	1.14	1.70	0.044	0.066		
Φ	3.60	3.90	0.141	0.153		
К	2.60TYP		0.102TYP			

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADS25A60#	A60# TO-220 ADS25A60# Tube		Tube	50pcs		
ADS25A80#	TO-220	ADS25A80#	Tube	50pcs		
Note:# = Gate Trigger Current Sensitivity and type						

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ADS25A60/80

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