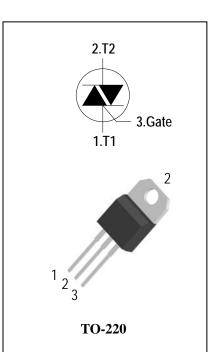
3 Quadrants High temperature Triacs

General Description

High current density due to mesa technology, guaranteed maximum junction temperature 150° C. The ADS6CH triac series is suitable for general purpose AC switching. They can beused as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids, motor speed controllers. The heatsink can be reduced,compared to traditional triacs, according to the high performance at given junction temperatures.

Features

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (IT(RMS)= 6A)
- ◆ High Commutation dv/dt
- High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



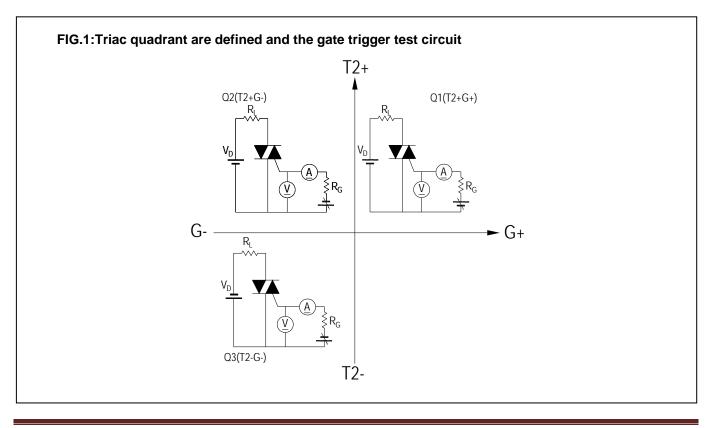
Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V _{DRM}	Popotitivo Poak Off State Voltage	Tj = 25°C	ADS6CH60	600	V
V_{RRM}	Repetitive Peak Off-State Voltage	1j - 25 C	ADS6CH80	800	V
I _{T(RMS)}	R.M.S On-State Current	T _C = 138 °C	6	А	
I _{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7	60/63	А	
l ² t	I ² t for fusing	tp=10ms	20	A ² s	
	Critical rate of rise of on-state F = 120 Hz Tj = 150°C			50	A /
dl/dt	current	$I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$	50	A/µs	
I _{GM}	Peak Gate Current	tp = 20 μs Tj = 150°C		4	А
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=150°C)			1	W
P_{GM}	Peak Gate Power Dissipation(tp=20us,Tj=150°C)			5	W
Tj	Operating Junction Temperature			- 40 ~ 150	°C
T _{STG}	Storage Temperature			- 40 ~ 150	°C



Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS6CH60/80		Unit		
					s	Blank	В	
I _{DRM}	Peak Forward Reverse Blocking		V _{DRM} = V _{RRM,} Tj = 25°C	Max	5		uA	
I _{RRM}	Current		V _{DRM} = V _{RRM,} Tj = 150°C	Max.	2.7		mA	
V _{TM}	Peak On-State Voltage		I _{TM} = 8.5Α, t _P = 380 μs	Max.	1.5			V
V_{GD}	Q1-Q2-Q3	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ Tj = 150°C	Min.	0.2			V
V _{GT}	Q1-Q2-Q3	Gate Trigger Voltage	$V_D = 12V$, $R_L = 33\Omega$ Max. Max.		1.5			V
I _{GT}	Q1-Q2-Q3	Gate Trigger Current			10	35	50	mA
I _H	Q1-Q2-Q3	Holding Current	I _T = 0.1A	Max.	20	45	60	mA
	Q1-Q3	Latching Current	I _G = 1.2 I _{GT}	Max.	20	50	70	mA
ΙL	Q2				35	70	100	
dV/dt	Critical Rate of Rise of Off-State Voltage		$V_D = 2/3V_{DRM}$ gate open Tj = 150°C	Min.	200	1000	1500	V/µs
(dV/dt)c	Critical Rate of Change of Commutating Voltage		V _D =400V Tj = 150°C (dl/dt)c=-2.6A/ms	Min.	1	15	20	V/µs
R _{th(j-c)}	Junction to case (AC)			Max.	1.8			°C/W
R _{th(j-a})	Junction to ambient			Max.	60			°C/W





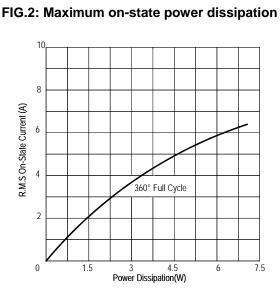


FIG.4: Maximum transient thermal impedance

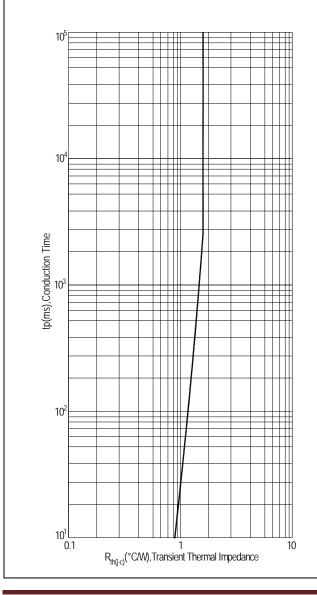
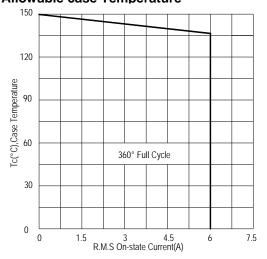
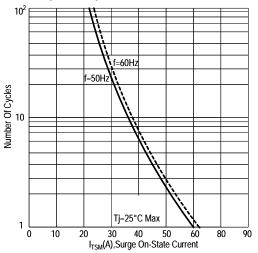
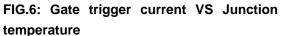


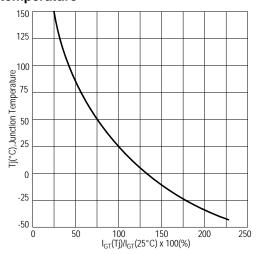
FIG.3: Typical RMS on-state current VS Allowable case Temperature

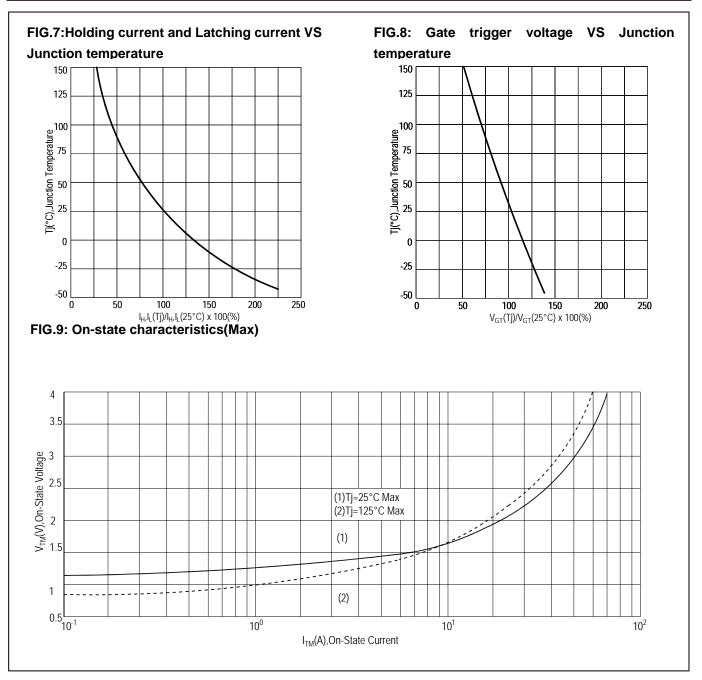






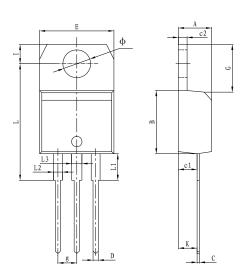






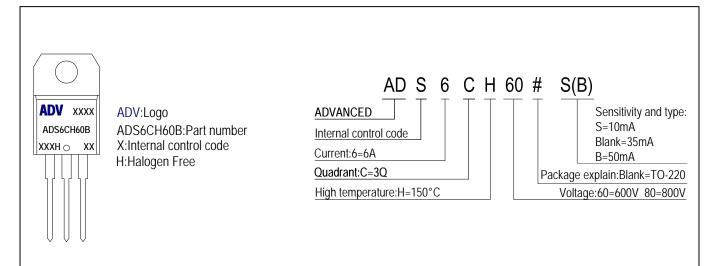
<u>ADV</u>

PACKAGE MECHANICAL DATA TO-220 Package Dimension



	Dimer	nsions	Dimensions			
Symbol	In Milli	meters	In Inches			
	Min	Max	Min	Max		
А	4.40	4.60	0.173	0.181		
В	9.00	9.30	0.354	0.366		
С	0.40	0.60	0.015	0.023		
c1	2.00	2.60	0.078	0.102		
c2	1.23	1.32	0.048	0.051		
D	0.70	1.00	0.027	0.039		
E	10.00	10.40	0.393	0.409		
g	2.40	2.70	0.094	0.106		
G	6.20	6.80	0.244	0.267		
I	2.65	2.95	0.104	0.116		
L	15.80	16.80	0.622	0.661		
L1	3.75		0.147			
L2	1.14	1.70	0.044	0.066		
L3	1.14	1.70	0.044	0.066		
Φ	3.60	3.90	0.141	0.153		
К	2.60TYP		0.102TYP			

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADS6CH60#	TO-220	ADS6CH60#	Tube	50pcs		
ADS6CH80#	TO-220	ADS6CH80#	Tube	50pcs		
Note:# = Gate Triager Current Sensitivity and type						

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