

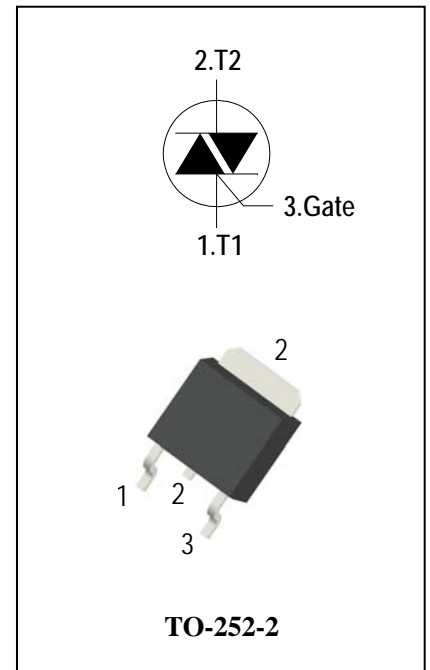
## 3 Quadrants High temperature Triacs

### General Description

High current density due to mesa technology , guaranteed maximum junction temperature 150° C. The ADS6CH triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners, Rectifier-fed DC inductive loads e.g. DC motors and solenoids , motor speed controllers. The heatsink can be reduced, compared to traditional triacs, according to the high performance at given junction temperatures.

### Features

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (  $I_{T(RMS)} = 6A$  )
- ◆ High Commutation  $dv/dt$
- ◆ High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



### Absolute Maximum Ratings

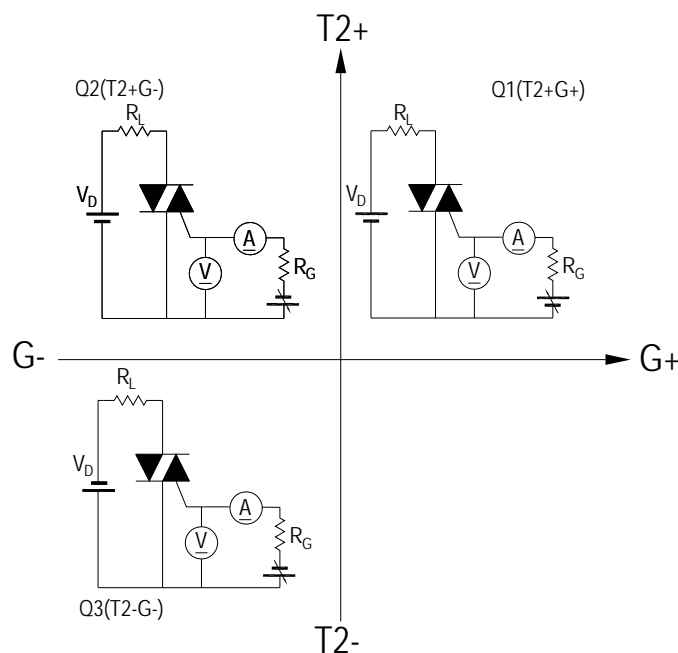
Symbol	Items	Conditions	Ratings	Unit
$V_{DRM}$ $V_{RRM}$	Repetitive Peak Off-State Voltage	$T_j = 25^\circ C$	ADS6CH60E 600 ADS6CH80E 800	V V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 110^\circ C$	6	A
$I_{TSM}$	Surge On-State Current	$t_p = 20ms(50Hz) / t_p = 16.7ms(60Hz)$	60/63	A
$I^2t$	$I^2t$ for fusing	$t_p = 10ms$	20	$A^2s$
$di/dt$	Critical rate of rise of on-state current	$F = 120 Hz$ $T_j = 150^\circ C$ $I_G = 2 \times I_{GT}$ , $t_r \leq 100 ns$	50	$A/\mu s$
$I_{GM}$	Peak Gate Current	$t_p = 20 \mu s$ $T_j = 150^\circ C$	4	A
$P_{G(AV)}$	Average Gate Power Dissipation( $T_j = 150^\circ C$ )		1	W
$P_{GM}$	Peak Gate Power Dissipation( $t_p = 20\mu s, T_j = 150^\circ C$ )		5	W
$T_j$	Operating Junction Temperature		- 40 ~ 150	$^\circ C$
$T_{STG}$	Storage Temperature		- 40 ~ 150	$^\circ C$



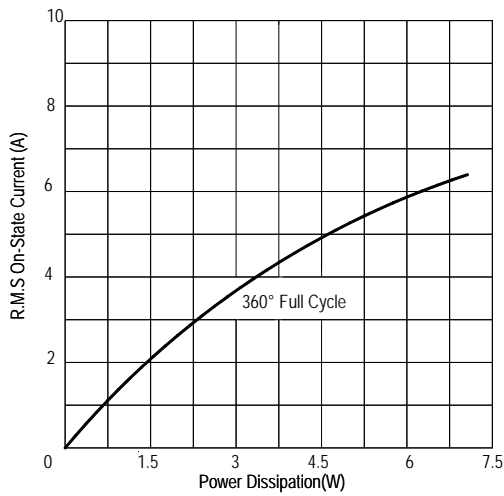
## Electrical Characteristics (T<sub>j</sub> = 25°C unless otherwise specified )

Symbol	Items		Conditions		ADS6CH60E/80E			Unit
					S	Blank	B	
I <sub>DRM</sub> I <sub>RRM</sub>	Peak Forward Reverse Blocking Current		V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 25°C	Max.	5			uA
			V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 150°C		2.7			mA
V <sub>TM</sub>	Peak On-State Voltage		I <sub>TM</sub> = 8.5A, t <sub>p</sub> = 380 μs	Max.	1.5			V
V <sub>GD</sub>	Q1-Q2-Q3	Non-Trigger Gate Voltage	V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ T <sub>j</sub> = 150°C	Min.	0.2			V
V <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Voltage	V <sub>D</sub> = 12V , R <sub>L</sub> = 33Ω	Max.	1.5			V
I <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Current		Max.	10	35	50	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.1A	Max.	20	45	60	mA
I <sub>L</sub>	Q1-Q3	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	20	50	70	mA
	Q2				35	70	100	
dV/dt	Critical Rate of Rise of Off-State Voltage		V <sub>D</sub> = 2/3V <sub>DRM</sub> gate open T <sub>j</sub> = 150°C	Min.	200	1000	1500	V/μs
(dV/dt) <sub>c</sub>	Critical Rate of Change of Commutating Voltage		V <sub>D</sub> =400V T <sub>j</sub> = 150°C (dI/dt) <sub>c</sub> =-2.6A/ms	Min.	1	15	20	V/μs
R <sub>th(j-c)</sub>	Junction to case (AC)			Max.	2.5			°C/W
R <sub>th(j-a)</sub>	Junction to ambient(Copper surface under tab:S=0.5cm <sup>2</sup> )			Max.	70			°C/W

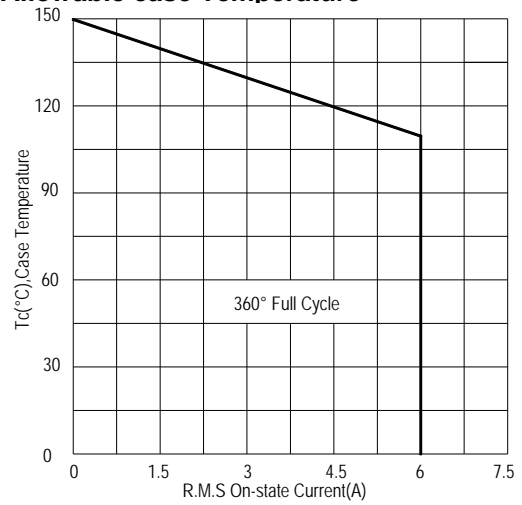
**FIG.1:Triac quadrant are defined and the gate trigger test circuit**



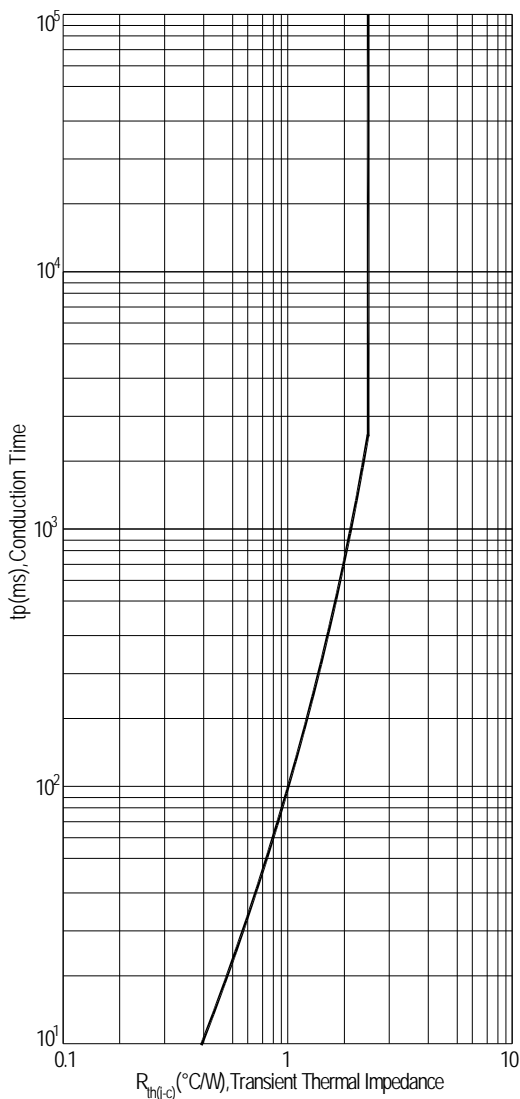
**FIG.2: Maximum on-state power dissipation**



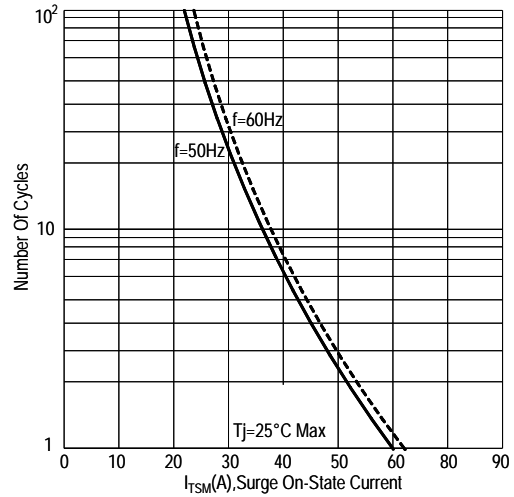
**FIG.3: Typical RMS on-state current VS Allowable case Temperature**



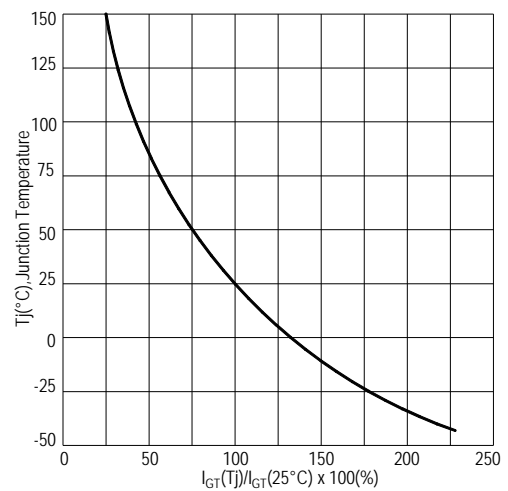
**FIG.4: Maximum transient thermal impedance**



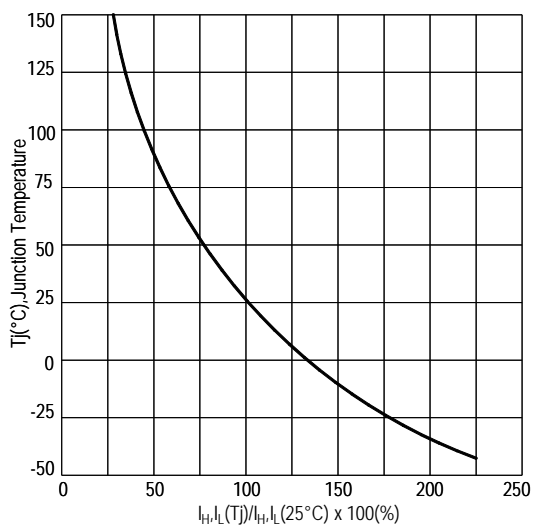
**FIG.5: Rated surge on-state current (Non-Repetitive)**



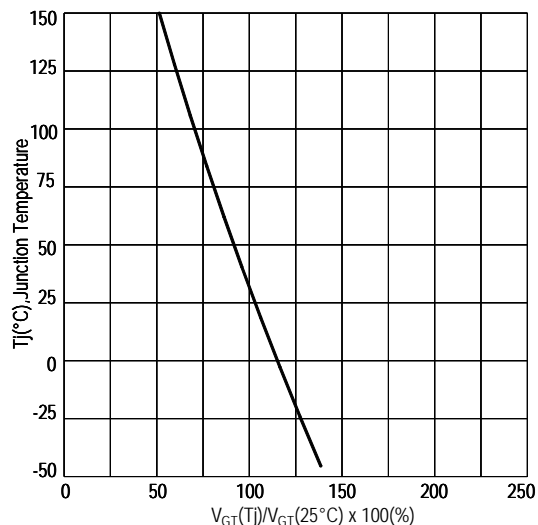
**FIG.6: Gate trigger current VS Junction temperature**



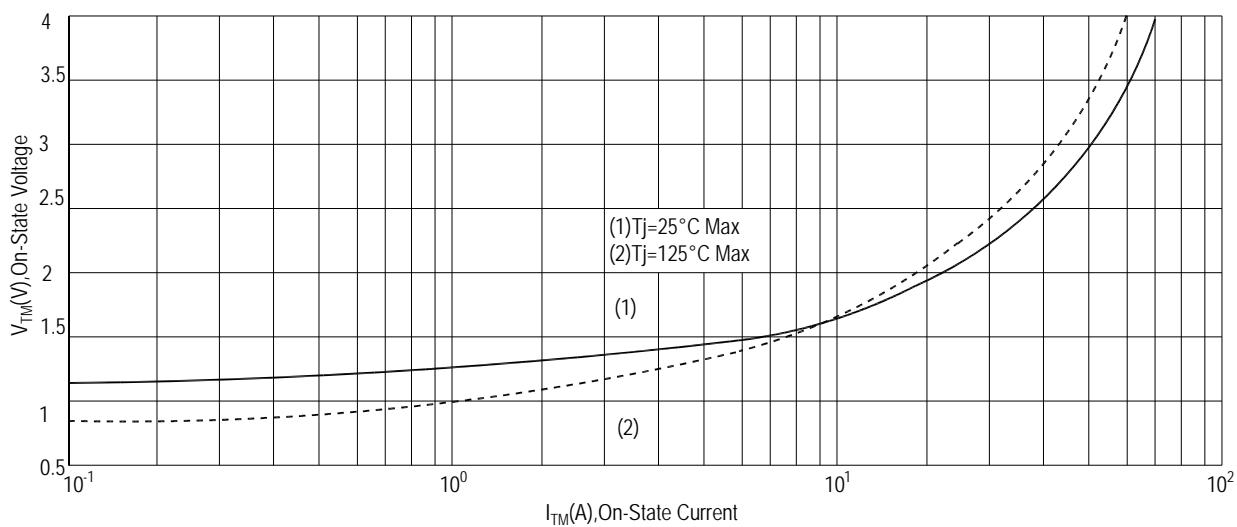
**FIG.7: Holding current and Latching current VS Junction temperature**



**FIG.8: Gate trigger voltage VS Junction temperature**

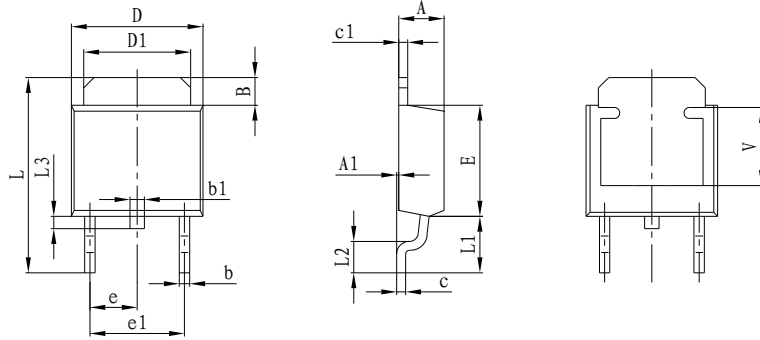


**FIG.9: On-state characteristics(Max)**



## PACKAGE MECHANICAL DATA

### TO-252-2 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.450	0.620	0.017	0.024
c1	0.450	0.620	0.017	0.024
D	6.350	6.650	0.250	0.262
D1	5.100	5.400	0.200	0.213
E	5.900	6.200	0.232	0.244
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	10.60	0.374	0.396
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	4.100 REF.		0.161 REF.	

### Making Diagram

**ADV**: Logo  
**ADS6CH60EB**: Part number  
**X**: Internal control code  
**H**: Halogen Free

A D S 6 C H 6 0 E S ( B )

ADVANCED	Internal control code	Current: 6=6A	Quadrant: C=3Q
High temperature: H=150°C	Sensitivity and type: S=10mA Blank=35mA B=50mA		
Package explain: E=TO-252-2		Voltage: 60=600V 80=800V	

### Ordering information

Part number	Package	Marking	Packing	Quantity
ADS6CH60E#	TO-252-2	ADS6CH60E#	Tube	80pcs
			Embossed tape	2500pcs
ADS6CH80E#	TO-252-2	ADS6CH80E#	Tube	80pcs
			Embossed tape	2500pcs

Note: # = Gate Trigger Current Sensitivity and type

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