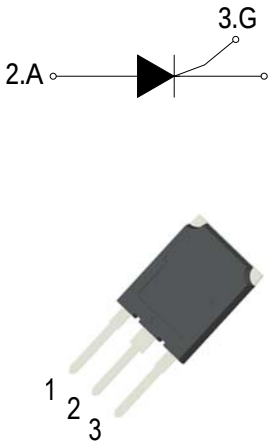
SCRs

General Description

The 70A SCR series of silicon controlled rectifiers,with high ability to withstand the shock loading of large current,provide high dv/dt rate with strong resistance to electromagnetic interference.They are especially recommended for use on solid state relay,motorcycle, power charger,T-tools etc.



TO-247S

Features

- Repetitive Peak Off-State Voltage : 1600V
- R.M.S On-State Current ($I_{T(RMS)}$=70 A)
- These are Pb-Free Devices

Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	Tj=25°C	ADS70A160S	1600	V
V_{RRM}	Repetitive peak reverse voltage				V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave , Tc = 80°C		45	A
$I_{T(RMS)}$	R.M.S On-State Current	Half Sine Wave , Tc = 80°C		70	A
I_{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		700	A
I^2t	I^2t for Fusing	Tj =25°C,tp =10ms		2450	A^2S
P_{GM}	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width ≤ 20μs		10	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W
I_{GM}	Peak Gate Current	Tj =125°C, Pulse Width ≤ 20μs		5	A
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C

ADV
Scan the QR code to view the latest product information

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADS70A160S			Unit
				S	Blank	W	
I_{DRM} I_{RRM}	Peak Forward Reverse Blocking Current	$V_{DRM} = V_{RRM}$ Tj = 25°C	Max.	50			uA
		$V_{DRM} = V_{RRM}$ Tj = 125°C		10			mA
V_{TM}	Peak On-State Voltage	I_{TM} = 100A, t_p = 380 μs	Max.	1.8			V
V_{GD}	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ R_L = 3.3 kΩ Tj = 125°C	Min.	0.25			V
V_{GT}	Gate Trigger Voltage	V_D = 12V ， R_L = 33Ω	Max.	1.5			V
I_{GT}	Gate Trigger Current		Max.	15	30	80	mA
I_H	Holding Current	I_T = 1A	Max.	30	40	150	mA
I_L	Latching Current	I_G = 1.2 I_{GT}	Max.	50	60	200	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	V_D = 2/3V_{DRM} gate open Tj = 125°C	Min.	1000	1200	1500	V/μs
$R_{th(j-c)}$	Junction to case (AC)		Max.	0.37			°C/W
$R_{th(j-a)}$	Junction to ambient		Max.	50			°C/W

FIG.1: Maximum average power dissipation (Single phase half wave)

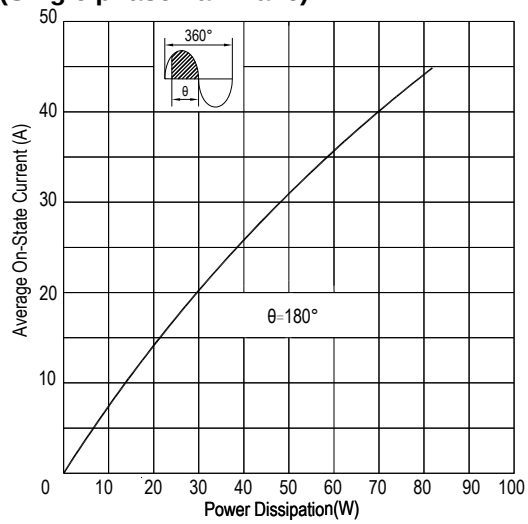


FIG.2: Average on-state current VS Allowable case Temperature(Single phase half wave)

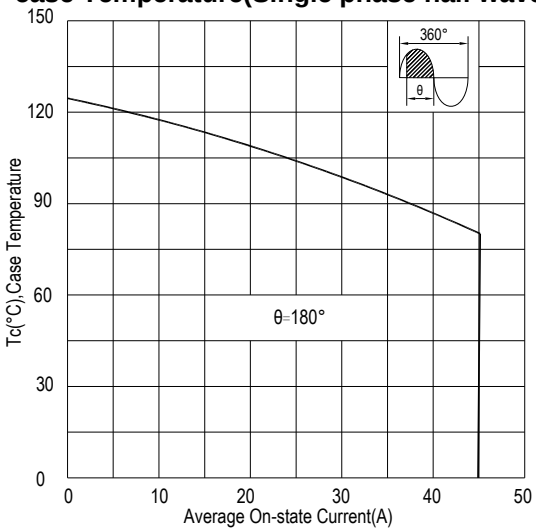


FIG.3: Gate trigger current VS Junction temperature

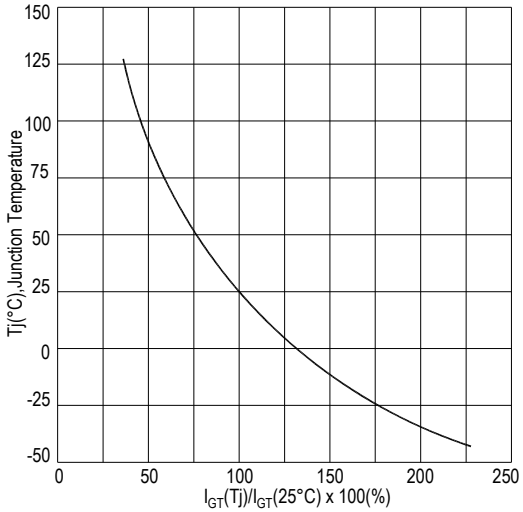


FIG.4: Rated surge on-state current (Non-Repetitive)

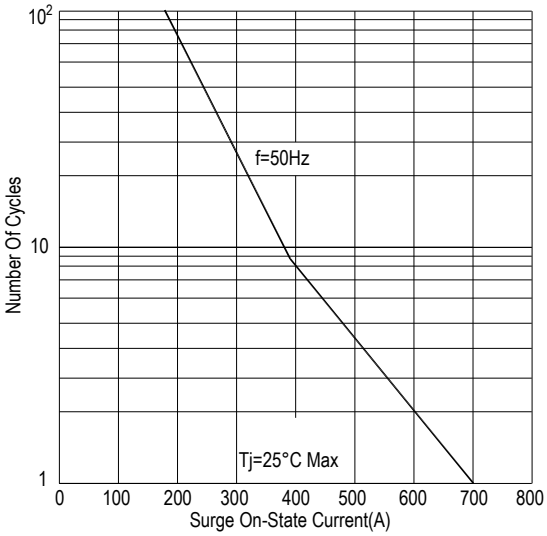


FIG.5: On-state characteristics(Max)

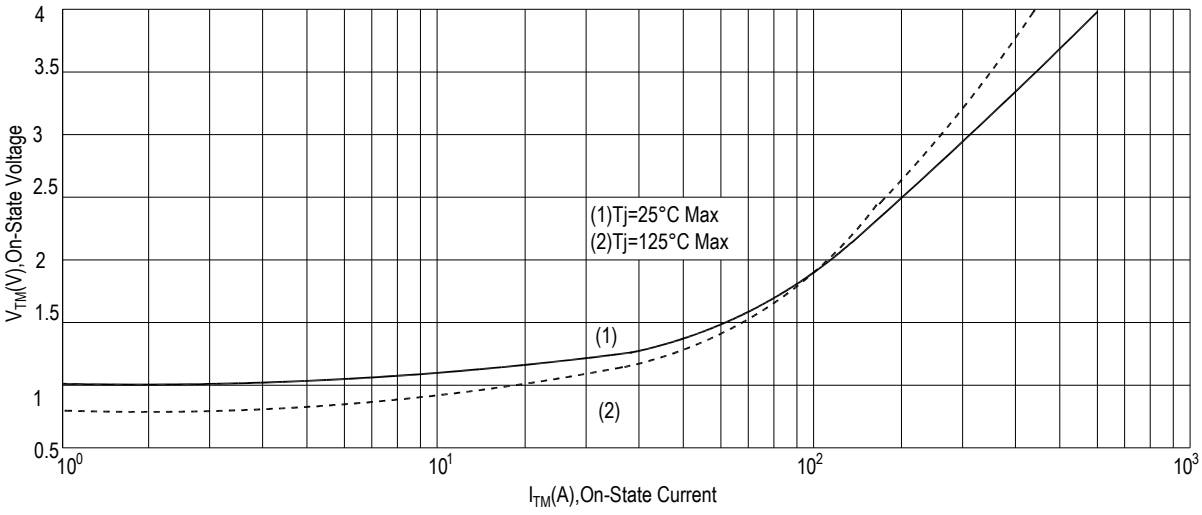


FIG.6:Holding current and Latching current VS Junction temperature

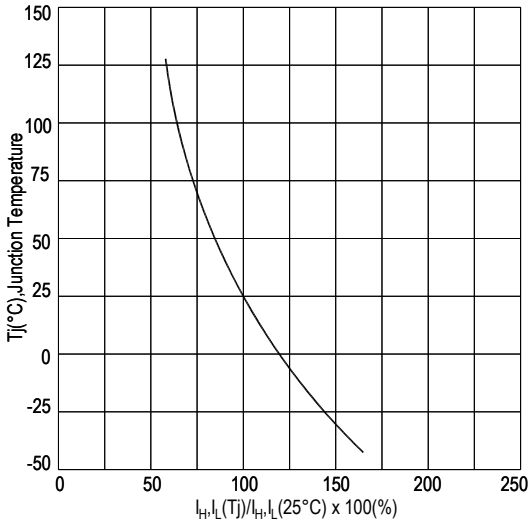
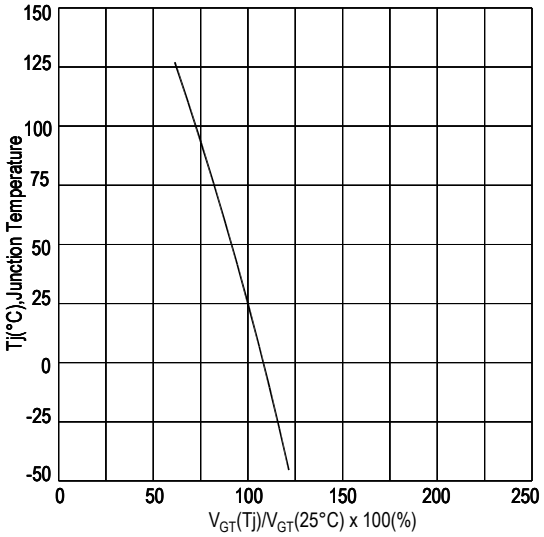
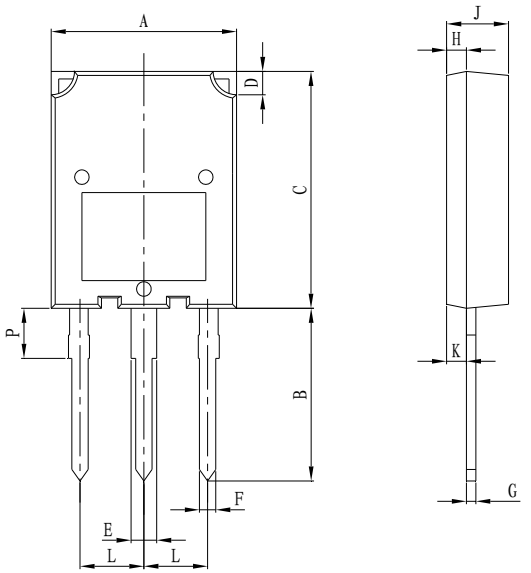


FIG.7: Gate trigger voltage VS Junction temperature



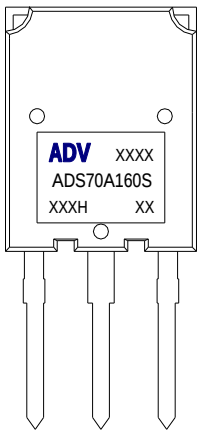
PACKAGE MECHANICAL DATA

TO-247S Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	**Max**	**Min**	**Max**
A	15.10	16.10	0.595	0.632
B	13.80	14.80	0.544	0.582
C	19.80	20.80	0.780	0.818
D	2.00	2.40	0.079	0.095
E	2.75	3.35	0.108	0.132
F	1.30	1.50	0.051	0.059
G	0.55	0.80	0.022	0.032
H	1.45	2.15	0.058	0.084
J	4.50	5.50	0.178	0.216
K	1.90	2.80	0.075	0.110
L	5.10	5.80	0.201	0.228
P	3.00	4.00	0.108	0.157

Making Diagram



ADV:Logo
ADS70A160S:Part number
X:Internal control code
H:Halogen Free

AD S 70 A 160 S T(S)(W)

- AD: ADVANCED
- S: Internal control code
- 70: Current:70=70A
- A: SCR Series
- 160: Voltage:160=1600V
- S: Package explain:S=TO-247S
- T(S)(W): Sensitivity and type: T=0.2mA, S=15mA, Blank=30mA, W=80mA

Ordering information

Part number	Package	Marking	Packing	Quantity
ADS70A160S#	TO-247S	ADS70A160S#	Tube	25pcs
Note:# = Gate Trigger Current Sensitivity and type				

Notice

1. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any ADV products listed in this document, please confirm the latest product information with a ADV sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by ADV such as that disclosed through our website. (http://www.advsemi.com)
2. ADV has used reasonable care in compiling the information included in this document, but ADV assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
3. You should use the products described herein within the range specified by ADV, especially with respectvto the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. ADV shall have no liability for malfunctions or damages arising out of the use of ADV products beyond such specified ranges.
4. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application.ADV makes no representations, warranties or guaranties regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or ADV products.
5. Although ADV endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a ADV product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
6. This document is provided for reference purposes only so that ADV customers may select the appropriate ADV products for their use. ADV neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of ADV or any third party with respect to the information in this document.
7. ADV shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts,programs, algorithms, and application circuit examples.
8. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from ADV.