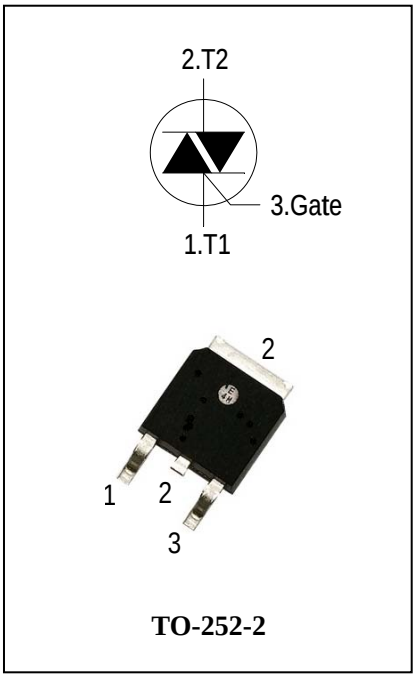
3 Quadrants Triacs

General Description

High current density due to mesa technology . the ADS8C triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers.



TO-252-2

Features

- Repetitive Peak Off-State Voltage: 600Vand800V
- R.M.S On-State Current ($I_{T(RMS)}$= 8A)
- High Commutation dv/dt
- These Devices are Pb-Free and are RoHS Compliant

Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	ADS8C60E	600	V
V_{RRM}			ADS8C80E	800	V
$I_{T(RMS)}$	R.M.S On-State Current	T_C = 110 °C		8	A
I_{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		80/84	A
I^2t	I^2t for fusing	tp=10ms		36	A^2s
dI/dt	Critical rate of rise of on-state current	F = 120 Hz Tj = 125°C I_G = 2 x I_{GT} , tr ≤ 100 ns		50	A/μs
I_{GM}	Peak Gate Current	tp = 20 μs Tj = 125°C		4	A
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			1	W
P_{GM}	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			10	W
T_j	Operating Junction Temperature			- 40 ~ 125	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C

ADV

Scan the QR code to view the latest product information

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Symbol	Items		Conditions		ADS8C60E/80E				Unit
					T	S	Blank	B	
I_{DRM}	Peak Forward Reverse Blocking Current		V_{DRM} = V_{RRM}, Tj = 25°C	Max.	5				uA
I_{RRM}			V_{DRM} = V_{RRM}, Tj = 125°C		1				mA
V_{TM}	Peak On-State Voltage		I_{TM} = 11A, t_p = 380 μs	Max.	1.55				V
V_{GD}	Q1-Q2-Q3	Non − Trigger Gate Voltage	V_D = V_{DRM} R_L = 3.3 kΩ Tj = 125°C	Min.	0.2				V
V_{GT}	Q1-Q2-Q3	GateTrigger Voltage	V_D = 12V ， R_L = 33Ω	Max.	1.5				V
I_{GT}	Q1-Q2-Q3	GateTrigger Current		Max.	5	10	35	50	mA
I_H	Q1-Q2-Q3	Holding Current	I_T = 0.1A	Max.	10	15	40	60	mA
I_L	Q1-Q3	Latching Current	I_G = 1.2 I_{GT}	Max.	15	20	50	70	mA
	Q2				25	35	60	80	
dV/dt	Critical Rate of Rise of Off-State Voltage		V_D = 2/3V_{DRM} gate open Tj = 125°C	Min.	50	200	500	1000	V/μs
(dV/dt)c	Rate of Change of Commutating Current,		(dI/dt)c=-3.5A/ms Tj = 125°C	Min.	0.5	1	10	25	V/μs
$R_{th(j-c)}$	Junction to case (AC)			Max.	1.6				°C/W
$R_{th(j-a)}$	Junction to ambient(Copper surface under tab:S=0.5cm^2)			Max.	70				°C/W

FIG.1:Triac quadrant are defined and the gate trigger test circuit

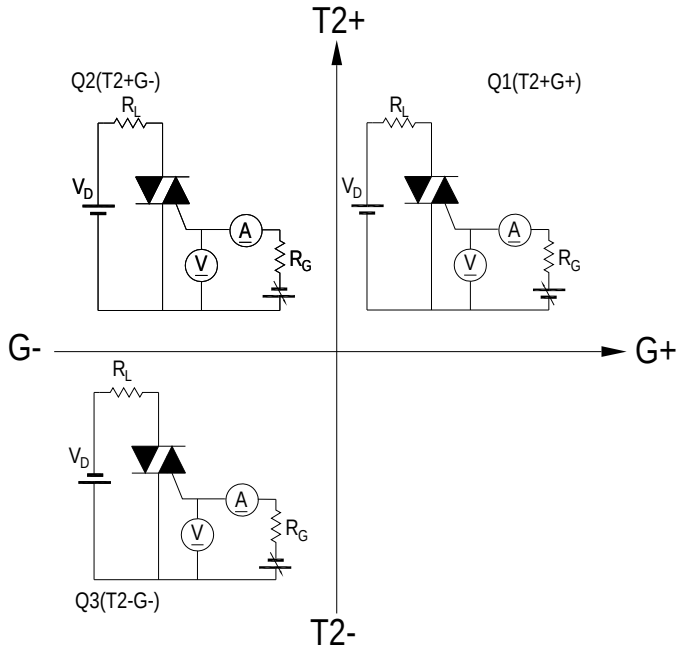


FIG.2: Maximum on-state power dissipation

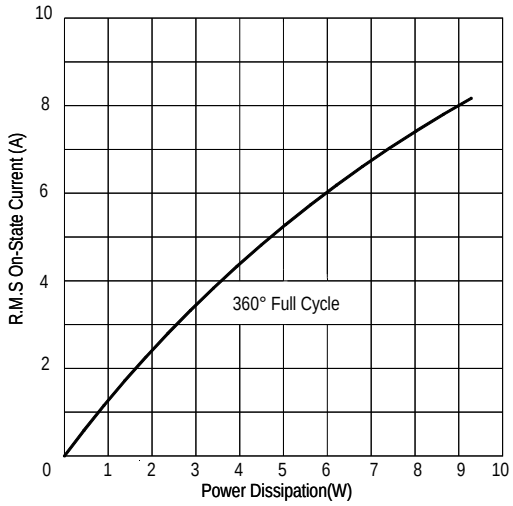


FIG.3: Typical RMS on-state current VS Allowable case Temperature

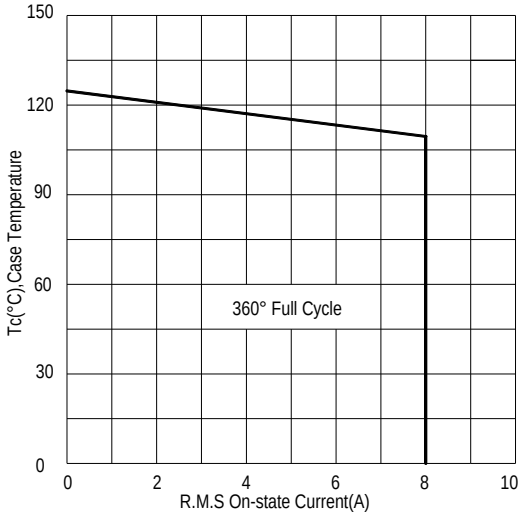


FIG.4: Maximum transient thermal impedance

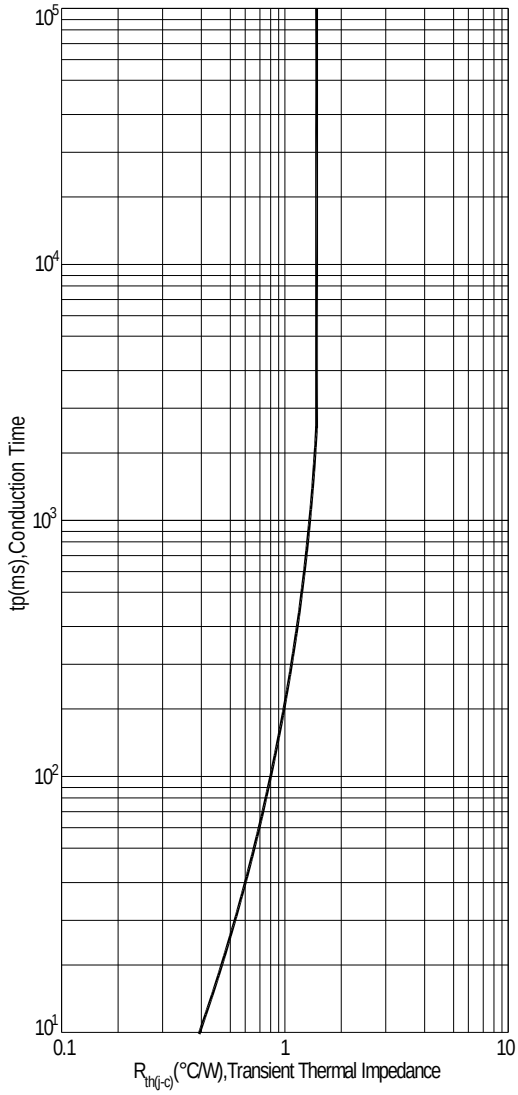


FIG.5: Rated surge on-state current (Non-Repetitive)

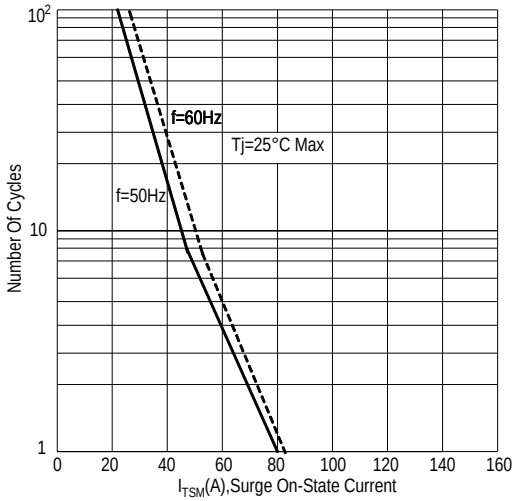


FIG.6: Gate trigger current VS Junction temperature

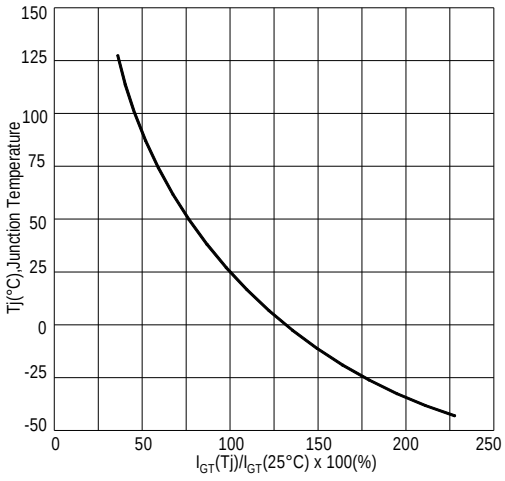


FIG.7:Holding current and Latching current VS Junction temperature

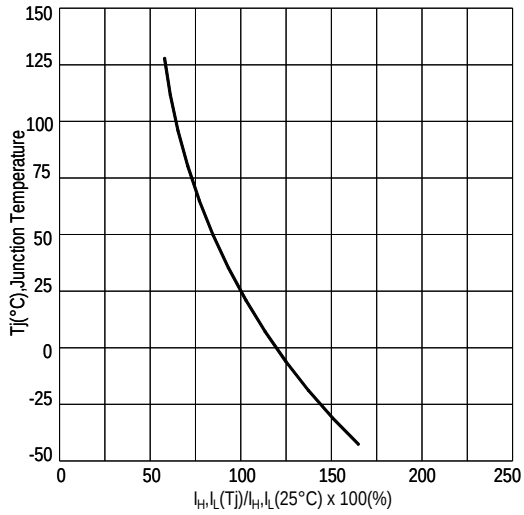


FIG.8: Gate trigger voltage VS Junction temperature

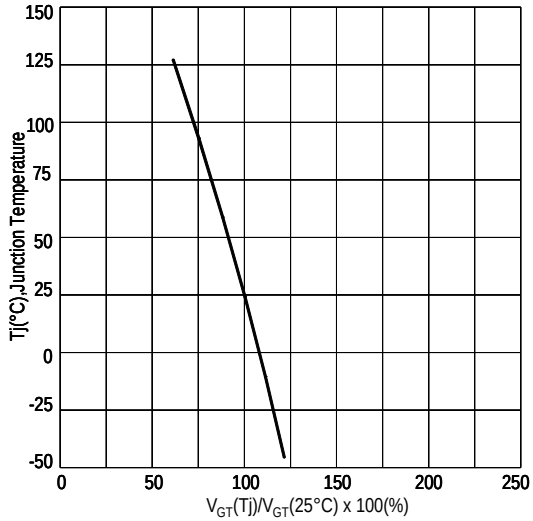
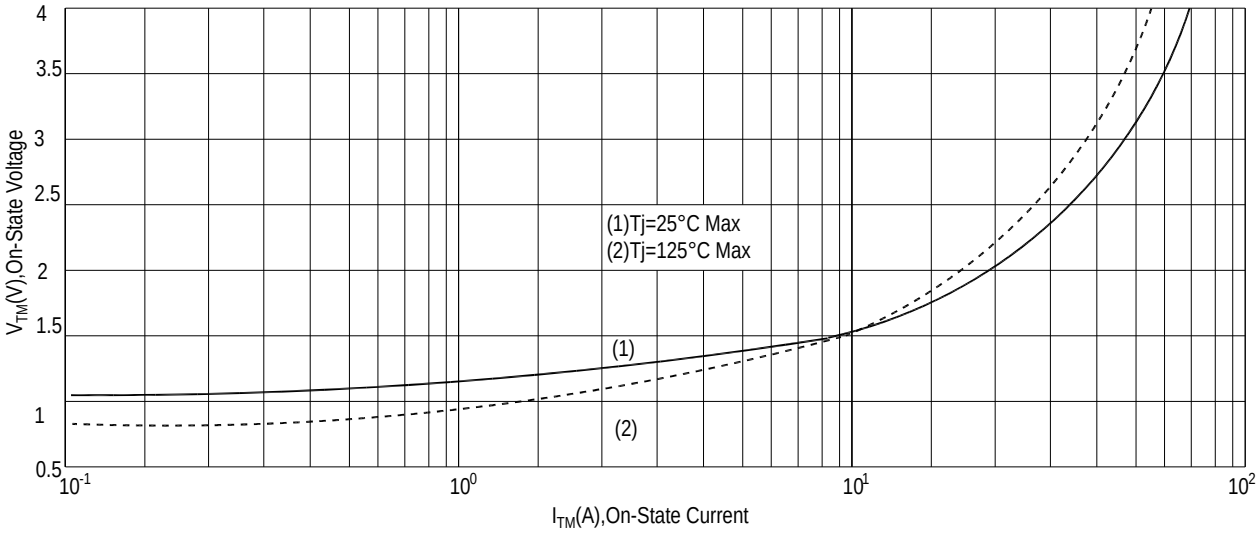
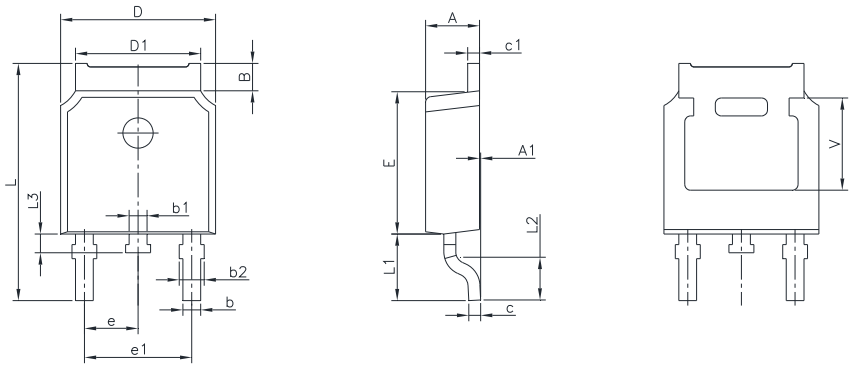


FIG.9: On-state characteristics(Max)



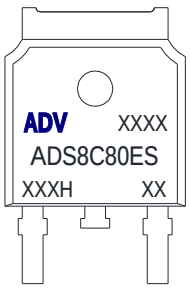
PACKAGE MECHANICAL DATA

TO-252-2 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.070	1.220	0.042	0.048
b	0.720	0.850	0.028	0.033
b1	0.720	0.850	0.028	0.033
c	0.450	0.620	0.017	0.024
c1	0.450	0.620	0.017	0.024
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.900	6.200	0.232	0.244
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	10.60	0.374	0.396
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.950 REF.		0.155 REF.	

Making Diagram



ADV:Logo
ADS8C80ES:Part number
X:Internal control code
H:Halogen Free

AD S 8 C 80 E T(S)(B)

- AD: ADVANCED
- S: Internal control code
- 8: Current:8=8A
- C: Quadrant:C=3Q
- 80: Voltage:60=600V 80=800V
- E: Package explain:E=TO-252-2
- T(S)(B): Sensitivity and type: T=5mA, S=10mA, Blank=35mA, B=50mA

Ordering information

Part number	Package	Marking	Packing	Quantity
ADS8C60E#	TO-252-2	ADS8C60E#	Tube	80pcs
			Embossed tape	2500pcs
ADS8C80E#	TO-252-2	ADS8C80E#	Tube	80pcs
			Embossed tape	2500pcs

Note:# = Gate Trigger Current Sensitivity and type

Notice

1. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any ADV products listed in this document, please confirm the latest product information with a ADV sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by ADV such as that disclosed through our website. (http://www.advsemi.com)
2. ADV has used reasonable care in compiling the information included in this document, but ADV assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
3. You should use the products described herein within the range specified by ADV, especially with respectvto the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. ADV shall have no liability for malfunctions or damages arising out of the use of ADV products beyond such specified ranges.
4. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application.ADV makes no representations, warranties or guaranties regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or ADV products.
5. Although ADV endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a ADV product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
6. This document is provided for reference purposes only so that ADV customers may select the appropriate ADV products for their use. ADV neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of ADV or any third party with respect to the information in this document.
7. ADV shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts,programs, algorithms, and application circuit examples.
8. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from ADV.