ADV SCRs

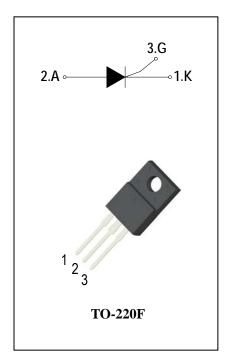
ADT16A60F/80F

General Description

The 16A SCR series of silicon controlled rectifiers, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current (IT(RMS)= 16 A)
- ♦ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V _{DRM}	Repetitive Peak Off-State Voltage	Tj=25°C	ADT16A60F	600	V
V _{RRM}	Repetitive peak reverse voltage	IJ-25 C	ADT16A80F	800	V
I _{T(AV)}	Average On-State Current	Half Sine Wave , Tc = 90°C		13	А
I _{T(RMS)}	R.M.S On-State Current	Half Sine Wave , Tc = 90°C		16	А
I _{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, tp=10ms(50Hz)Tj =25°C		200	А
l ² t	I ² t for Fusing	Tj =25°C,tp =10ms		180	A ² S
P _{GM}	Forward Peak Gate Power Dissipation	Tj =125°C, Pulse Width $\leq 20\mu s$		5	W
P _{G(AV)}	Forward Average Gate Power Dissipation	Tj =25°C, tp =10ms		1	W
I _{GM}	Peak Gate Current	Tj =125°C, Pulse Width $\leq 20\mu s$		4	А
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T _{STG}	Storage Temperature			- 40 ~ 150	°C



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Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items	Conditions		ADT16A60F/80F		Unit	
-				S	Blank	1	
1	Peak Forward Reverse	$V_{DRM} = V_{RRM}$		2		uA	
I _{DRM} I _{RRM}	Blocking Current	Tj = 25°C V _{DRM} = V _{RRM} Tj = 125°C	Max.			mA	
V _{TM}	Peak On-State Voltage	I _{TM} = 32A, t _P = 380 μs	Max.	1.6		V	
V_{GD}	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ Tj = 125°C	Min.	0.2		V	
V_{GT}	Gate Trigger Voltage		Max.	1.3		V	
I _{GT}	Gate Trigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	15	30	mA	
I _H	Holding Current	I _T = 0.5A	Max.	30	40	mA	
١L	Latching Current	I _G = 1.2 I _{GT}	Max.	50	60	mA	
dV/dt	Critical Rate of Rise of Off-State Voltage	V _D = 2/3V _{DRM} gate open Tj = 125°C	Min.	500	600	V/µs	
R _{th(j-c)}	Junction to case (AC)		Max.	2.1		°C/W	
R _{th(j-a)}	Junction to ambient		Max.	60		°C/W	

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FIG.1: Maximum average power dissipation (Single phase half wave) 360° θ 12 Average On-State Current (A) 9 6 θ=180° 3 0 10 12 18 20 2 4 6 8 14 16 Power Dissipation(W)

FIG.3: Gate trigger current VS Junction temperature

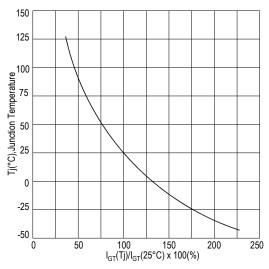
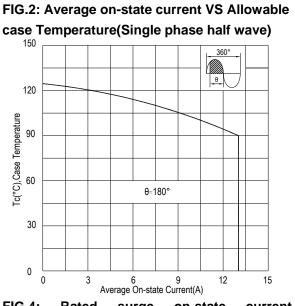
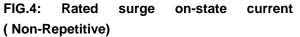
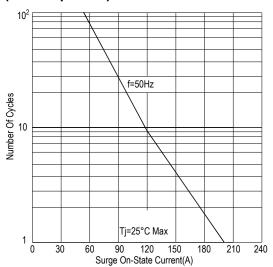
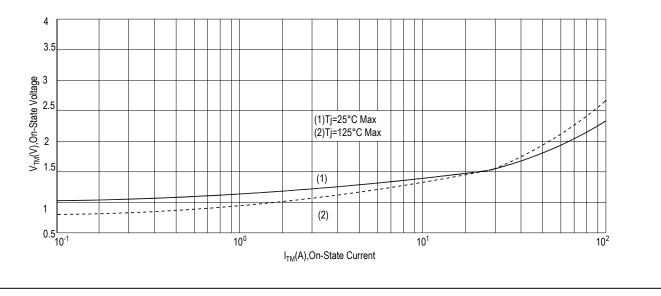


FIG.5: On-state characteristics(Max)

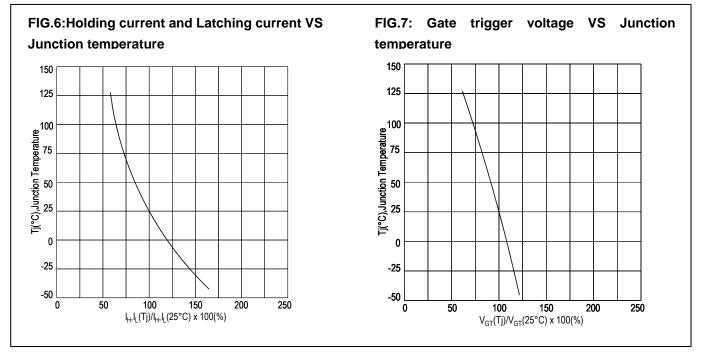






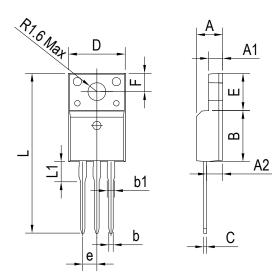


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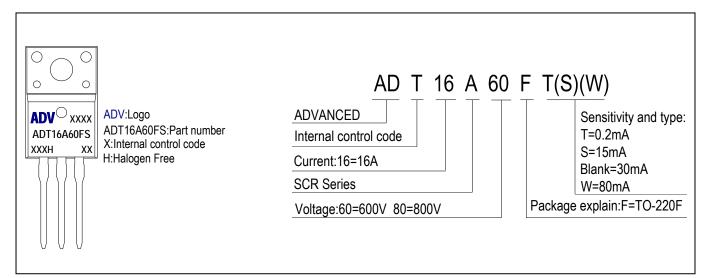


PACKAGE MECHANICAL DATA TO-220F Package Dimension



	Dimensions In Millimeters		Dimensions In Inches		
Symbol					
	Min	Max	Min	Max	
А	4.300	4.800	0.169	0.189	
A1	2.400	2.700	0.094	0.106	
A2	2.500	3.000	0.098	0.118	
В	8.800	9.300	0.346	0.367	
b	0.600	0.950	0.023	0.037	
b1	1.100	1.700	0.043	0.067	
С	0.500	0.750	0.020	0.030	
D	9.700	10.360	0.382	0.408	
Е	6.400	6.800	0.252	0.268	
е	2.540 TYP		0.100 TYP		
F	3.300 REF		0.130 REF		
L	28.000	30.000	1.102	1.181	
L1	2.900	3.630	0.114	0.143	

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADT16A60F#	TO-220F	ADT16A60F#	Tube	50pcs		
ADT16A80F#	TO-220F	ADT16A80F#	Tube	50pcs		
Nator# = Cata Triagar Current Sopolitivity and type						

Note:# = Gate Trigger Current Sensitivity and type

ADT16A60F/80F

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