

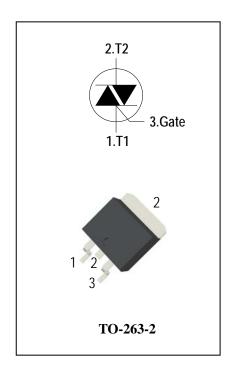
3 Quadrants Triacs

General Description

High current density due to mesa technology .the ADT25C triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids, motor speed controllers.

Features

- ◆ Repetitive Peak Off-State Voltage: 600Vand800V
- ◆ R.M.S On-State Current (I_{T(RMS)}= 25A)
- ◆ High Commutation dv/dt
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Denetitive Deals Off Ctate Valtage	T: _ 25°C	ADT25C60G	600	V
V_{RRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	ADT25C80G	800	V
$I_{T(RMS)}$	R.M.S On-State Current	T _C = 100 °C		25	Α
I_{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		250/260	Α
l ² t	I ² t for fusing	tp=10ms		335	A ² s
-11/-14	Critical rate of rise of on-state	state F = 120 Hz Tj = 125°C			A /
dl/dt	current	I _G = 2 x I _{GT} , tr ≤ 100 ns	55	A/µs	
I_{GM}	Peak Gate Current	tp = 20 μs Tj = 125°C		4	Α
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			1	W
P_GM	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			10	W
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T_{STG}	Storage Temperature			- 40 ~ 150	°C



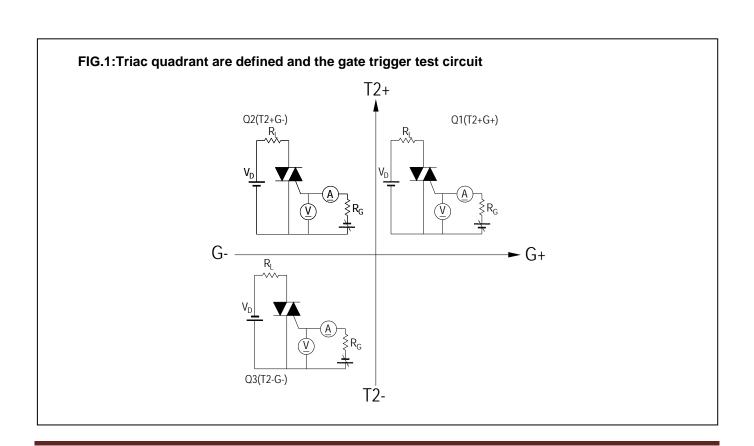


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Electrical Characteristics(Tj = 25°C unless otherwise specified)

Symbol	Items		Conditions		ADT25C60G/80G			Unit
					S	Blank	В	
I _{DRM}	Peak Forward Reverse Blocking		V _{DRM} = V _{RRM} , Tj = 25°C		5		uA	
I _{RRM}	Current		$V_{DRM} = V_{RRM}$, $Tj = 125$ °C	Max.	3			mA
V _{TM}	Peak On-S	tate Voltage	I _{TM} = 35A, t _p = 380 μs	Max.	1.5			V
V_{GD}	Q1-Q2-Q3	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 125^{\circ}\text{C}$	Min.	0.2		٧	
V_{GT}	Q1-Q2-Q3	Gate Trigger Voltage	V 40V D 200	Max.	1.3		V	
I _{GT}	Q1-Q2-Q3	Gate Trigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	10	35	50	mA
I _H	Q1-Q2-Q3	Holding Current	I _T = 0.1A	Max.	20	50	75	mA
	Q1-Q3	Latabian Cumant	1 - 401	Max.	20	80	90	mA
IL	Q2	Latching Current	$I_G = 1.2 I_{GT}$		35	90	110	
dV/dt	Critical Rate of Rise of Off-State Voltage		$V_D = 2/3V_{DRM}$ gate open Tj = 125°C	Min.	500	1000	1500	V/µs
(dV/dt)c	Critical Rate of Change of Commutating Voltage		(dl/dt)c=-12A/ms Tj = 125°C	Min.	1	15	20	V/µs
R _{th(j-c)}	Junction to case (AC)		Max.	0.8		°C/W		
R _{th(j-a)}	Junction to ambient(Copper surface under tab:S=1cm²)			Max.		45		°C/W



ADV

FIG.2: Maximum on-state power dissipation

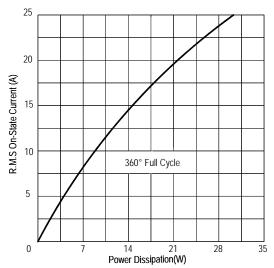


FIG.4: Maximum transient thermal impedance

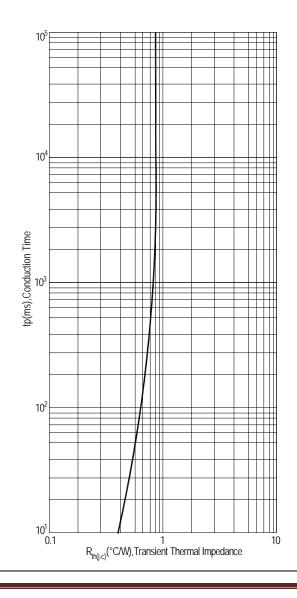


FIG.3: Typical RMS on-state current VS Allowable case Temperature

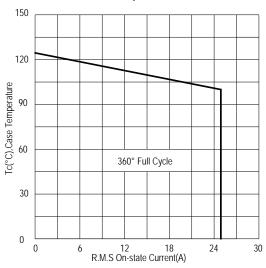


FIG.5: Rated surge on-state current (Non-Repetitive)

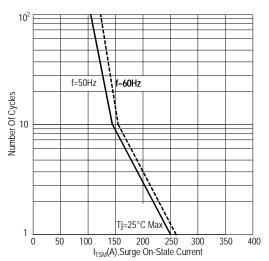


FIG.6: Gate trigger current VS Junction temperature

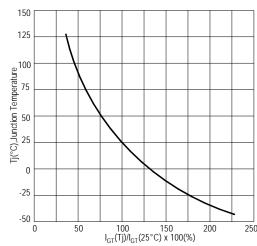






FIG.7:Holding current and Latching current VS Junction temperature

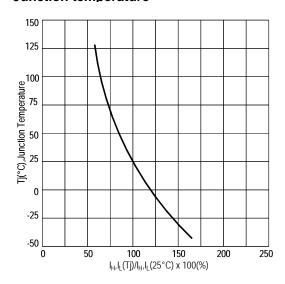


FIG.8: Gate trigger voltage VS Junction temperature

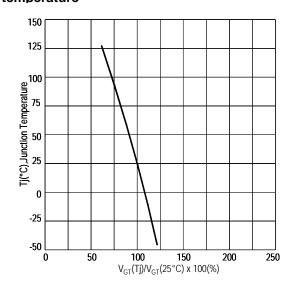
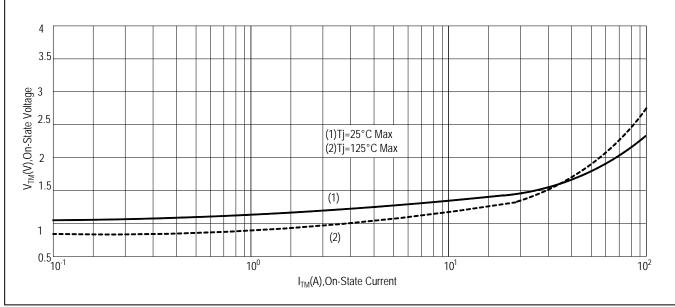


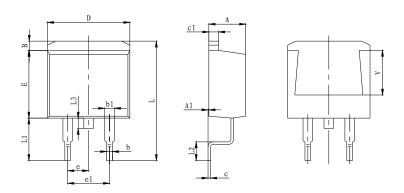
FIG.9: On-state characteristics(Max)



4 / 6 www.advsemi.com Feb,2013 -Rev.3.02

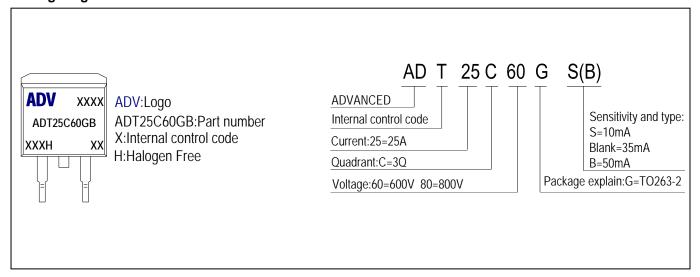


PACKAGE MECHANICAL DATA TO-263-2 Package Dimension



Cumh	Dimer	nsions	Dimensions		
Symb	In Milli	meters	In Inches		
ol	Min	Max	Min	Max	
Α	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540 TYP		0.100 TYP		
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2 2.340 2.740		0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.600 REF		0.220 REF		

Making Diagram



Ordering information

Part number	Package	Marking	Packing	Quantity		
ADT25C60G#	TO-263-2	ADT25C60G#	Tube	50pcs		
AD125C00G#	10-203-2	AD125C00G#	Embossed tape	800pcs		
ADT25C80G#	TO-263-2	ADT25C80G#	Tube	50pcs		
AD125C00G#			Embossed tape	800pcs		
Note:# = Gate Trigger Current Sensitivity and type						



ADT25C60G/80G

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6 / 6 www.advsemi.com Feb,2013 -Rev.3.02