

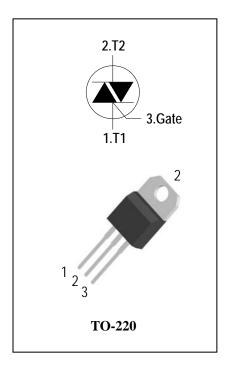
### 3 Quadrants High temperature Triacs

### **General Description**

High current density due to mesa technology , guaranteed maximum junction temperature 150° C. The ADT25CH triac series is suitable for general purpose AC switching. They can beused as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers. The heatsink can be reduced,compared to traditional triacs, according to the high performance at given junction temperatures.

### **Features**

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (IT(RMS)= 25A)
- ♦ High Commutation dv/dt
- ◆ High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



### **Absolute Maximum Ratings**

Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$	Denetitive Deals Off Chate Veltage	T: 05°C	ADT25CH60	600	V
$V_{RRM}$	Repetitive Peak Off-State Voltage	Tj = 25°C	ADT25CH80	800	V
I <sub>T(RMS)</sub>	R.M.S On-State Current	T <sub>C</sub> = 105 °C		25	Α
I <sub>TSM</sub>	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		250/260	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	tp=10ms		335	A <sup>2</sup> s
-11/-14	Critical rate of rise of on-state F = 120 Hz Tj = 150°C			55	A/µs
dI/dt	current	I <sub>G</sub> = 2 x I <sub>GT</sub> , tr ≤ 100 ns			
I <sub>GM</sub>	Peak Gate Current	tp = 20 μs Tj = 150°C		4	Α
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=150°C)			1	W
$P_GM$	Peak Gate Power Dissipation(tp=20us,Tj=150°C)			10	W
T <sub>j</sub>	Operating Junction Temperature			- 40 ~ 150	°C
T <sub>STG</sub>	Storage Temperature			- 40 ~ 150	°C



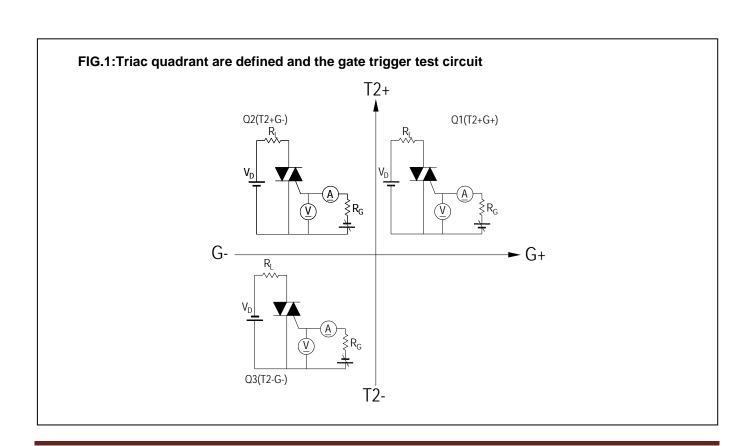


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### **Electrical Characteristics**( Tj = 25°C unless otherwise specified )

Symbol	Items Cond		Conditions		ADT25CH60/80			Unit
						Blank	В	
I <sub>DRM</sub>	Peak Forward Reverse Blocking		$V_{DRM} = V_{RRM}$ , $Tj = 25$ °C	May	5		uA	
I <sub>RRM</sub>	Current		$V_{DRM} = V_{RRM}$ , $Tj = 150$ °C	Max.	8.6			mA
$V_{TM}$	Peak On-S	tate Voltage	$I_{TM} = 35A$ , $t_p = 380 \mu s$	Max.	1.5			V
$V_{GD}$	Q1-Q2-Q3	Non-Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 150^{\circ}\text{C}$	Min.	0.2		>	
$V_{GT}$	Q1-Q2-Q3	Gate Trigger Voltage	V 40V D 200	Max.	1.3			V
I <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Current	$V_D = 12V$ , $R_L = 33\Omega$	Max.	10	35	50	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.1A	Max.	20	50	75	mA
IL	Q1-Q3	Latabina Cumant	sing Current $I_G = 1.2 I_{GT}$ Max.	Mov	20	80	90	m A
	Q2	Laterling Current		35	90	110	mA	
dV/dt	Critical Rate of Rise of Off-State  Voltage		$V_D = 2/3V_{DRM}$ gate open Tj = 150°C	Min.	500	1000	1500	V/µs
(dV/dt)c	Critical Rate of Change of Commutating Voltage		$V_D$ =400V Tj = 150°C (dl/dt)c=-12A/ms	Min.	1	15	20	V/µs
R <sub>th(j-c)</sub>	Junction to case (AC)		Max.	0.8		°C/W		
R <sub>th(j-a)</sub>	Junction to ambient		Max.	60		°C/W		



# **ADV**

FIG.2: Maximum on-state power dissipation

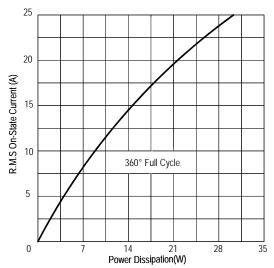


FIG.4: Maximum transient thermal impedance

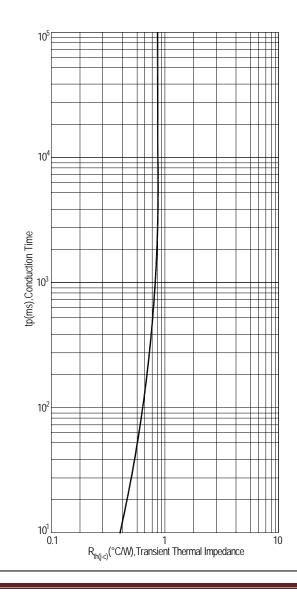


FIG.3: Typical RMS on-state current VS Allowable case Temperature

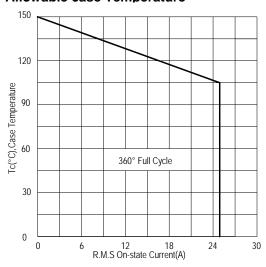


FIG.5: Rated surge on-state current (Non-Repetitive)

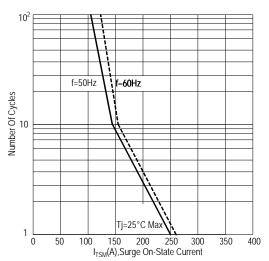


FIG.6: Gate trigger current VS Junction temperature

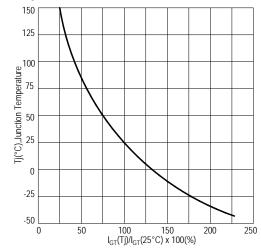




FIG.7:Holding current and Latching current VS Junction temperature

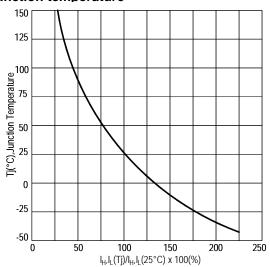


FIG.8: Gate trigger voltage VS Junction temperature

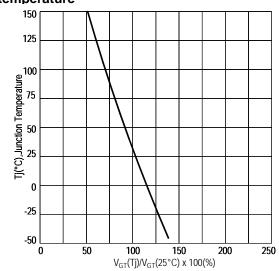
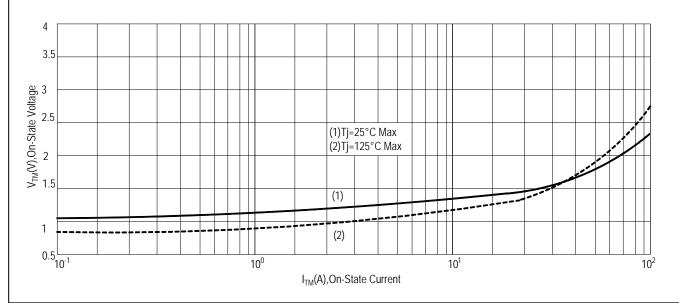


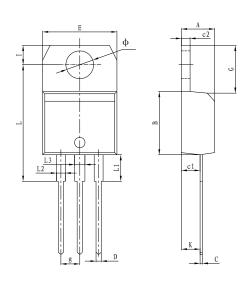
FIG.9: On-state characteristics(Max)



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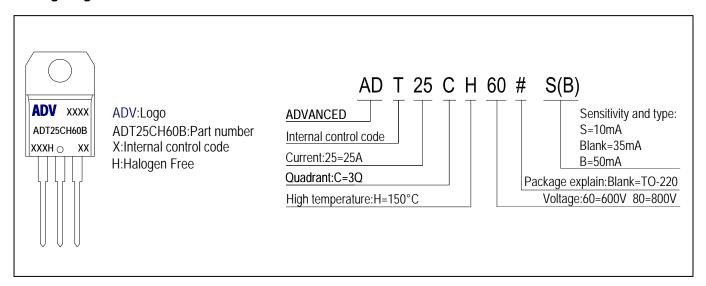


## PACKAGE MECHANICAL DATA TO-220 Package Dimension



	Dimer	nsions	Dimensions		
Symbol	Symbol In Millin		In Inches		
	Min	Max	Min	Max	
Α	4.40	4.60	0.173	0.181	
В	9.00	9.30	0.354	0.366	
С	0.40	0.60	0.015	0.023	
c1	2.00	2.60	0.078	0.102	
c2	1.23	1.32	0.048	0.051	
D	0.70	1.00	0.027	0.039	
E	10.00	10.40	0.393	0.409	
g	2.40	2.70	0.094	0.106	
G	6.20	6.80	0.244	0.267	
I	2.65	2.95	0.104	0.116	
L	15.80	16.80	0.622	0.661	
L1	3.75		0.147		
L2	1.14	1.70	0.044	0.066	
L3	1.14	1.70	0.044	0.066	
Ф	Ф 3.60 3.90		0.141	0.153	
К	2.60TYP		0.102TYP		

### **Making Diagram**



### **Ordering information**

Part number	Package	Marking	Packing	Quantity			
ADT25CH60#	TO-220	ADT25CH60#	Tube	50pcs			
ADT25CH80#	TO-220	ADT25CH80#	Tube	50pcs			
Note:# = Gate Trigger Current Sensitivity and type							



### ADT25CH60/80

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