

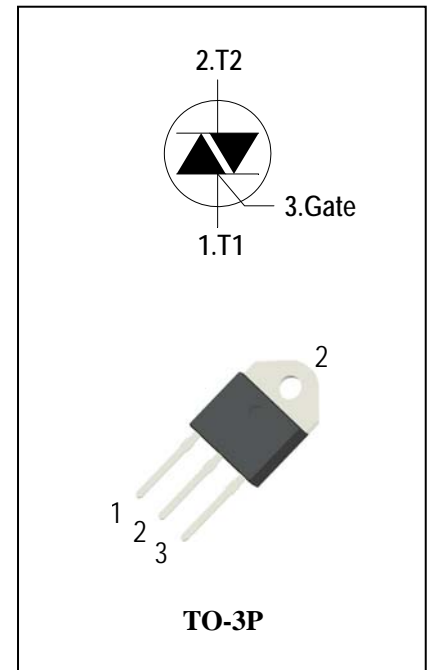
## 3 Quadrants High temperature Triacs

### General Description

High current density due to mesa technology , guaranteed maximum junction temperature 150° C. The ADT25CH triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners, Rectifier-fed DC inductive loads e.g. DC motors and solenoids , motor speed controllers. The heatsink can be reduced, compared to traditional triacs, according to the high performance at given junction temperatures.

### Features

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (  $I_{T(RMS)} = 25\text{ A}$  )
- ◆ High Commutation  $dv/dt$
- ◆ High junction temperature operating capability
- ◆ These Devices are Pb-Free and are RoHS Compliant



### Absolute Maximum Ratings

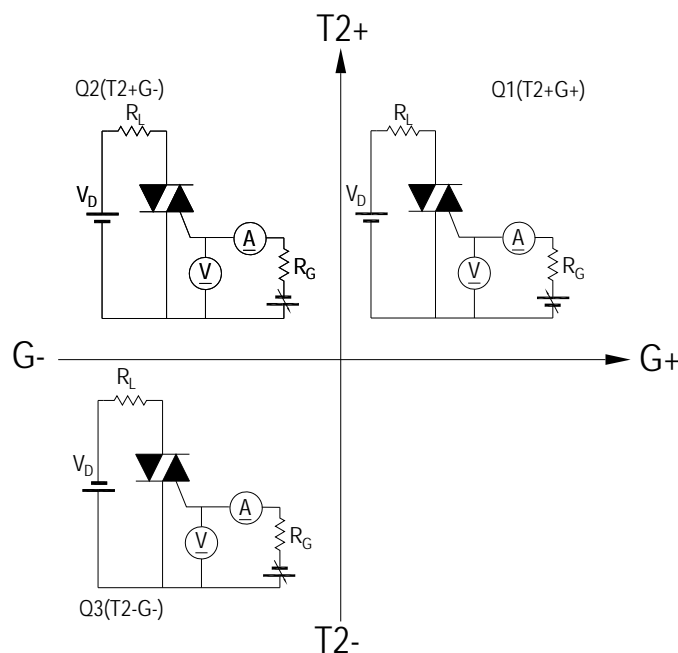
Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$ $V_{RRM}$	Repetitive Peak Off-State Voltage	$T_j = 25^\circ\text{C}$	ADT25CH60H	600	V
			ADT25CH80H	800	V
$I_{T(RMS)}$	R.M.S On-State Current	$T_C = 105^\circ\text{C}$		25	A
$I_{TSM}$	Surge On-State Current	$t_p = 20\text{ms}(50\text{Hz})/t_p = 16.7\text{ms}(60\text{Hz})$		250/260	A
$I^2t$	$I^2t$ for fusing	$t_p = 10\text{ms}$		335	$\text{A}^2\text{s}$
$di/dt$	Critical rate of rise of on-state current	$F = 120\text{ Hz } T_j = 150^\circ\text{C}$ $I_G = 2 \times I_{GT}, t_r \leq 100\text{ ns}$		55	$\text{A}/\mu\text{s}$
$I_{GM}$	Peak Gate Current	$t_p = 20\text{ }\mu\text{s } T_j = 150^\circ\text{C}$		4	A
$P_{G(AV)}$	Average Gate Power Dissipation( $T_j = 150^\circ\text{C}$ )			1	W
$P_{GM}$	Peak Gate Power Dissipation( $t_p = 20\text{ }\mu\text{s}, T_j = 150^\circ\text{C}$ )			10	W
$T_j$	Operating Junction Temperature			- 40 ~ 150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature			- 40 ~ 150	$^\circ\text{C}$



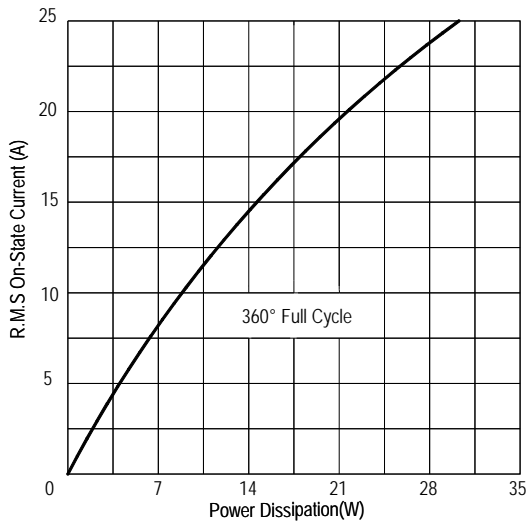
## Electrical Characteristics (T<sub>j</sub> = 25°C unless otherwise specified )

Symbol	Items		Conditions		ADT25CH60H/80H			Unit
					S	Blank	B	
I <sub>DRM</sub> I <sub>RRM</sub>	Peak Forward Reverse Blocking Current		V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 25°C V <sub>DRM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 150°C	Max.	5 8.6			uA mA
V <sub>TM</sub>	Peak On-State Voltage		I <sub>TM</sub> = 35A, t <sub>p</sub> = 380 μs	Max.	1.5			V
V <sub>GD</sub>	Q1-Q2-Q3	Non-Trigger Gate Voltage	V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ T <sub>j</sub> = 150°C	Min.	0.2			V
V <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Voltage	V <sub>D</sub> = 12V , R <sub>L</sub> = 33Ω	Max.	1.3			V
I <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Current		Max.	10	35	50	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.1A	Max.	20	50	75	mA
I <sub>L</sub>	Q1-Q3	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	20	80	90	mA
	Q2				35	90	110	
dV/dt	Critical Rate of Rise of Off-State Voltage		V <sub>D</sub> = 2/3V <sub>DRM</sub> gate open T <sub>j</sub> = 150°C	Min.	500	1000	1500	V/μs
(dV/dt) <sub>c</sub>	Critical Rate of Change of Commutating Voltage		V <sub>D</sub> =400V T <sub>j</sub> = 150°C (dI/dt) <sub>c</sub> =-12A/ms	Min.	1	15	20	V/μs
R <sub>th(j-c)</sub>	Junction to case (AC)			Max.	0.6			°C/W
R <sub>th(j-a)</sub>	Junction to ambient			Max.	50			°C/W

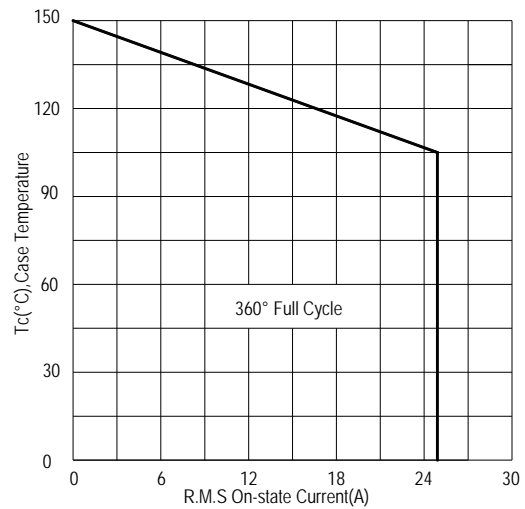
FIG.1:Triac quadrant are defined and the gate trigger test circuit



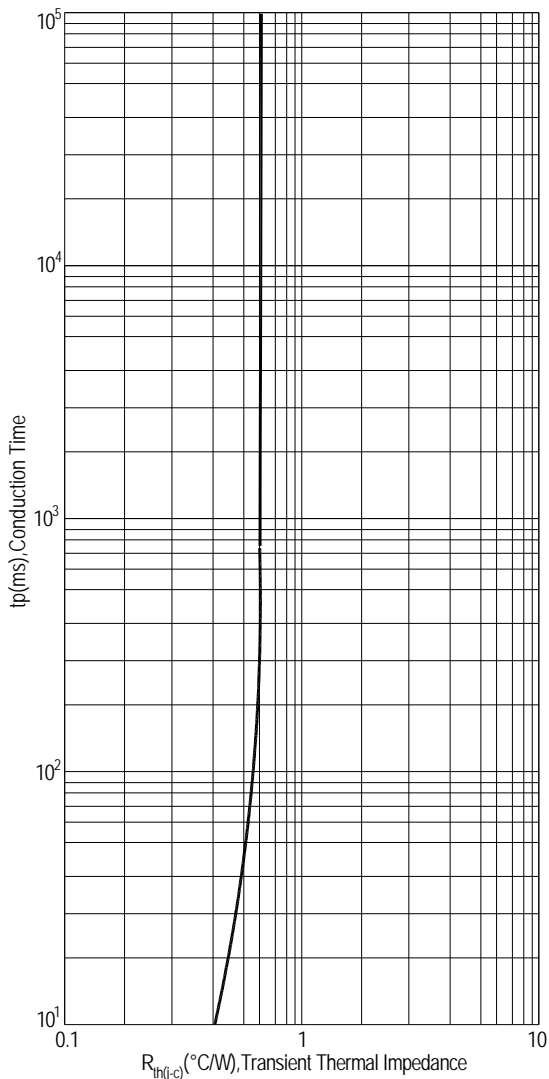
**FIG.2: Maximum on-state power dissipation**



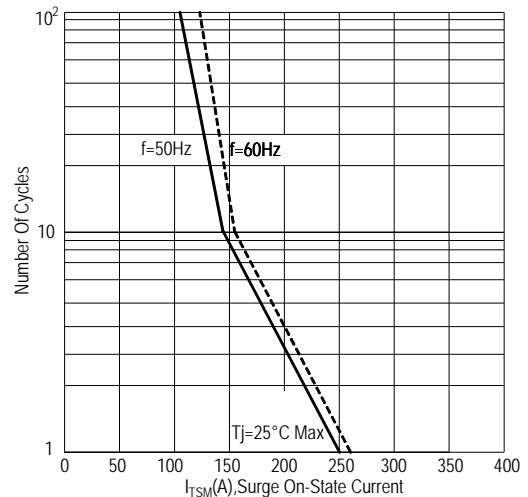
**FIG.3: Typical RMS on-state current VS Allowable case Temperature**



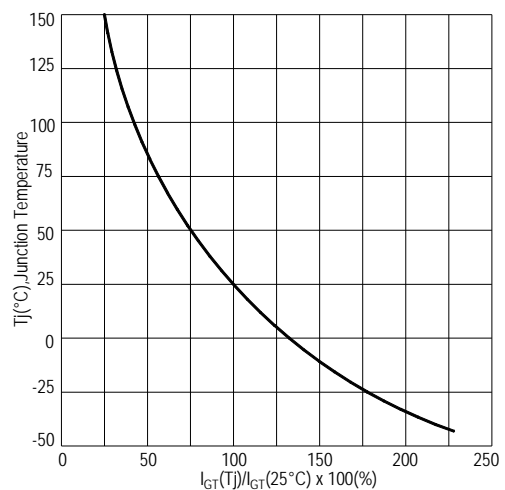
**FIG.4: Maximum transient thermal impedance**



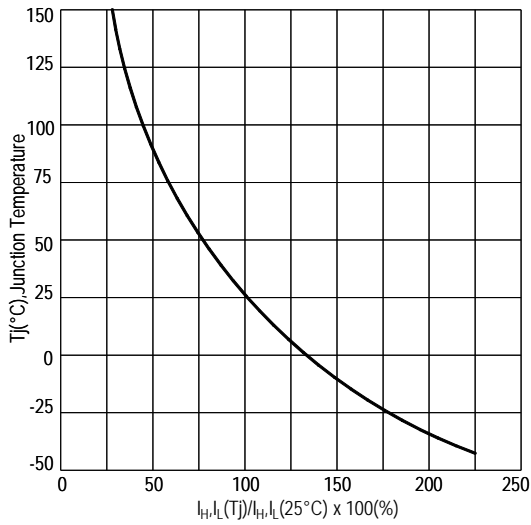
**FIG.5: Rated surge on-state current (Non-Repetitive)**



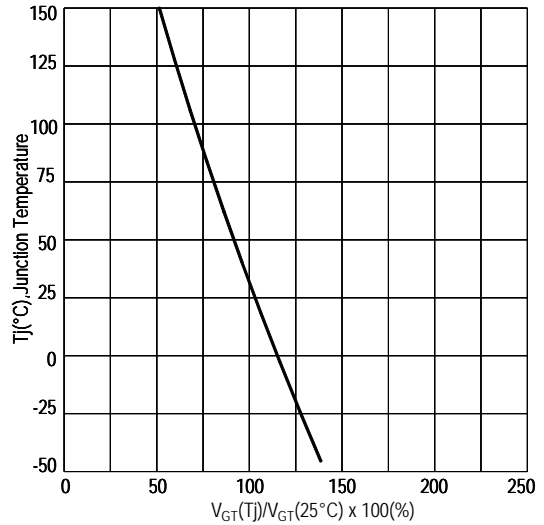
**FIG.6: Gate trigger current VS Junction temperature**



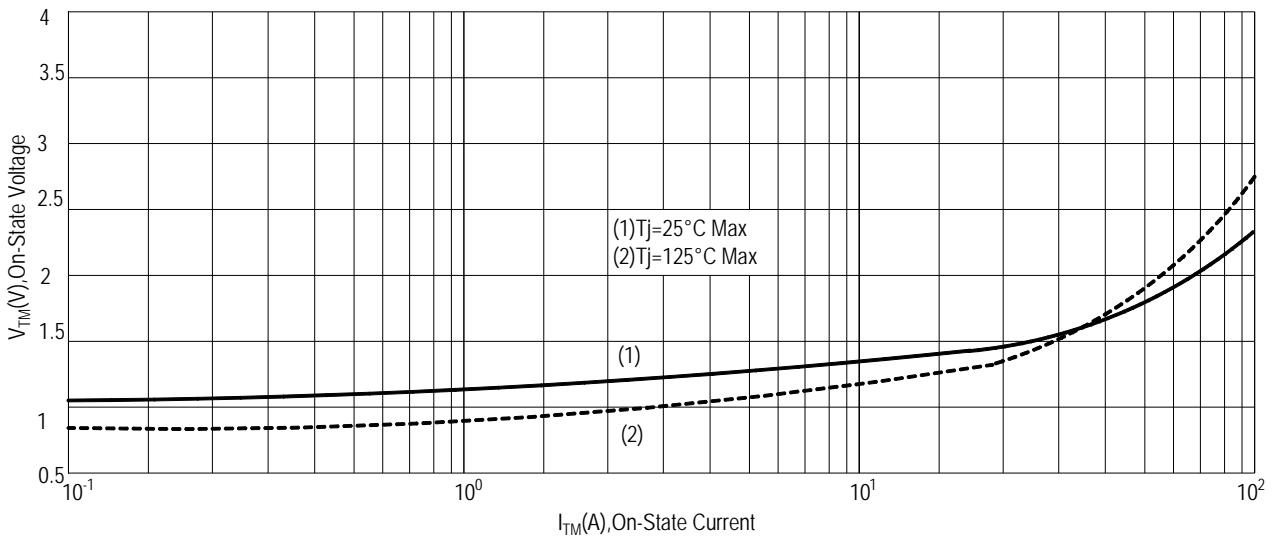
**FIG.7: Holding current and Latching current VS Junction temperature**



**FIG.8: Gate trigger voltage VS Junction temperature**

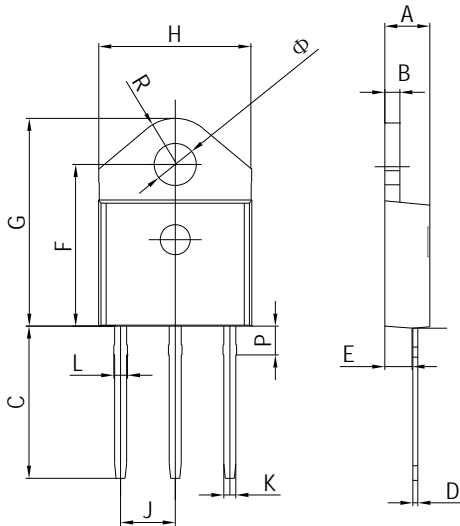


**FIG.9: On-state characteristics(Max)**



## PACKAGE MECHANICAL DATA

### TO-3P Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.4	4.6	0.173	0.181
B	1.45	1.55	0.057	0.061
C	14.35	15.60	0.565	0.614
D	0.5	0.7	0.020	0.028
E	2.7	2.9	0.106	0.114
F	15.8	16.5	0.622	0.650
G	20.4	21.1	0.815	0.831
H	15.1	15.5	0.594	0.610
J	5.4	5.65	0.213	0.222
K	1.2	1.4	0.047	0.055
Ø	4.08	4.20	0.161	0.165
L	1.35	1.50	0.053	0.059
P	2.8	3.0	0.110	0.118
R	4.60 typ.		0.181 typ.	

### Making Diagram

ADV: Logo  
 ADT25CH60HB: Part number  
 X: Internal control code  
 H: Halogen Free

AD T 25 C H 60 H S(B)

ADVANCED	Internal control code	Current: 25=25A	Quadrant: C=3Q
High temperature: H=150°C	Sensitivity and type: S=10mA Blank=35mA B=50mA		
Package explain: H=TO-3P		Voltage: 60=600V 80=800V	

### Ordering information

Part number	Package	Marking	Packing	Quantity
ADT25CH60H#	TO-3P	ADT25CH60H#	Tube	30pcs
ADT25CH80H#	TO-3P	ADT25CH80H#	Tube	30pcs

Note: # = Gate Trigger Current Sensitivity and type

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