

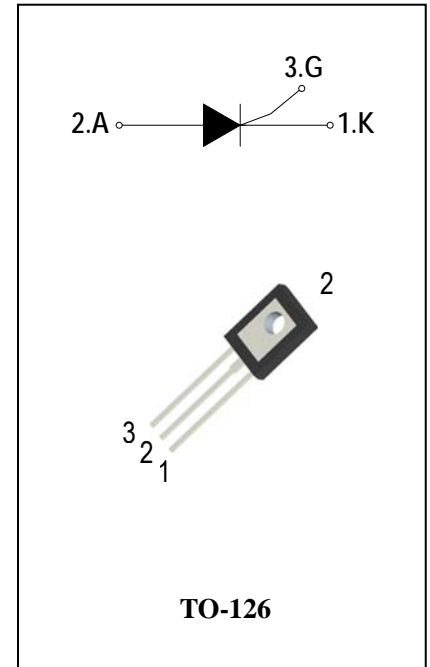
SCRs

General Description

Available either in sensitive or standard gate triggering levels, the 4A SCR series is suitable to fit all modes of control found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, in-rush current limiting circuits, capacitive discharge ignition, voltage regulation circuits...

Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current ($I_{T(RMS)}$) = 4A)
- ◆ These are Pb-Free Devices



Absolute Maximum Ratings

| Symbol | Items | Conditions | | Ratings | Unit |
|--------------|---|---|----------|------------|------------------------|
| V_{DRM} | Repetitive Peak Off-State Voltage | $T_j = 25^\circ\text{C}$ | ADT4A60B | 600 | V |
| V_{RRM} | Repetitive peak reverse voltage | | ADT4A80B | 800 | V |
| $I_{T(AV)}$ | Average On-State Current | Half Sine Wave , $T_c = 100^\circ\text{C}$ | | 3.5 | A |
| $I_{T(RMS)}$ | R.M.S On-State Current | Half Sine Wave , $T_c = 100^\circ\text{C}$ | | 4 | A |
| I_{TSM} | Surge On-State Current | 1/2 Cycle, Sine Wave Non-Repetitive, $t_p = 10\text{ms}(50\text{Hz}) T_j = 25^\circ\text{C}$ | | 36 | A |
| I^2t | I^2t for Fusing | $T_j = 25^\circ\text{C}, t_p = 10\text{ms}$ | | 4.5 | A^2S |
| di/dt | Critical rate of rise of on-state current | $T_j = 125^\circ\text{C}, t_r \leq 100\text{ns}$ | | 50 | $\text{A}/\mu\text{s}$ |
| P_{GM} | Forward Peak Gate Power Dissipation | $T_j = 125^\circ\text{C}, \text{Pulse Width} \leq 20\mu\text{s}$ | | 2 | W |
| $P_{G(AV)}$ | Forward Average Gate Power Dissipation | $T_j = 25^\circ\text{C}, t_p = 10\text{ms}$ | | 0.2 | W |
| I_{GM} | Peak Gate Current | $T_j = 125^\circ\text{C}, \text{Pulse Width} \leq 20\mu\text{s}$ | | 1.2 | A |
| T_j | Operating Junction Temperature | | | - 40 ~ 125 | $^\circ\text{C}$ |
| T_{STG} | Storage Temperature | | | - 40 ~ 150 | $^\circ\text{C}$ |



Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

| Symbol | Items | Conditions | | ADT4A60B/80B | | Unit |
|--------------------------------------|---|---|------|--------------|-----|--------------------|
| | | | | T | S | |
| I_{DRM} I_{RRM} | Peak Forward Reverse Blocking Current | $V_{\text{DRM}} = V_{\text{RRM}}, R_{\text{GK}} = 1\text{K}\Omega$ $T_j = 25^\circ\text{C}$ | Max. | 5 | | μA |
| | | $V_{\text{DRM}} = V_{\text{RRM}}, R_{\text{GK}} = 1\text{K}\Omega$ $T_j = 125^\circ\text{C}$ | | 1 | | mA |
| V_{TM} | Peak On-State Voltage | $I_{\text{TM}} = 10\text{A}, t_p = 380\ \mu\text{s}$ | Max. | 1.7 | | V |
| V_{GD} | Non-Trigger Gate Voltage | $V_{\text{D}} = V_{\text{DRM}}, R_{\text{L}} = 3.3\ \text{k}\Omega$ $R_{\text{GK}} = 1\text{K}\Omega, T_j = 125^\circ\text{C}$ | Min. | 0.2 | | V |
| V_{GT} | Gate Trigger Voltage | $V_{\text{D}} = 12\text{V}, R_{\text{L}} = 33\Omega$ | Max. | 1.3 | | V |
| I_{GT} | Gate Trigger Current | | Max. | 0.2 | 15 | mA |
| I_{H} | Holding Current | $I_{\text{T}} = 0.05\text{A}, R_{\text{GK}} = 1\text{K}\Omega$ | Max. | 5 | 30 | mA |
| I_{L} | Latching Current | $I_{\text{G}} = 1.2 I_{\text{GT}}, R_{\text{GK}} = 1\text{K}\Omega$ | Max. | 6 | 50 | mA |
| dV/dt | Critical Rate of Rise of Off-State Voltage | $V_{\text{D}} = 2/3 V_{\text{DRM}}$ gate open $R_{\text{GK}} = 1\text{K}\Omega, T_j = 125^\circ\text{C}$ | Min. | 5 | 150 | V/ μs |
| $R_{\text{th(j-c)}}$ | Junction to case | | Max. | 7.2 | | $^\circ\text{C/W}$ |
| $R_{\text{th(j-a)}}$ | Junction to ambient | | Max. | 60 | | $^\circ\text{C/W}$ |

FIG.1: Maximum average power dissipation (Single phase half wave)

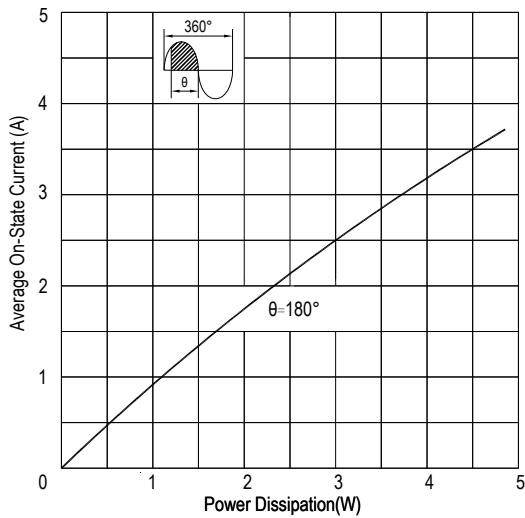


FIG.2: Average on-state current VS Allowable case Temperature (Single phase half wave)

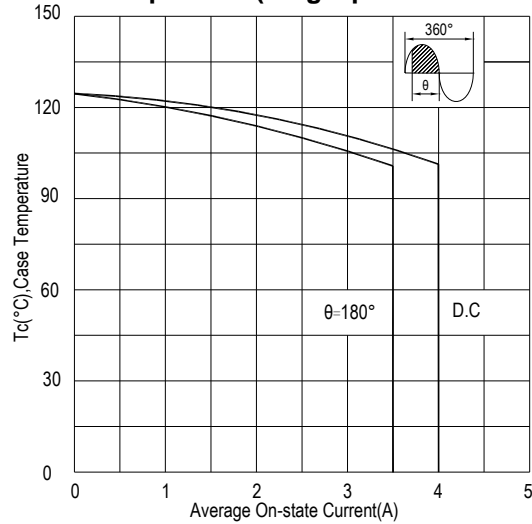


FIG.3: Gate trigger current VS Junction temperature

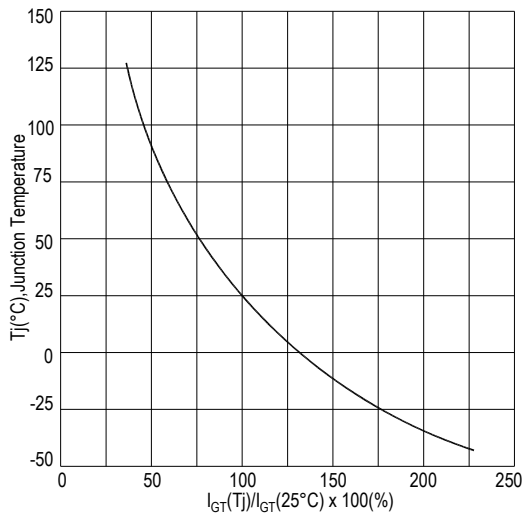


FIG.4: Rated surge on-state current (Non-Repetitive)

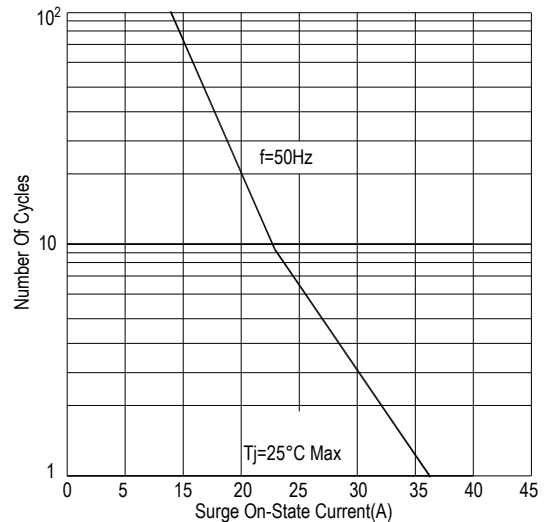


FIG.5: On-state characteristics(Max)

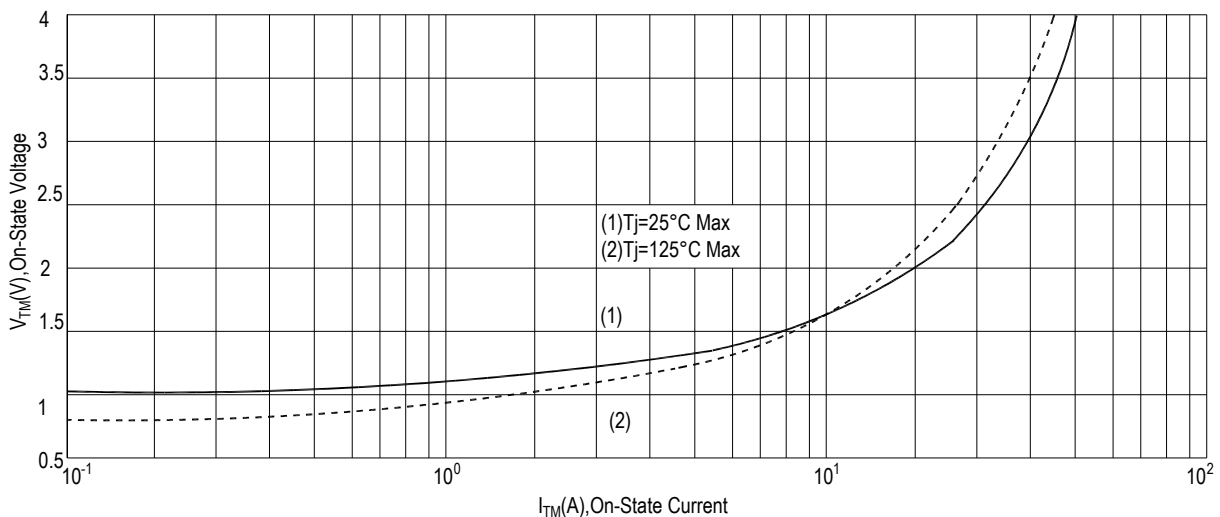


FIG.6: Holding current and Latching current VS Junction temperature

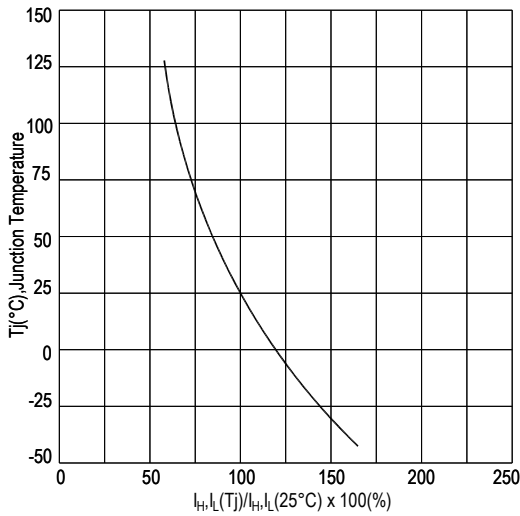


FIG.7: Gate trigger voltage VS Junction temperature

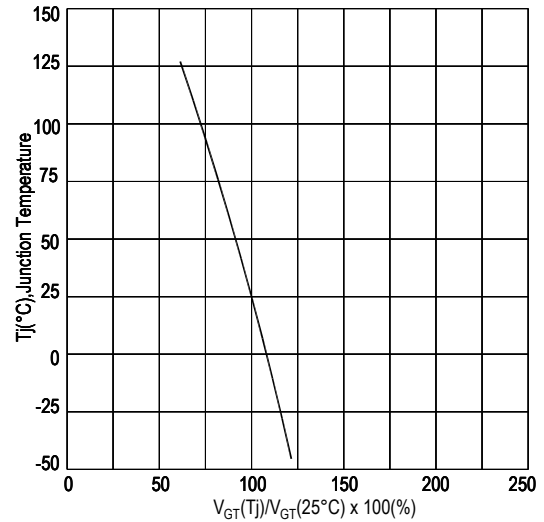


FIG.8: Gate trigger current VS Junction temperature for type T gate triggering

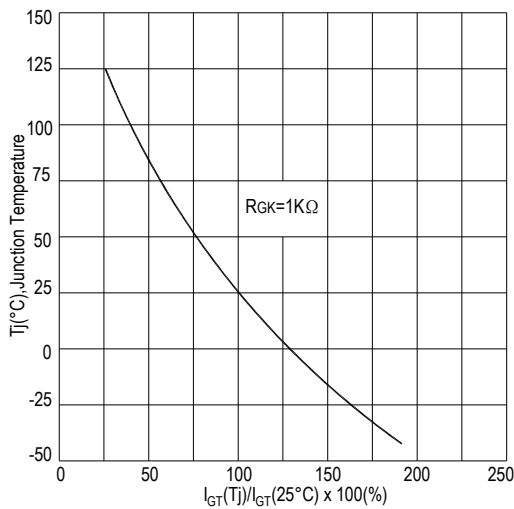
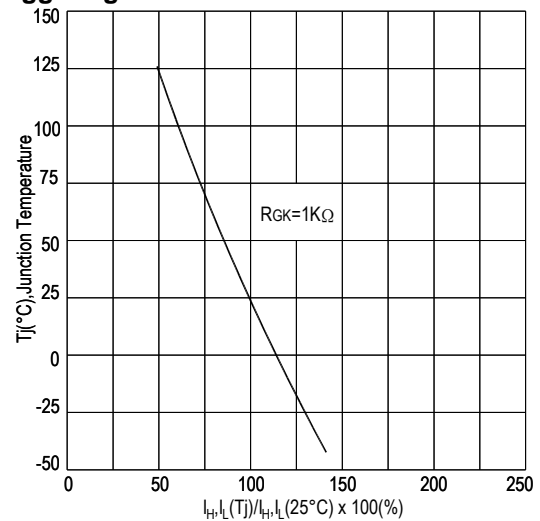
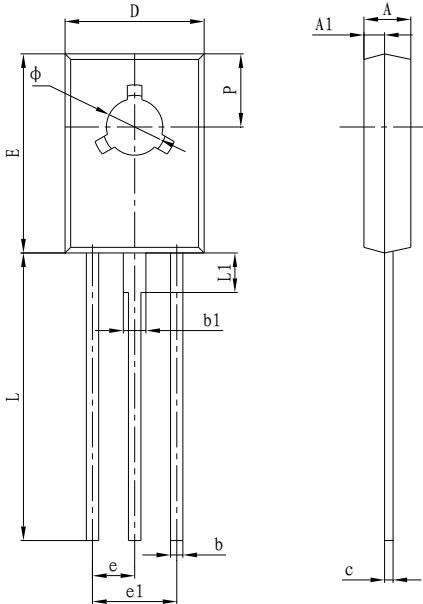


FIG.8: Holding current and Latching current VS Junction temperature for type T gate triggering



PACKAGE MECHANICAL DATA

TO-126 Package Dimension



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 2.500 | 2.900 | 0.098 | 0.114 |
| A1 | 1.100 | 1.500 | 0.043 | 0.059 |
| b | 0.660 | 0.860 | 0.026 | 0.034 |
| b1 | 1.170 | 1.370 | 0.046 | 0.054 |
| c | 0.450 | 0.600 | 0.018 | 0.024 |
| D | 7.400 | 8.000 | 0.291 | 0.315 |
| E | 10.600 | 11.000 | 0.417 | 0.433 |
| e | 2.290 TYP | | 0.090 TYP | |
| e1 | 4.480 | 4.680 | 0.176 | 0.184 |
| L | 15.300 | 15.700 | 0.602 | 0.618 |
| L1 | 2.100 | 2.300 | 0.083 | 0.091 |
| P | 3.900 | 4.100 | 0.154 | 0.161 |
| Φ | 3.000 | 3.200 | 0.118 | 0.126 |

Making Diagram

ADV: Logo
ADT4A60BT: Part number
X: Internal control code
H: Halogen Free

AD T 4 A 60 B T(S)(W)

| | | | |
|------------|--|---------------------------|--------------------------|
| ADVANCED | Internal control code | Current: 4=4A | Voltage: 60=600V 80=800V |
| SCR Series | Sensitivity and type: T=0.2mA S=15mA Blank=30mA W=80mA | Package explain: B=TO-126 | |

Ordering information

| Part number | Package | Marking | Packing | Quantity |
|-------------|---------|-----------|------------|----------|
| ADT4A60B# | TO-126 | ADT4A60B# | Vinyl sack | 500pcs |
| ADT4A80B# | TO-126 | ADT4A80B# | Vinyl sack | 500pcs |

Note: # = Gate Trigger Current Sensitivity and type

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