

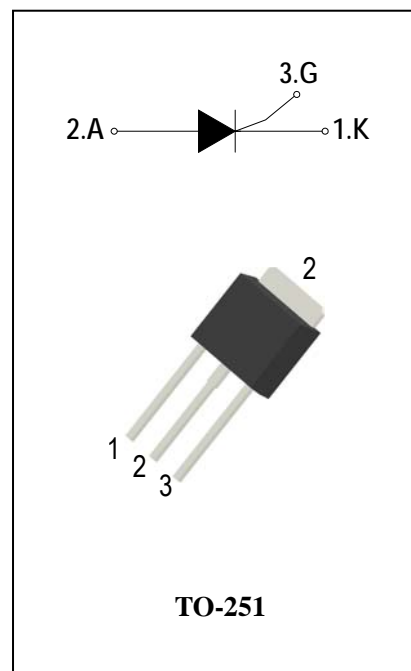
SCRs

General Description

Available either in sensitive or standard gate triggering levels, the 8A SCR series is suitable to fit all modes of control found in applications such as overvoltage crowbar protection, motor control circuits in power tools and kitchen aids, in-rush current limiting circuits, capacitive discharge ignition, voltage regulation circuits...

Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current ($I_{T(RMS)}$) = 8A)
- ◆ These are Pb-Free Devices



Absolute Maximum Ratings

Symbol	Items	Conditions		Ratings	Unit
V_{DRM}	Repetitive Peak Off-State Voltage	$T_j = 25^\circ\text{C}$	ADT8A60D	600	V
V_{RRM}	Repetitive peak reverse voltage		ADT8A80D	800	V
$I_{T(AV)}$	Average On-State Current	Half Sine Wave , $T_c = 100^\circ\text{C}$		7	A
$I_{T(RMS)}$	R.M.S On-State Current	Half Sine Wave , $T_c = 100^\circ\text{C}$		8	A
I_{TSM}	Surge On-State Current	1/2 Cycle, Sine Wave Non-Repetitive, $t_p = 10\text{ms}(50\text{Hz}) T_j = 25^\circ\text{C}$		90	A
I^2t	I^2t for Fusing	$T_j = 25^\circ\text{C}, t_p = 10\text{ms}$		24.5	A^2S
di/dt	Critical rate of rise of on-state current	$T_j = 125^\circ\text{C}, t_r \leq 100\text{ns}$		50	$\text{A}/\mu\text{s}$
P_{GM}	Forward Peak Gate Power Dissipation	$T_j = 125^\circ\text{C}, \text{Pulse Width} \leq 20\mu\text{s}$		5	W
$P_{G(AV)}$	Forward Average Gate Power Dissipation	$T_j = 25^\circ\text{C}, t_p = 10\text{ms}$		1	W
I_{GM}	Peak Gate Current	$T_j = 125^\circ\text{C}, \text{Pulse Width} \leq 20\mu\text{s}$		4	A
T_j	Operating Junction Temperature			- 40 ~ 125	$^\circ\text{C}$
T_{STG}	Storage Temperature			- 40 ~ 150	$^\circ\text{C}$



Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Items	Conditions		ADT8A60D/80D		Unit
				T	S	
I_{DRM} I_{RRM}	Peak Forward Reverse Blocking Current	$V_{\text{DRM}} = V_{\text{RRM}}, R_{\text{GK}} = 1\text{K}\Omega$ $T_j = 25^\circ\text{C}$	Max.	5		μA
		$V_{\text{DRM}} = V_{\text{RRM}}, R_{\text{GK}} = 1\text{K}\Omega$ $T_j = 125^\circ\text{C}$		2		mA
V_{TM}	Peak On-State Voltage	$I_{\text{TM}} = 20\text{A}, t_p = 380\ \mu\text{s}$	Max.	1.6		V
V_{GD}	Non-Trigger Gate Voltage	$V_{\text{D}} = V_{\text{DRM}}, R_{\text{L}} = 3.3\ \text{k}\Omega$ $R_{\text{GK}} = 1\text{K}\Omega, T_j = 125^\circ\text{C}$	Min.	0.2		V
V_{GT}	Gate Trigger Voltage	$V_{\text{D}} = 12\text{V}, R_{\text{L}} = 33\Omega$	Max.	1.3		V
I_{GT}	Gate Trigger Current		Max.	0.2	15	mA
I_{H}	Holding Current	$I_{\text{T}} = 0.05\text{A}, R_{\text{GK}} = 1\text{K}\Omega$	Max.	5	40	mA
I_{L}	Latching Current	$I_{\text{G}} = 1.2 I_{\text{GT}}, R_{\text{GK}} = 1\text{K}\Omega$	Max.	6	50	mA
dV/dt	Critical Rate of Rise of Off-State Voltage	$V_{\text{D}} = 2/3 V_{\text{DRM}}$ gate open $R_{\text{GK}} = 1\text{K}\Omega, T_j = 125^\circ\text{C}$	Min.	5	150	V/ μs
$R_{\text{th(j-c)}}$	Junction to case		Max.	20		$^\circ\text{C/W}$
$R_{\text{th(j-a)}}$	Junction to ambient		Max.	100		$^\circ\text{C/W}$

FIG.1: Maximum average power dissipation (Single phase half wave)

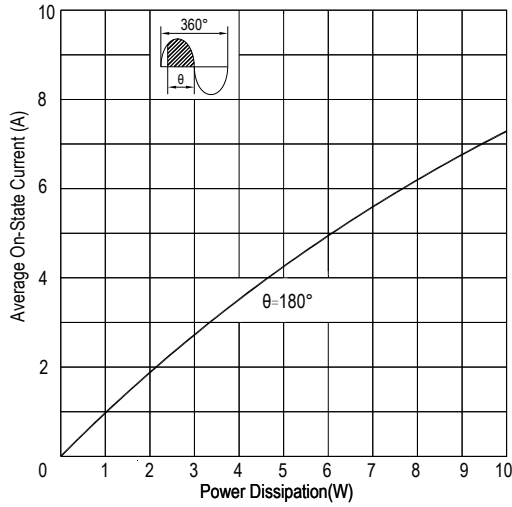


FIG.2: Average on-state current VS Allowable case Temperature (Single phase half wave)

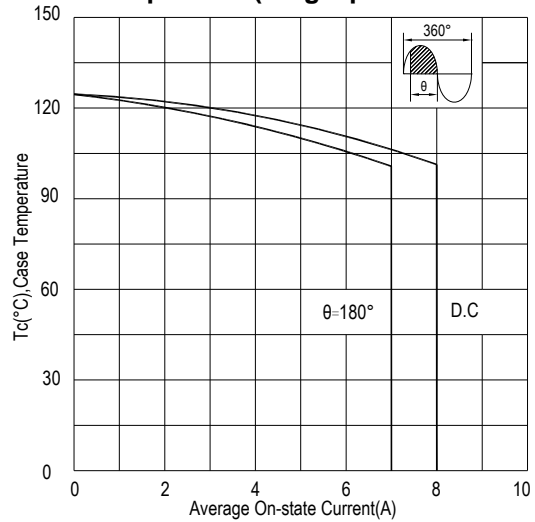


FIG.3: Gate trigger current VS Junction temperature

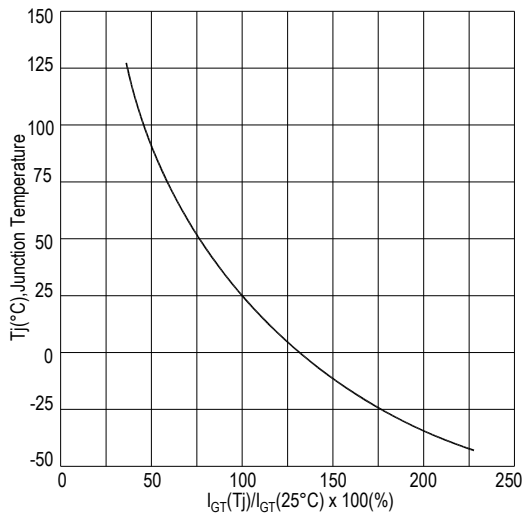


FIG.4: Rated surge on-state current (Non-Repetitive)

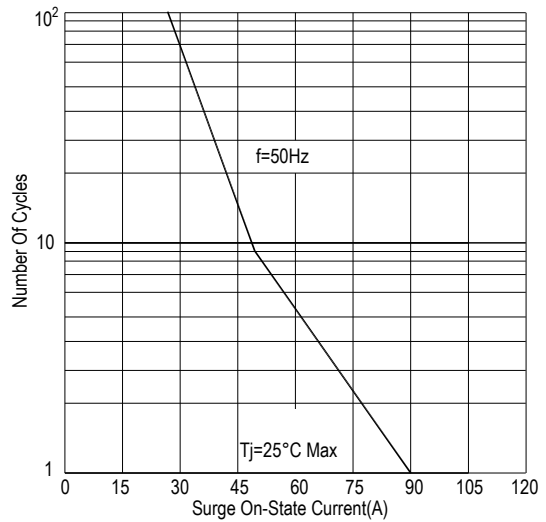


FIG.5: On-state characteristics(Max)

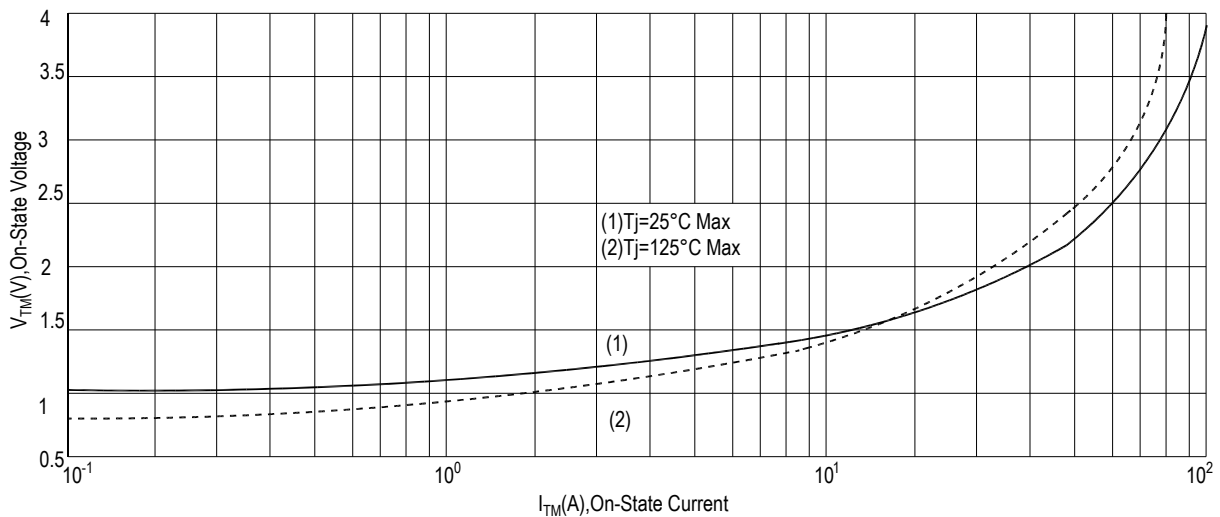


FIG.6: Holding current and Latching current VS Junction temperature

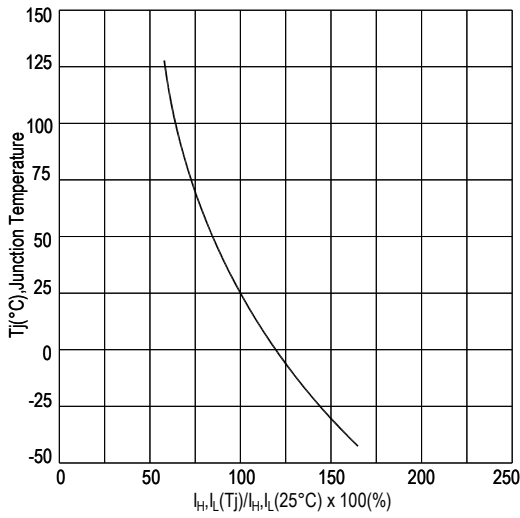


FIG.7: Gate trigger voltage VS Junction temperature

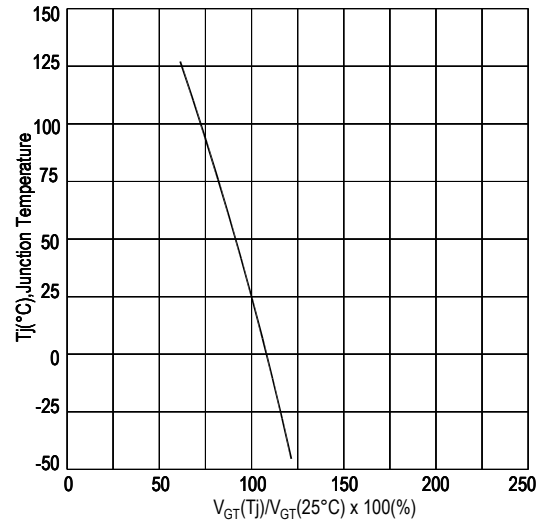


FIG.8: Gate trigger current VS Junction temperature for type T gate triggering

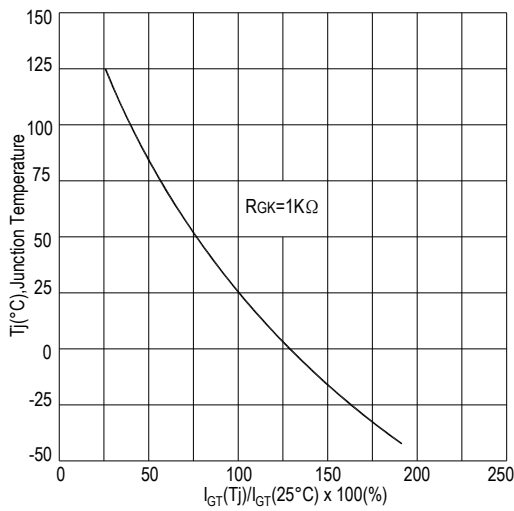
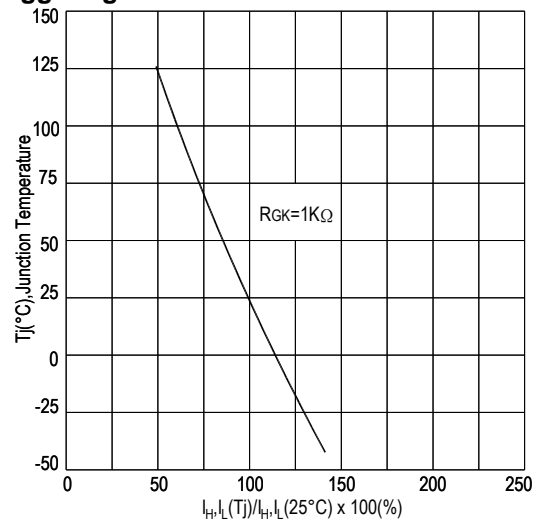
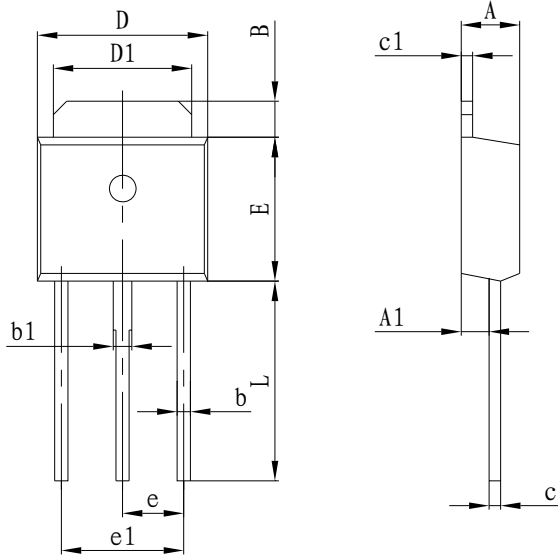


FIG.8: Holding current and Latching current VS Junction temperature for type T gate triggering



PACKAGE MECHANICAL DATA

TO-251 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.200	2.400	0.087	0.094
A1	0.900	1.100	0.035	0.043
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.620	0.017	0.024
c1	0.480	0.620	0.019	0.024
D	6.350	6.700	0.252	0.264
D1	5.100	5.400	0.200	0.213
E	6.000	6.200	0.236	0.244
e	2.300TYP		0.091TYP	
e1	4.500	4.700	0.177	0.185
L	8.900	9.400	0.350	0.370

Making Diagram

ADV:Logo
ADT8A80DS:Part number
X:Internal control code
H:Halogen Free

AD T 8 A 80 # T(S)(W)

ADVANCED Internal control code Current:8=8A SCR Series Voltage:60=600V 80=800V	T(S) W	Sensitivity and type: T=0.2mA S=15mA Blank=30mA W=80mA Package explain:D=TO-251
--	-----------	--

Ordering information

Part number	Package	Marking	Packing	Quantity
ADT8A60D#	TO-251	ADT8A60D#	Tube	80pcs
ADT8A80D#	TO-251	ADT8A80D#	Tube	80pcs

Note:# = Gate Trigger Current Sensitivity and type

Notice

1. All information included in this document such as product data, diagrams, charts, programs, algorithms, and application circuit examples, is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any ADV products listed in this document, please confirm the latest product information with a ADV sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by ADV such as that disclosed through our website. (<http://www.advsemi.com>)
2. ADV has used reasonable care in compiling the information included in this document, but ADV assumes no liability whatsoever for any damages incurred as a result of errors or omissions in the information included in this document.
3. You should use the products described herein within the range specified by ADV, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. ADV shall have no liability for malfunctions or damages arising out of the use of ADV products beyond such specified ranges.
4. When using or otherwise relying on the information in this document, you should evaluate the information in light of the total system before deciding about the applicability of such information to the intended application. ADV makes no representations, warranties or guaranties regarding the suitability of its products for any particular application and specifically disclaims any liability arising out of the application and use of the information in this document or ADV products.
5. Although ADV endeavors to improve the quality and reliability of its products, IC products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a ADV product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other applicable measures. Among others, since the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
6. This document is provided for reference purposes only so that ADV customers may select the appropriate ADV products for their use. ADV neither makes warranties or representations with respect to the accuracy or completeness of the information contained in this document nor grants any license to any intellectual property rights or any other rights of ADV or any third party with respect to the information in this document.
7. ADV shall have no liability for damages or infringement of any intellectual property or other rights arising out of the use of any information in this document, including, but not limited to, product data, diagrams, charts, programs, algorithms, and application circuit examples.
8. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written approval from ADV.