#### **Product Features**

- $\bullet~30\sim2650 MHz$
- High Gain
- High linearity
- SOIC-8 SMD Type package
- Lower manufacturing cost
- -72dBc CSO 79 Channels @ +40dBmV/ch
- -65dBc CTB 79 Channels @ +40dBmV/ch

# Applications

**AE505** 

- Low Noise Amplifier for
- CATV
- Cable Modem
- FTTH (G-PON, GE-PON)
- Optical node
- Repeater
- RF Sub-Systems
- Base Station
- Converter



Package Type : SOIC-8

# Description

AE505 is designed as low cost drive amplifiers for many applications including FTTH, CATV System. This MMIC is based on Gallium Arsenide Enhancement Mode pHEMT which shows low current draw and very low noise. The data in this spec sheet is valid only for 750hm application.

# **Electrical Specifications**

PARAMETER		UNIT	MIN	ТҮР	MAX	CONDITION
Frequency		MHz	30		2650	-
Ga	Gain		12	14	-	30 ~ 1000MHz
Gain F	Gain Flatness		-	1.5	-	30 ~ 1000MHz
Input Re	Input Return Loss		-	-13	-	-
Output R	Output Return Loss		-	-15	-	-
Outp	Output IP3		38.5	41.5	-	@ 500MHz/10dBm 2tone
1dB Compr	1dB Compression Point		21.5	24.5	-	@500MHz
Noise	Noise Figure			3.4	4	@500MHz
CSO	30 ~ 1004MHz	dBc		-72		70 shamal Elat +40 dDmV/sh
СТВ	50 ~ 1004MHZ	dBc	-	-65	-	79 channel, Flat, +40dBmV/ch
DC Current		mA	-	220	-	Vdd = 5.0V

Note

1. Test conditions unless otherwise noted. Test Freq = 500MHz, T=25  $^{\circ}$ C, Vdd=5V, 75 $\Omega$  system

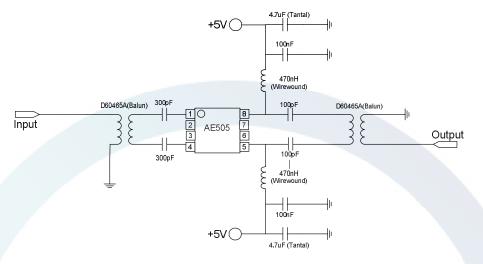
2. OIP3 measured with 2 tones at an output power of +10dBm/tone separated by 1MHz, Test Freq = 500MHz

# **Absolute Maximum Ratings**

PARAMETER	UNIT	MIN	ТҮР	MAX
Device Voltage	VDC		5	12
Operating Temperature	C	-40	-	85
Storage Temperature	°C	-40	-	150
ESD Human Body Model	-	-	Class 1A	-
Moisture Sensitivity Level	-	-	MSL1	-
Junction Temperature (Tj)	C	-	-	180
Thermal Resistance (Rth)	°C/w	-	35	-

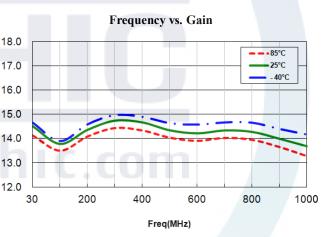
# **RFHIC**

Application Circuit @ 30 ~ 1000MHz, 75ohm System, VDD=5V

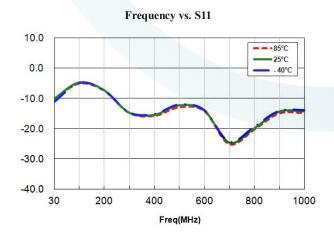


#### Typical Performance @ VDD=5V, IDS=220mA, T=25 °C, 75ohm System

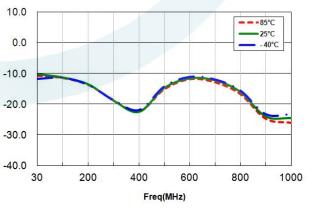
PARAMETER	UNIT	TYPICAL		
Frequency	MHz	30	500	1000
Gain(S21)	dB	14.8	14.3	13.6
Input Return Loss(S11)	dB	-10	-12	-14
Output Return Loss(S22)	dB	-10	-14.6	-24
Output IP3	dBm	39.3	41.5	36
1dB Compression Point	dBm	20.6	24.5	22.2
Noise Figure	dB	3.3	3.4	3.6
CSO*	dBc	-72		
CTB*	dBc	-65		
Current	mA	220		



\* 79channels\_Flat, +40dBmV



Frequency vs. S22



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200

50.0

45.0

40.0

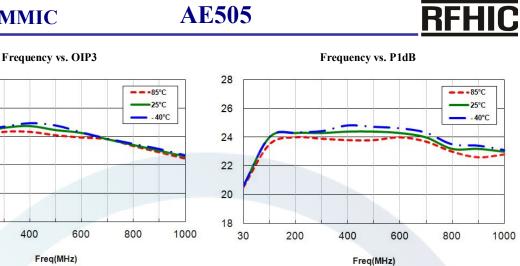
35.0

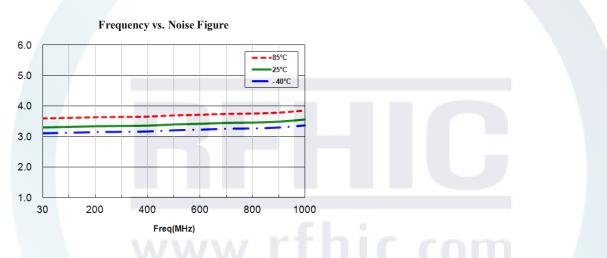
30.0

25.0

30

**AE505** 



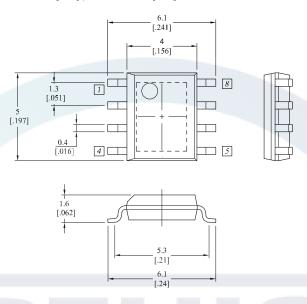


### Multi-Tone Test: 79CH\_FLAT@Output Power +40dBmV/Ch

			Leve	el: 40dBmV	Tilt: 79CH	_flat			
	СТВ	СТВ	N-	CSU	CSU	CSU	CSL	CSL	CSL
FRQ	RAW	COR	FLR	RAW	COR	FRQ	RAW	COR	FRQ
55.25	67	67	85.8	82.5	85.6	56	72.3	72.6	54
211.25	65	65	85.8	80.5	82.2	212.5	76	76.5	209.99
331.25	65.1	65.1	83.6	76	76.8	332.5	74.7	75.4	329.99
445.25	65.2	65.2	84.6	73.4	73.8	446.49	77.2	78	443.99
547.25	66.1	66.1	83.2	72	72	548.49	80.9	85.3	546.49
Min	65	65	83.2	72	72	56	72.3	72.6	54
Max	67	67	85.8	82.5	85.6	548.49	80.9	85.3	546.49

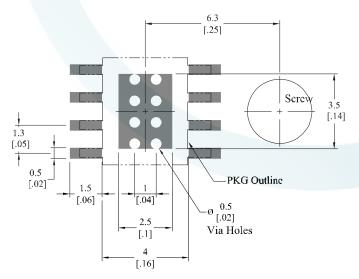
# Package Dimensions (Type: SOIC-8)

\* Unit: mm[inch] | Tolerance ±0.2[.008]



Pin Description							
Pin No	Function	Pin No	Function				
1	RF IN(2)	5	RF OUT(1)				
2	GND	6	GND				
3	GND	7	GND				
4	RF IN(1)	8	RF OUT(2)				
	× /						

#### **Recommended Pattern**



#### **Mounting Configuration Notes**

- 1. Ground / thermal via holes are critical for the proper performance of this device.
- 2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.

**RFHIC** 

- Mounting screws can be added near the part to fasten the board to a heat sink. Ensure that the ground / thermal via hole region contacts the heat sink.
- 4. Do not put solder mask on the backside of the PCB in the region where the board contacts the heat sink.
- 5. RF trace width depends upon the PCB material and construction.
- 6. Use 1 oz. Copper minimum.



#### **Revision History**

Part Number	Release Date	Version	Modification	Data Sheet Status
AE505	20140721	1.0	Typical performance(2p)	-
AE505	20140519	0.1	Initial Release of Data sheet	Preliminary



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