Alfa-MOS Technology

AFC5521
100V N & P Pair Enhancement Mode MOSFET

General Description

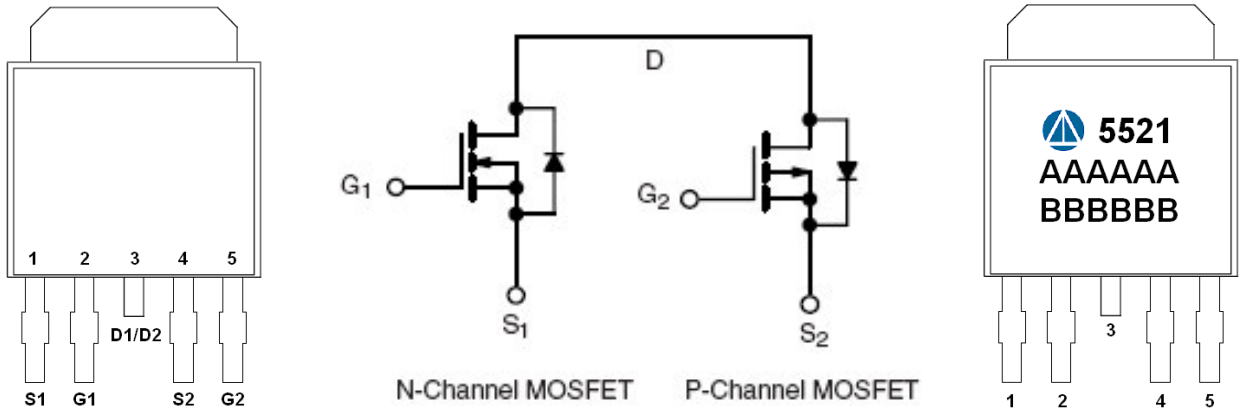
AFC5521, N & P Pair enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent RDS(ON), low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

N-Channel
- 100V/5.0A,$R_{DS(ON)}$= 110mΩ@V_{GS}=10V
- 100V/3.0A,$R_{DS(ON)}$= 120mΩ@V_{GS}=4.5V

P-Channel
- -100V/-5.0A,$R_{DS(ON)}$= 190mΩ@V_{GS}= -10V
- -100V/-3.0A,$R_{DS(ON)}$= 200mΩ@V_{GS}= -4.5V

Pin Description (TO-252-4L)



Application

- DC/DC Conversion
- Load Switch
- DC FAN

Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D1 / D2	Drain 1 / Drain 2
4	S2	Source 2
5	G2	Gate 2

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFC5521T254RG	5521	TO-252-4L	Tape & Reel	2500 EA

※ A Lot code
※ B Date code
※ AFC5521T254RG : 13" Tape & Reel ; Pb- Free ; Halogen- Free

Absolute Maximum Ratings (N-Channel)

(T_A=25℃ Unless otherwise noted)

Parameter		Symbol	Typical	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate –Source Voltage		V_{GSS}	±20	V
Continuous Drain Current(T_J=150℃)	T_c=25℃	I_D	5	A
	T_c=70℃		3	
Pulsed Drain Current		I_{DM}	8	
Continuous Source Current(Diode Conduction)		I_S	2	
Power Dissipation	T_A=25℃	P_D	2.8	W
	T_A=70℃		1.8	
Operating Junction Temperature		T_J	150	℃
Storage Temperature Range		T_{STG}	-55/150	℃
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	62.5	℃/W

Electrical Characteristics (N-Channel)

(T_A=25℃ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V,I_D=250uA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	1.0	1.8	2.5	
Gate Leakage Current	I_{GSS}	V_{DS}=0V,V_{GS}=±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V,V_{GS}=0V			1	uA
		V_{DS}=80V,V_{GS}=0V T_J=85℃			5	
On-State Drain Current	$I_{D(on)}$	V_{DS}≧5V,V_{GS}=10V	8			A
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=5.0A		86	110	mΩ
		V_{GS}=4.5V,I_D=3.0A		90	120	
Forward Transconductance	g_{FS}	V_{DS}=10V,I_D=3A		12		S
Diode Forward Voltage	V_{SD}	I_S=3A,V_{GS}=0V		0.8	1.2	V
Dynamic						
Total Gate Charge	Q_g	V_{DS}=50V,V_{GS}=10V I_D≡4.5A		10	15	nC
Gate-Source Charge	Q_{gs}			1.7		
Gate-Drain Charge	Q_{gd}			2		
Input Capacitance	C_{iss}	V_{DS}=50V,V_{GS}=0V f=1MHz		415		pF
Output Capacitance	C_{oss}			40		
Reverse Transfer Capacitance	C_{rss}			20		
Turn-On Time	$t_{d(on)}$	V_{DD}=50V,R_L=23.8Ω I_D≡2.1A,V_{GEN}=10V R_G=1.0Ω		10	15	ns
	t_r			10	15	
Turn-Off Time	$t_{d(off)}$			12	20	
	t_f			10	15	

Absolute Maximum Ratings (P-Channel)

(T_A=25℃ Unless otherwise noted)

Parameter		Symbol	Typical	Unit
Drain-Source Voltage		V_{DSS}	-100	V
Gate –Source Voltage		V_{GSS}	±20	V
Continuous Drain Current(T_J=150℃)	T_A=25℃	I_D	-5	A
	T_A=70℃		-3	
Pulsed Drain Current		I_{DM}	-8	
Continuous Source-Drain Diode Current		I_S	-2	
Power Dissipation	T_A=25℃	P_D	2.8	W
	T_A=70℃		1.8	
Operating Junction Temperature		T_J	150	℃
Storage Temperature Range		T_{STG}	-55/150	℃
Thermal Resistance-Junction to Ambient		$R_{\theta JA}$	62.5	℃/W

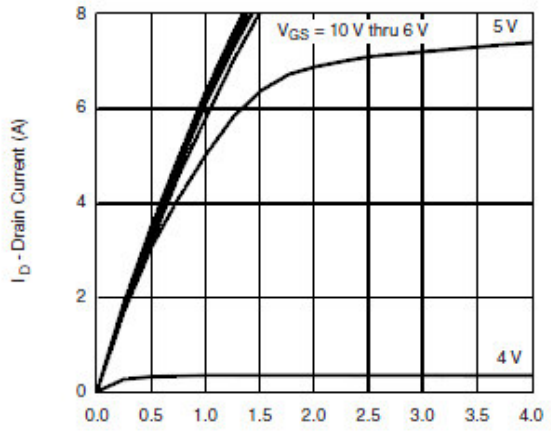
.

Electrical Characteristics (P-Channel)

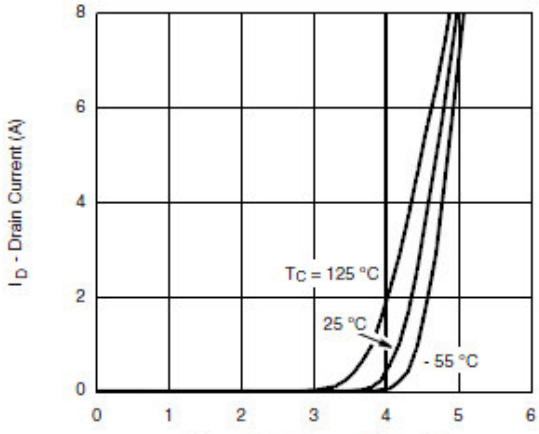
(T_A=25℃ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V,I_D= -250uA	-100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D= -250uA	-1.0		-2.5	
Gate Leakage Current	I_{GSS}	V_{DS}=0V,V_{GS}= ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}= -80V,V_{GS}=0V			-1	uA
		V_{DS}= -80V,V_{GS}=0V T_J=85℃			-30	
On-State Drain Current	$I_{D(on)}$	V_{DS}≧ -10V,V_{GS}= -10V	-18			A
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = -10V,I_D=-5.0A		167	190	mΩ
		V_{GS} = -4.5V,I_D=-3.0A		177	200	
Forward Transconductance	g_{FS}	V_{DS}= -15V,I_D= -3.2A		12		S
Diode Forward Voltage	V_{SD}	I_S= -2A,V_{GS}=0V		-0.8	-1.3	V
Dynamic						
Total Gate Charge	Q_g	V_{DS}=-50V,V_{GS}=-4.5V I_D= -4.0A		12	20	nC
Gate-Source Charge	Q_{gs}			3.0		
Gate-Drain Charge	Q_{gd}			4.5		
Input Capacitance	C_{iss}	V_{DS}=-50V,V_{GS}=0V f=1MHz		1100		pF
Output Capacitance	C_{oss}			70		
Reverse Transfer Capacitance	C_{rss}			45		
Turn-On Time	$t_{d(on)}$	V_{DD}=-50V,R_L=17Ω I_D≡-2.8A,V_{GEN}=-10V R_G=1Ω		8	15	ns
	t_r			15	20	
Turn-Off Time	$t_{d(off)}$			35	50	
	t_f			10	25	

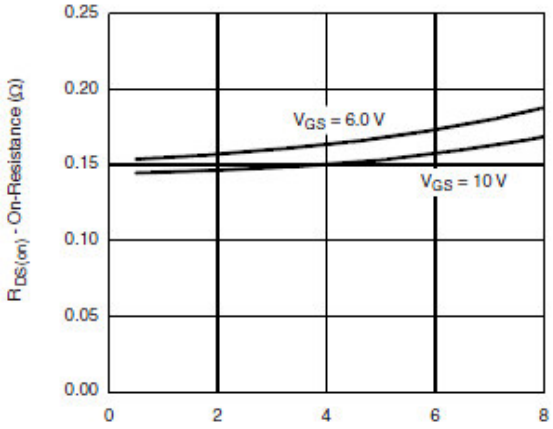
Typical Characteristics (N-Channel)



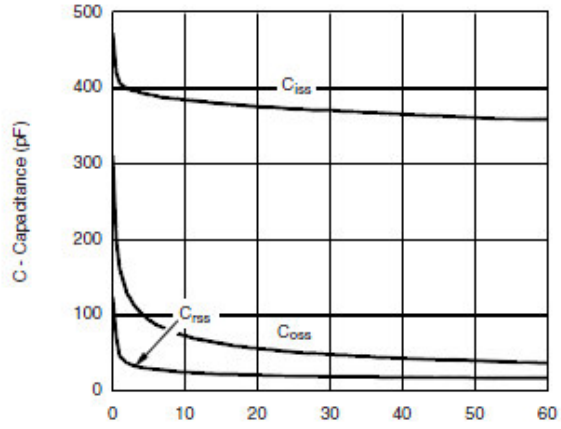
Output Characteristics



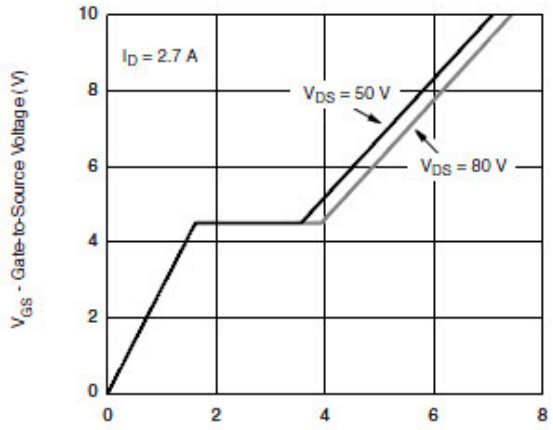
Transfer Characteristics



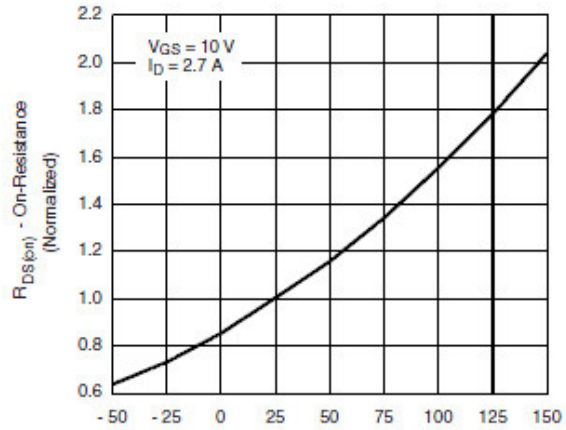
On-Resistance vs. Drain Current



Capacitance

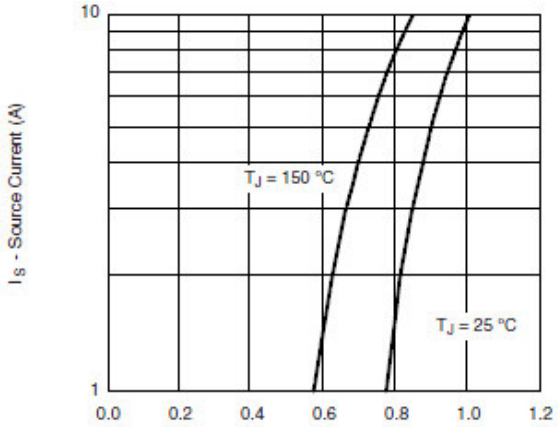


Gate Charge

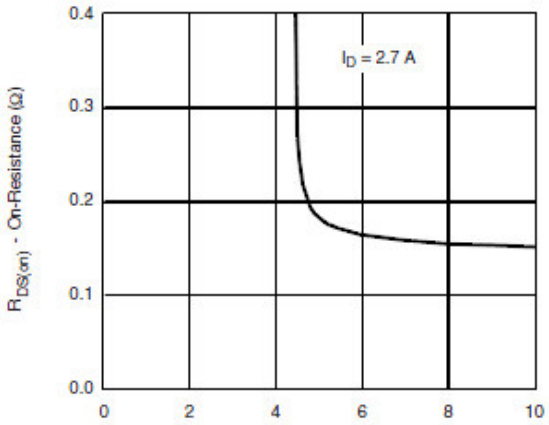


On-Resistance vs. Junction Temperature

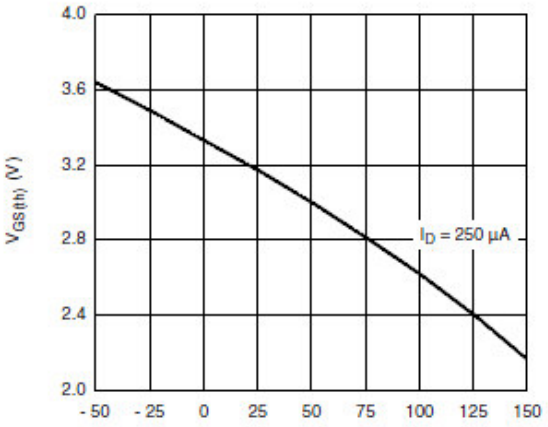
Typical Characteristics (N-Channel)



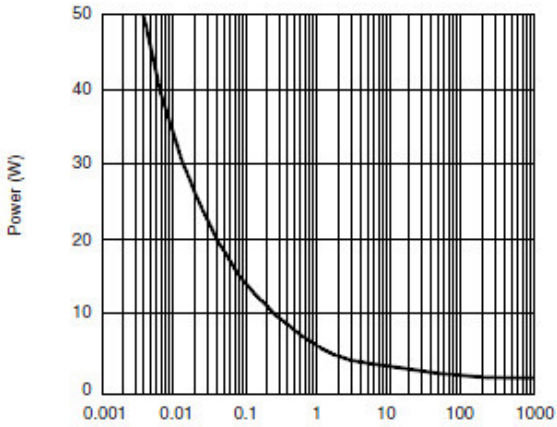
Source-Drain Diode Forward Voltage



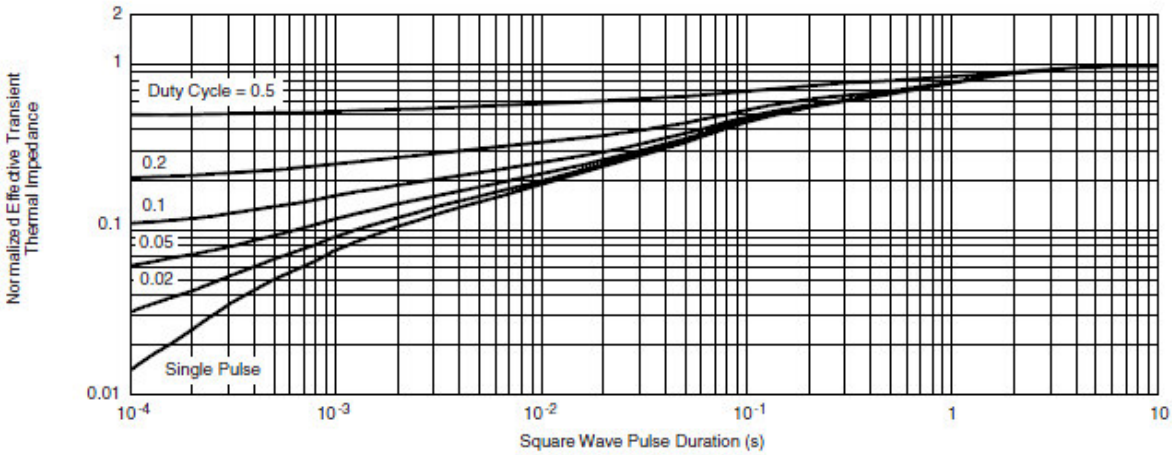
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

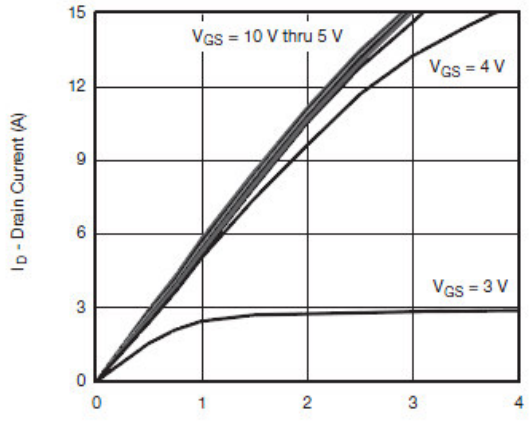


Single Pulse Power

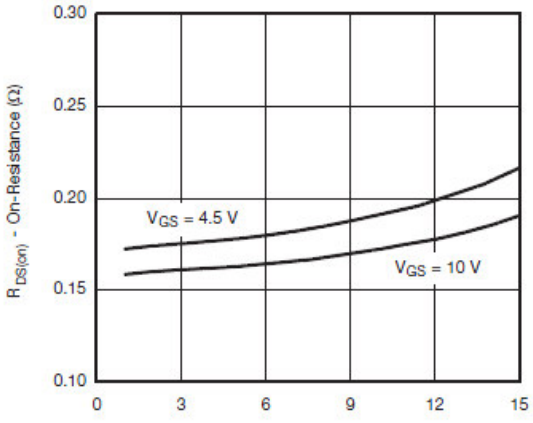


Normalized Thermal Transient Impedance, Junction-to-Foot

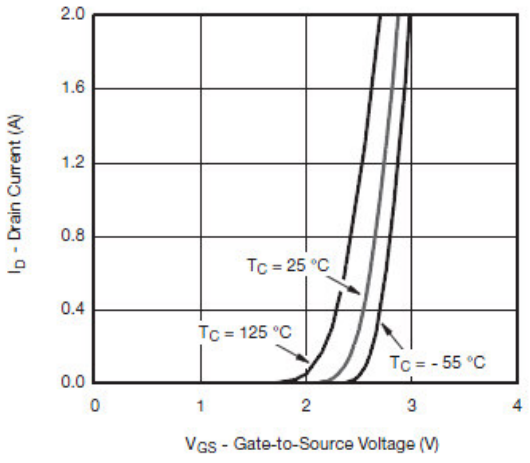
Typical Characteristics (P-Channel)



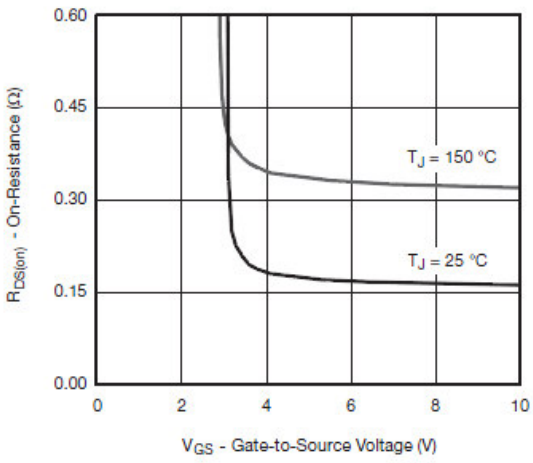
Output Characteristics



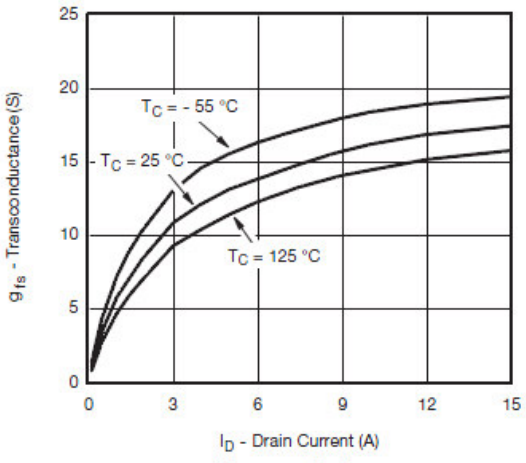
On-Resistance vs. Drain Current



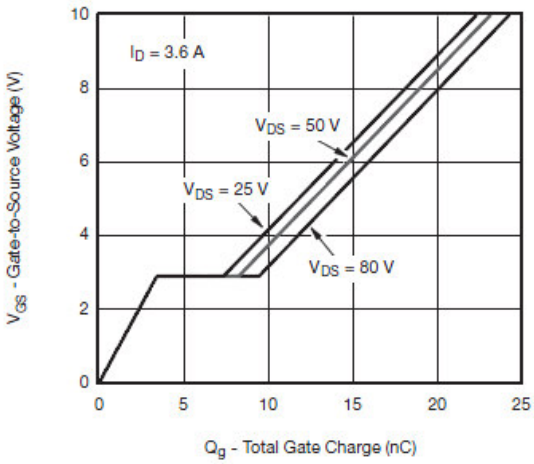
Transfer Characteristics



On-Resistance vs. Gate-to-Source Voltage

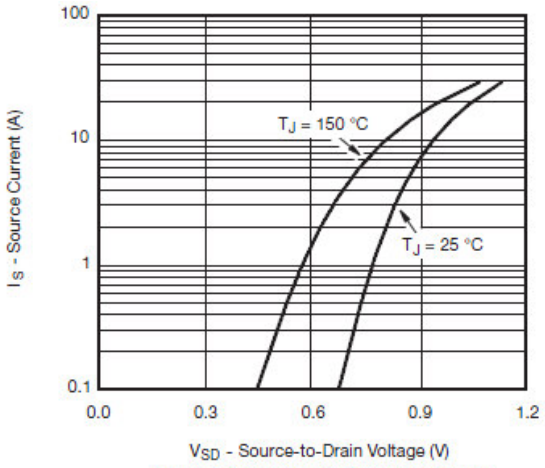


Transconductance

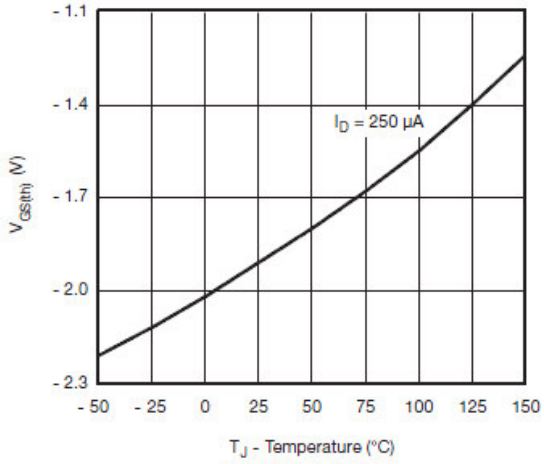


Gate Charge

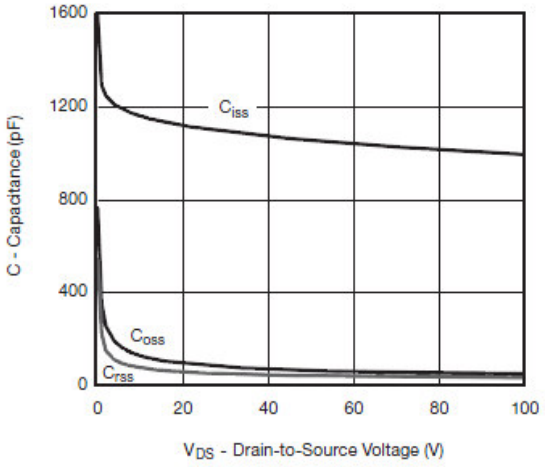
Typical Characteristics (P-Channel)



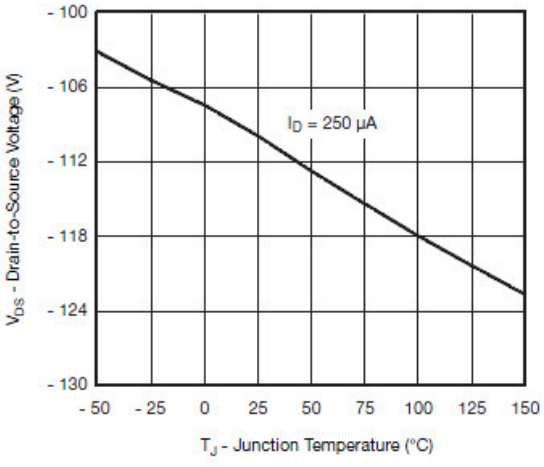
Source-Drain Diode Forward Voltage



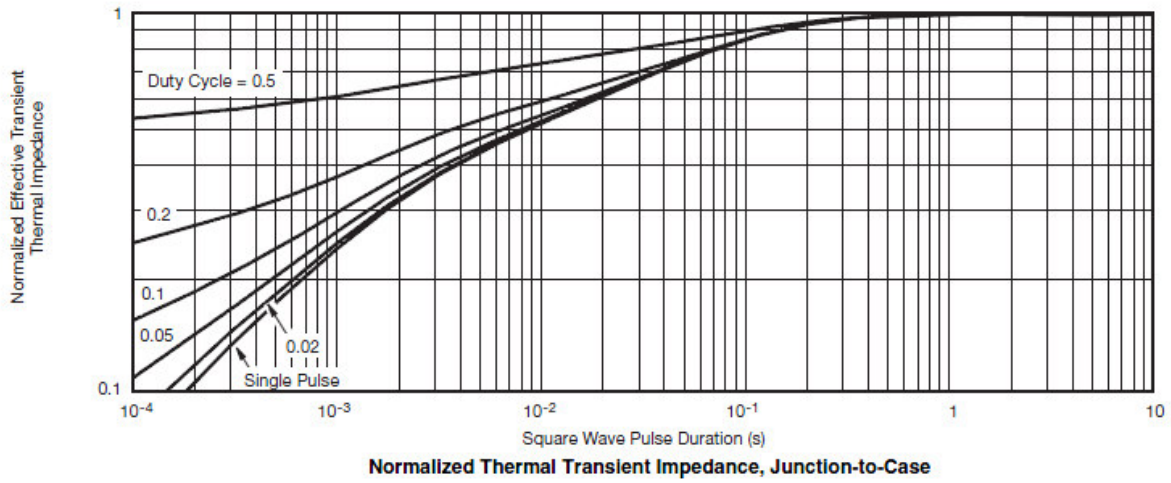
Threshold Voltage



Capacitance



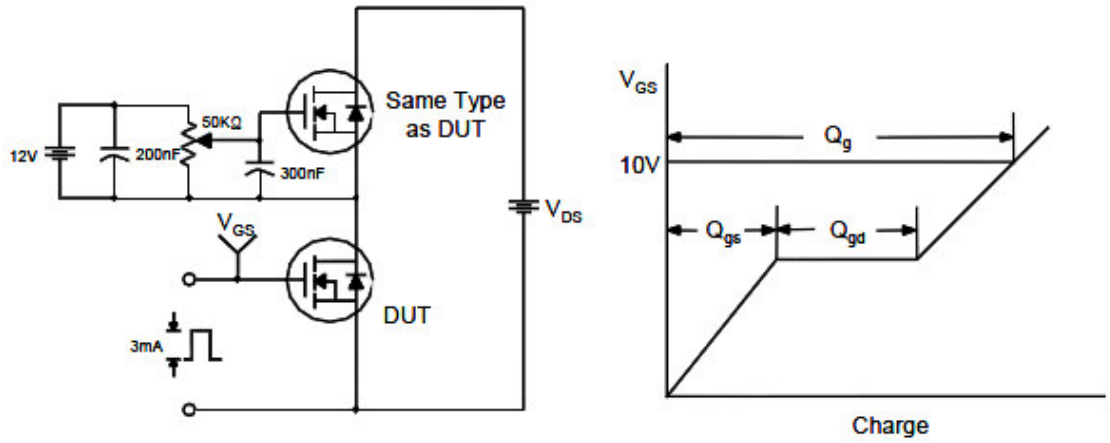
Drain Source Breakdown vs. Junction Temperature



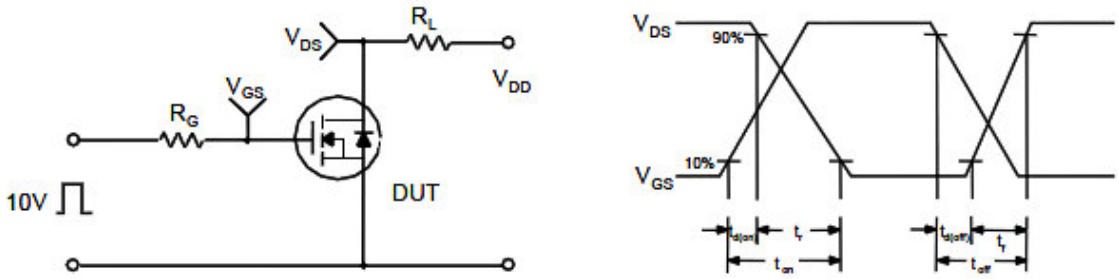
Normalized Thermal Transient Impedance, Junction-to-Case

Typical Characteristics

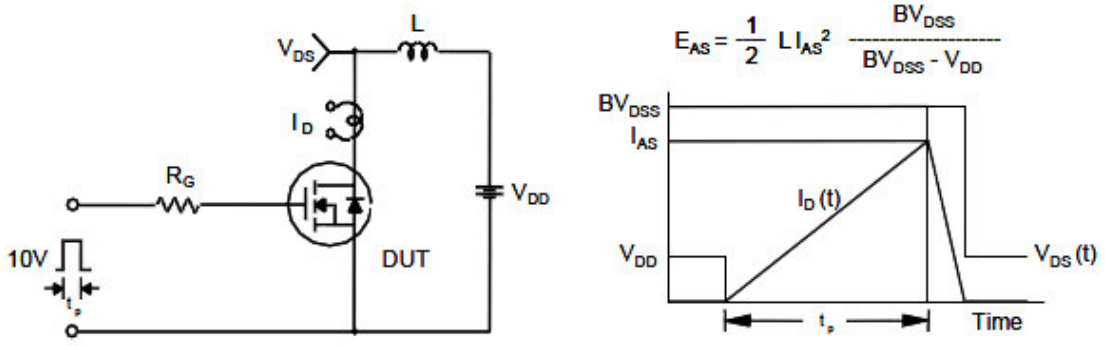
Gate Charge Test Circuit & Waveform



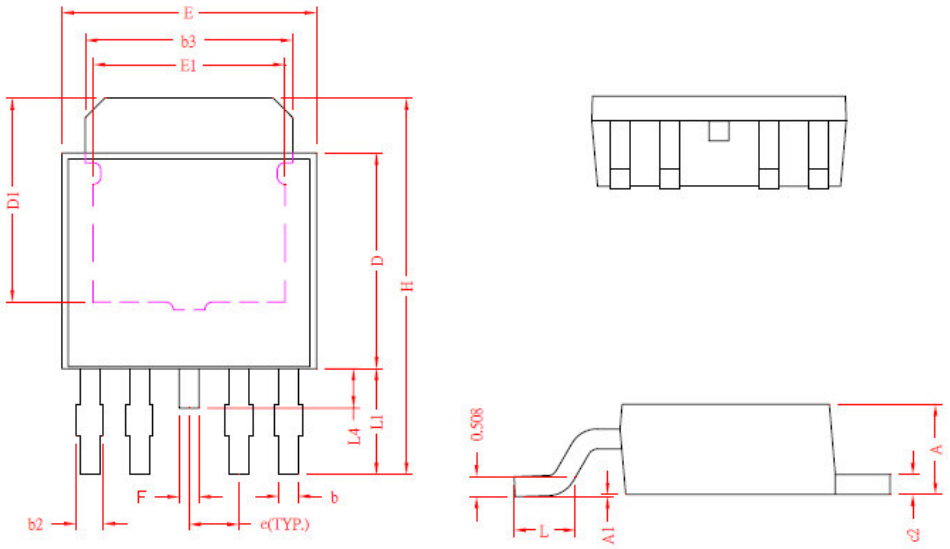
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Package Information (TO-252-4L)



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.20	2.40	E	6.40	6.80
A1	0	0.15	E1	3.81	---
b	0.40	0.60	e	1.27 REF.	
b2	0.50	0.80	F	0.40	0.60
b3	5.20	5.50	H	9.40	10.20
c2	0.45	0.55	L	1.40	1.77
D	5.40	5.80	L1	2.40	3.00
D1	4.57	---	L4	0.80	1.20

2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)
Tel : 886 2) 2651 3928
Fax : 886 2) 2786 8483
©http://www.alfa-mos.com