

2-Line ESD Protection Low Capacitance Unidirectional TVS

General Description

AFE1212A are designed by unidirectional TVS diode, to protect high speed data interfaces. This product has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients). The TVS diode prevents over-voltage on the power line, protecting any downstream components. This device is optimized for ESD protection of portable electronics. They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge).

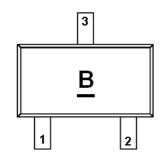
Features

- Transient protection for high-speed data lines to IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- Small package saves board space
- Protects up to four I/O lines & power line
- Low leakage current and clamping voltage
- Low operating voltage: 12V
- Solid-state silicon-avalanche technology

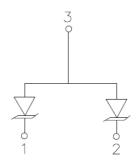
Application

- Monitors and Flat Panel Displays
- Cellular Handsets and Accessories
- Cordless Phone
- PDA
- Notebooks and Handhelds
- Portable Instrumentation
- Digital Cameras
- MP3 Player

Pin Description (SOT-523)



Schematic & PIN Configuration(SOT-523)



Ordering Information

| Part Ordering No. | Part Marking | Package | Unit | Quantity |
|-------------------|--------------|---------|-------------|----------|
| AFE1212AS52RG | <u>B</u> | SOT-523 | Tape & Reel | 3000 EA |

AFE1212AS52RG: 7" Tape & Reel; Pb- Free; Halogen- Free

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ABSOULTE MAXIMUM RATINGS

(T_A=25°C Unless otherwise noted)

| Parameter | Symbol | Typical | Unit |
|---|-----------------|---------------|-------------------------|
| Peak Pulse Power (t _p = 8/20 μs) | P _{pk} | 250 | W |
| Maximum Peak Pulse Current ($t_p = 8/20 \mu s$) | l _{PP} | 8 | Α |
| ESD per IEC 61000 – 4 – 2 (Air) | V _{PP} | ±15 | KV |
| ESD per IEC 61000 – 4 – 2 (Contact) | V _{PP} | ±8 | KV |
| Operating Junction Temperature | TJ | -55 ~ 125 | $^{\circ}\!\mathbb{C}$ |
| Storage Temperature Range | Tstg | -55 ~ 150 | $^{\circ}\!\mathbb{C}$ |
| Lead Soldering Temperature | T∟ | 260 (10sec) | $^{\circ}\! \mathbb{C}$ |

ELECTRICAL CHARACTERISTICS

(Ta=25°C Unless otherwise noted)

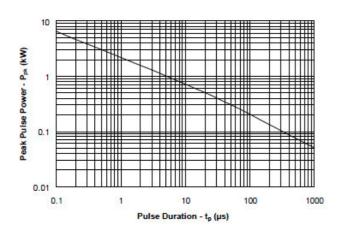
| Parameter | Symbol | Conditions | Min. | Тур | Max. | Unit |
|-----------------------------|------------------|---|------|-----|------|------|
| Reverse Stand – Off Voltage | V _{RWM} | Pin 1 to 2 or Pin 2 to 1 | | | 12 | V |
| Reverse Breakdown Voltage | V _{BR} | It = 1mA Pin 1 to 2 or Pin 2 to 1 | 13.3 | | | ٧ |
| Reverse Leakage Current | IR | V _{RWM} = 12V , T=25°C Pin 1 to 2 or Pin 2 to 1 | | | 1.0 | μA |
| Clamping Voltage | Vc | I _{PP} = 1A , tp = 8/20 μs Pin 1 to 2 or 2 to 1 | | | 19 | ٧ |
| Clamping Voltage | Vc | I _{PP} = 8A , tp = 8/20 μs Pin 1 to 2 or 2 to 1 | | | 25 | ٧ |
| Junction Capacitance | Cj | V _R = 0V , f = 1MHz | | 16 | 20 | рF |



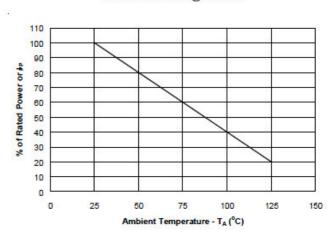
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Typical Characteristics

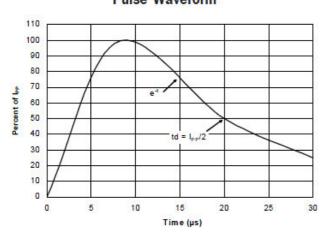
Non-Repetitive Peak Pulse Power vs. Pulse Time



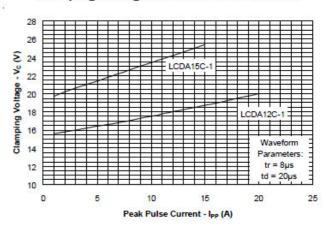
Power Derating Curve



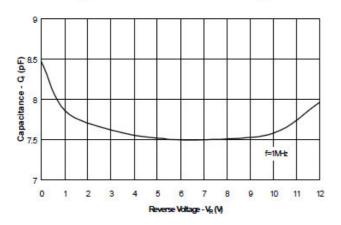
Pulse Waveform



Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage





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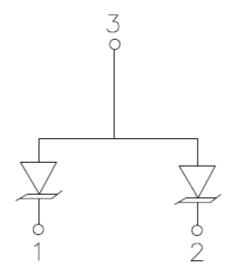
Application Information

Device Connection Options

Device Connection for Protection of Two Data Lines

AFE1212A is designed to protect up to two unidirectional data lines. The device is connected as follows:

1. Unidirectional protection of five I/O lines is achieved by connecting pins 1 and 2 to the data lines. Pin 3 is connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.



Circuit Board Layout Recommendations for Suppression of ESD.

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

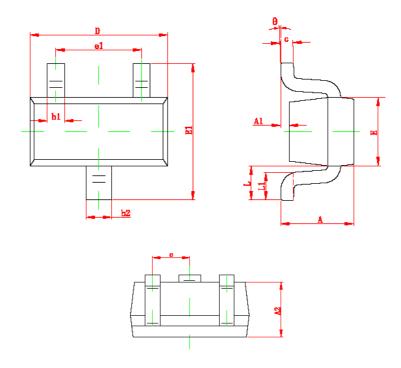
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Package Information (SOT-523)



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | | |
|--------|---------------------------|-------|----------------------|-------|--|
| | Min | Max | Min | Max | |
| Α | 0.700 | 0.900 | 0.028 | 0.035 | |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 | |
| A2 | 0.700 | 0.800 | 0.028 | 0.031 | |
| b1 | 0.150 | 0.250 | 0.006 | 0.010 | |
| b2 | 0.250 | 0.325 | 0.010 | 0.013 | |
| С | 0.100 | 0.200 | 0.004 | 0.008 | |
| D | 1.500 | 1.700 | 0.059 | 0.067 | |
| E | 0.750 | 0.850 | 0.030 | 0.033 | |
| E1 | 1.450 | 1.750 | 0.057 | 0.069 | |
| e | 0.500 TYP | | 0.020 TYP | | |
| e1 | 0.900 | 1.100 | 0.035 | 0.043 | |
| L | 0.550 REF | | 0.022 REF | | |
| L1 | 0.280 | 0.440 | 0.011 | 0.017 | |
| θ | 0° | 4° | 0° | 4° | |

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