



**Alfa-MOS  
Technology**

**AFN2920W  
20V N-Channel  
Enhancement Mode MOSFET**

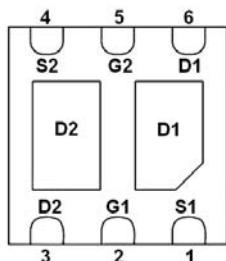
## General Description

AFN2920W, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

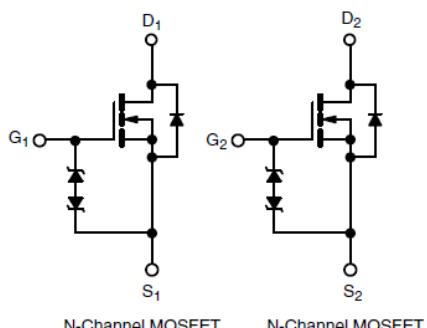
## Features

- 20V/5.0A,  $R_{DS(ON)}=19m\Omega @ V_{GS}=4.5V$
- 20V/4.6A,  $R_{DS(ON)}=23m\Omega @ V_{GS}=2.5V$
- 20V/4.2A,  $R_{DS(ON)}=34m\Omega @ V_{GS}=1.8V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- ESD protection
- DFN2X2-6L package design

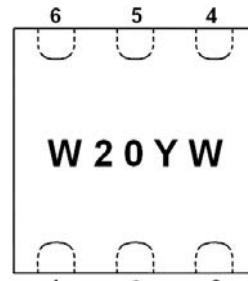
## Pin Description ( DFN2X2-6L )



BOTTOM VIEW



N-Channel MOSFET



TOP VIEW

## Application

- Load Switch with Low Voltage Drop
- Load Switch for 1.2 V/1.5 V/1.8 V Power Lines
- Smart Phones, Tablet PCs, Portable Media Players

## Pin Define

Pin	Symbol	Description
1	S1	Source1
2	G1	Gate1
3	D2	Drain2
4	S2	Source2
5	G2	Gate2
6	D1	Drain1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN2920WFN226RG	W20YW	DFN2X2-6L	Tape & Reel	4000 EA

※ W20 parts code

※ Y year code

※ W week code ( A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52 )

※ AFN2920WFN226RG : 7" Tape & Reel ; Pb- Free ; Halogen- Free



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### Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	$V_{DSS}$	20	V
Gate –Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	4.5	A
		4.5	
Pulsed Drain Current	$I_{DM}$	20	A
Continuous Source Current(Diode Conduction)	$I_S$	1.6	A
Power Dissipation	$P_D$	7.8	W
		5.0	
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	52	$^\circ\text{C}/\text{W}$
Thermal Resistance-Junction to Case(Drain)	$R_{\theta JC}$	12.5	

### Electrical Characteristics

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

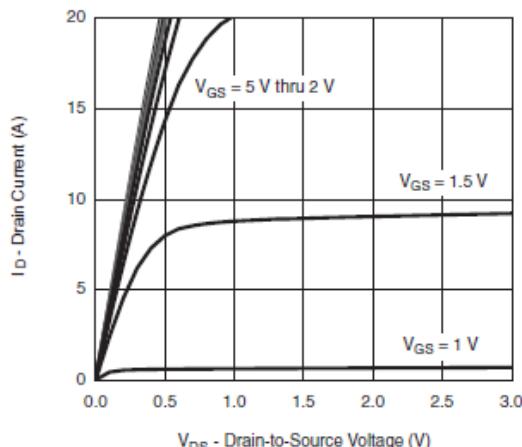
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.4		1.0	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 10$	$\mu\text{A}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=16\text{V}, V_{GS}=0\text{V}$			1	$\mu\text{A}$
		$V_{DS}=16\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5\text{V}, V_{GS}=4.5\text{V}$	15			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5\text{V}, I_D=5.0\text{A}$		15	19	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=4.6\text{A}$		18	23	
		$V_{GS}=1.8\text{V}, I_D=4.2\text{A}$		27	34	
Forward Transconductance	$g_{FS}$	$V_{DS}=6\text{V}, I_D=5\text{A}$		28		S
Diode Forward Voltage	$V_{SD}$	$I_S=1.5\text{A}, V_{GS}=0\text{V}$		0.85	1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=6\text{V}, V_{GS}=4.5\text{V}$ $I_D=5.0\text{A}$		6.0	12	nC
Gate-Source Charge	$Q_{gs}$			0.8		
Gate-Drain Charge	$Q_{gd}$			0.8		
Input Capacitance	$C_{iss}$	$V_{DS}=6\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		620		pF
Output Capacitance	$C_{oss}$			180		
Reverse Transfer Capacitance	$C_{rss}$			100		
Turn-On Time	$t_{d(on)}$	$V_{DD}=10\text{V}, R_L=5.5\Omega$ $I_D=3.6\text{A}, V_{GEN}=4.5\text{V}$		10	20	ns
	$t_r$			10	20	
Turn-Off Time	$t_{d(off)}$			25	40	
	$t_f$			10	20	



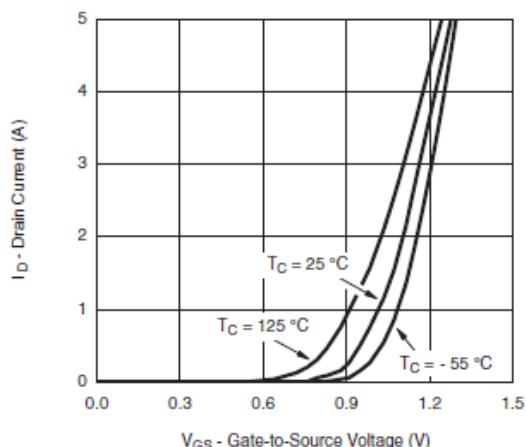
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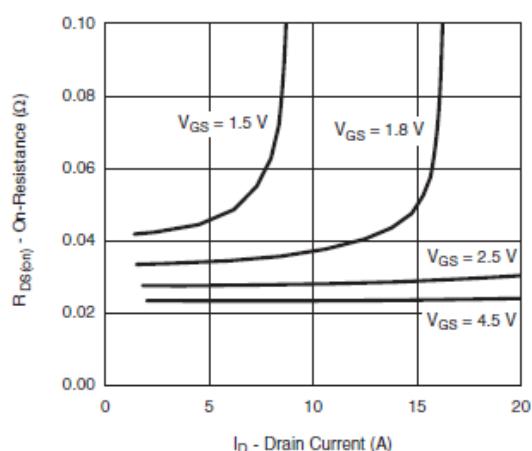
## Typical Characteristics



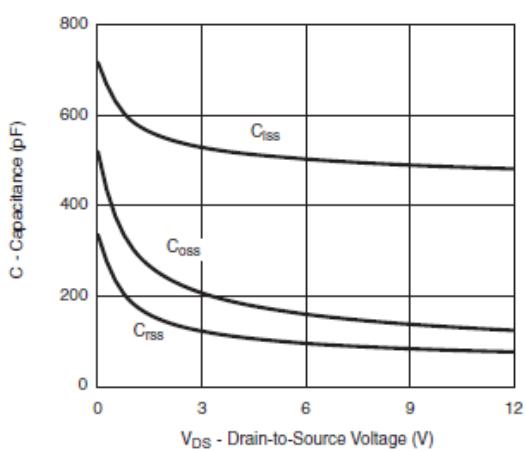
Output Characteristics



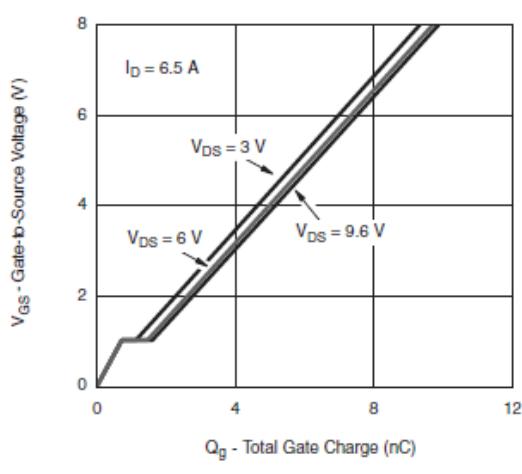
Transfer Characteristics



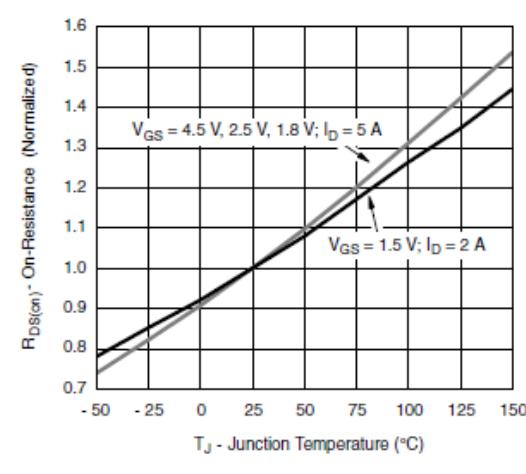
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge



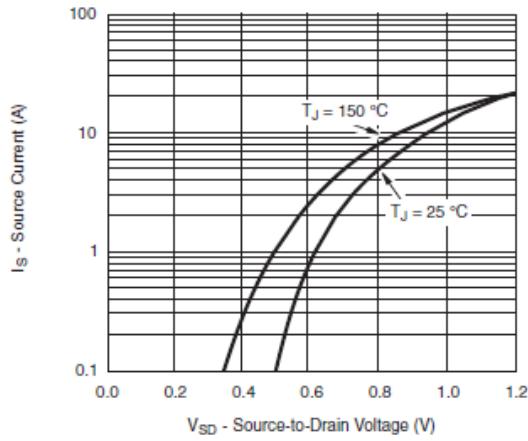
On-Resistance vs. Junction Temperature



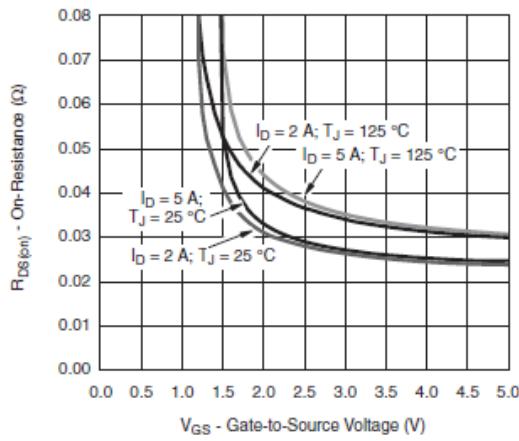
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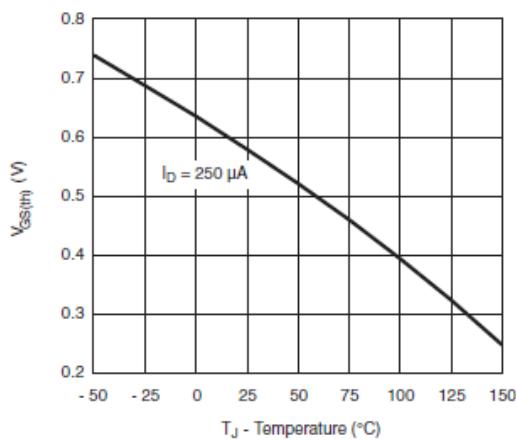
### Typical Characteristics



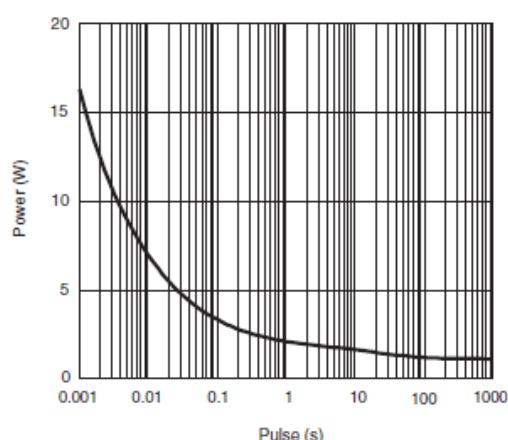
Source-Drain Diode Forward Voltage



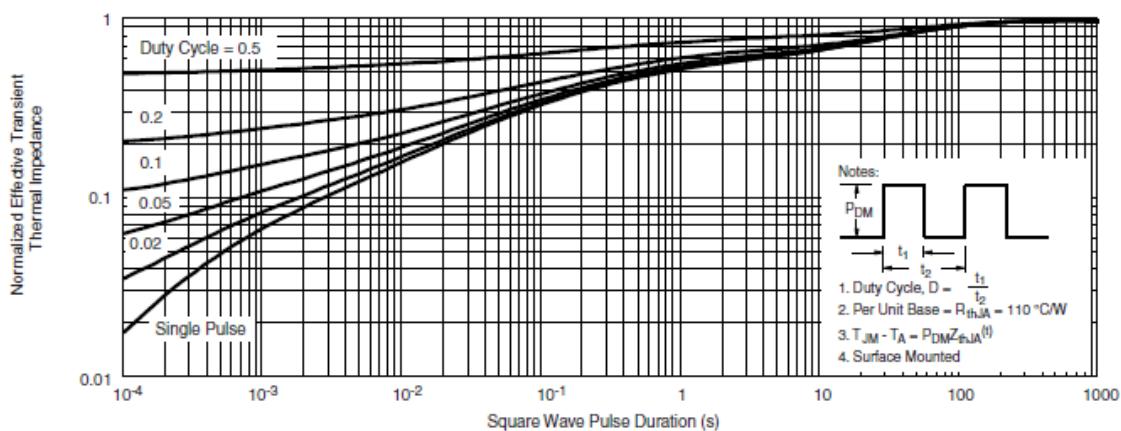
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power (Junction-to-Ambient)



Normalized Thermal Transient Impedance, Junction-to-Ambient

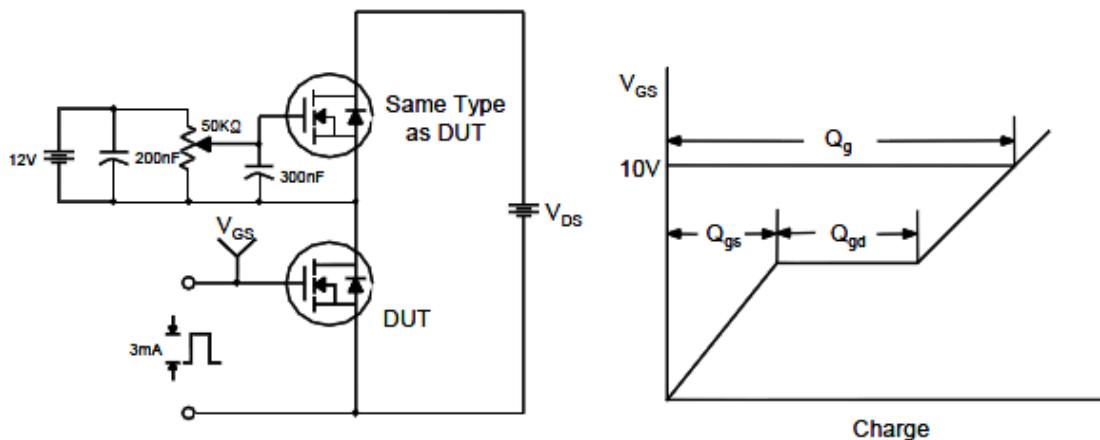


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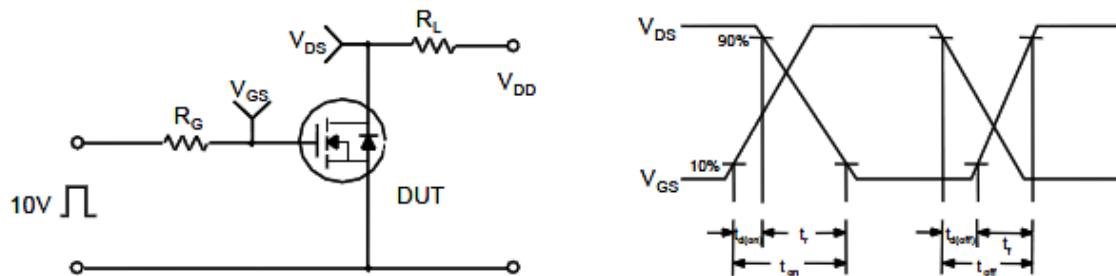
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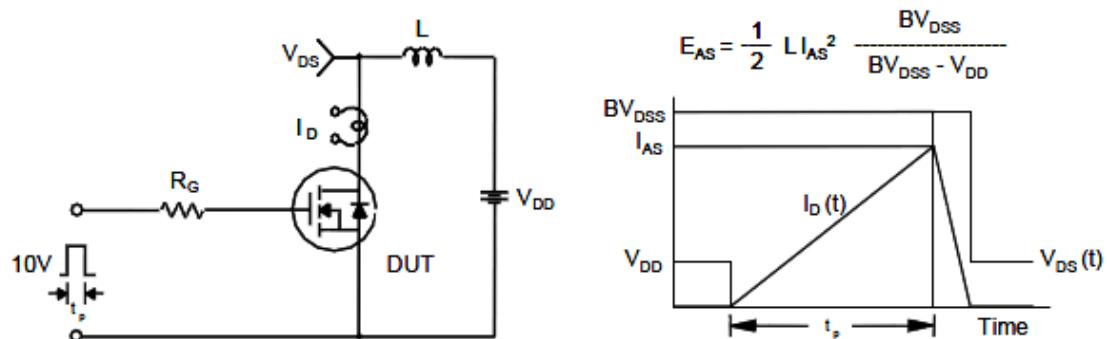
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

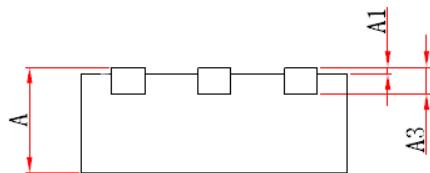
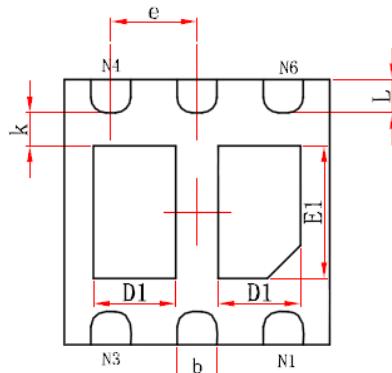
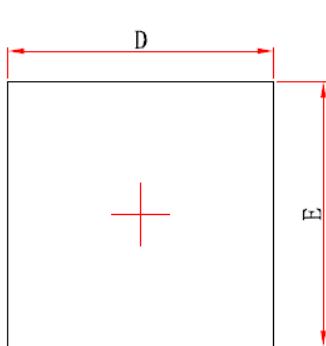




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**Package Information ( DFN2X2-6L )**



**Top View**

**Bottom View**

**Side View**

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	1.924	2.076	0.076	0.082
E	1.924	2.076	0.076	0.082
D1	0.520	0.720	0.020	0.028
E1	0.900	1.100	0.035	0.043
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
L	0.174	0.326	0.007	0.013

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