



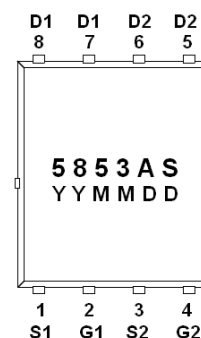
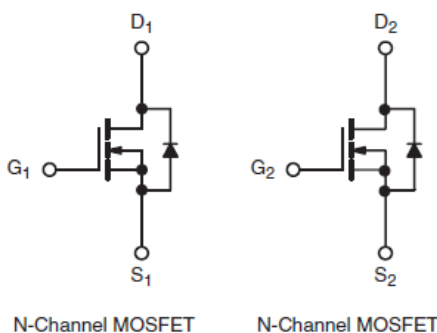
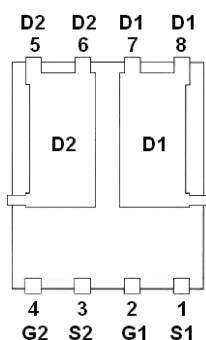
General Description

AFN5853AS, Dual N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge. These devices are particularly suited for low voltage power management, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- 40V/18A, $R_{DS(ON)}=9m\Omega@V_{GS}=10V$
- 40V/15A, $R_{DS(ON)}=11m\Omega@V_{GS}=4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- DFN 5X6-8L package design

Pin Description (DFN5X6-8L)



Application

- POL
- DC/DC

Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN5853ASFN568RG	5853AS	DFN 5X6-8L	Tape & Reel	2500 EA

※ 5853AS : Parts Code

※ YYMMDD : Date Code

※ AFN5853ASFN568RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings (T_A=25°C Unless otherwise noted)

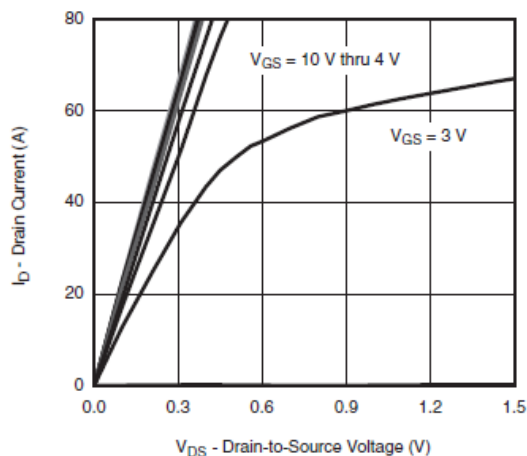
Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	40	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current(T _J =150°C)	I _D	T _C =25°C	A
		T _C =85°C	
		T _A =25°C	
		T _A =85°C	
Pulsed Drain Current	I _{DM}	80	A
Continuous Source Current(Diode Conduction)	I _S	T _C =25°C	A
		T _A =25°C	
Power Dissipation	P _D	T _C =25°C	W
		T _C =85°C	
		T _A =25°C	
		T _A =70°C	
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	62.5	°C/W

Electrical Characteristics (T_A=25°C Unless otherwise noted)

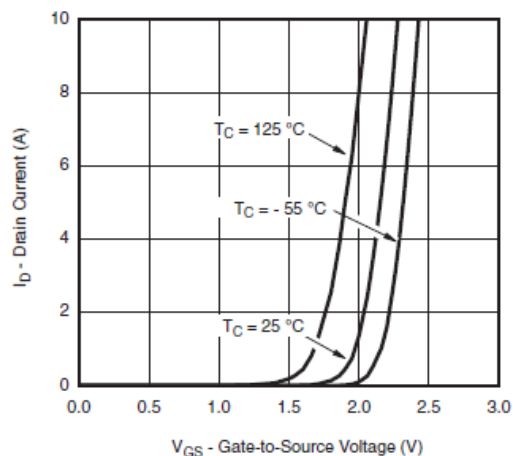
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250uA	40			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.8	2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =32V, V _{GS} =0V			1	uA
		V _{DS} =32V, V _{GS} =0V T _J =85°C			10	
On-State Drain Current	I _{D(on)}	V _{DS} ≥ 5V, V _{GS} =10V	20			A
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =18A		6.5	9	mΩ
		V _{GS} =4.5V, I _D =15A		8	11	
Forward Transconductance	g _{FS}	V _{DS} =15V, I _D =20A		70		S
Diode Forward Voltage	V _{SD}	I _S =5A, V _{GS} =0V		0.8	1.3	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =20V, V _{GS} =4.5V I _D ≡18.5A		21	32	nC
Gate-Source Charge	Q _{gs}			6.2		
Gate-Drain Charge	Q _{gd}			6.2		
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V f=1MHz		2300		pF
Output Capacitance	C _{oss}			340		
Reverse Transfer Capacitance	C _{rss}			140		
Turn-On Time	t _{d(on)}	V _{DD} =20V, R _L =2.0Ω I _D ≡10A, V _{GEN} =10V R _G =1.0Ω		10	20	ns
	t _r			10	20	
Turn-Off Time	t _{d(off)}			30	55	
	t _f			10	20	



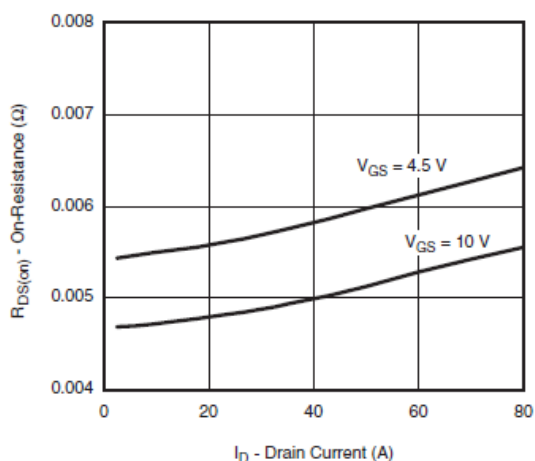
Typical Characteristics



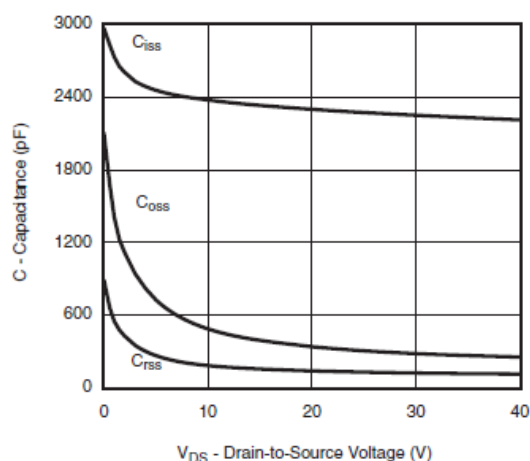
Output Characteristics



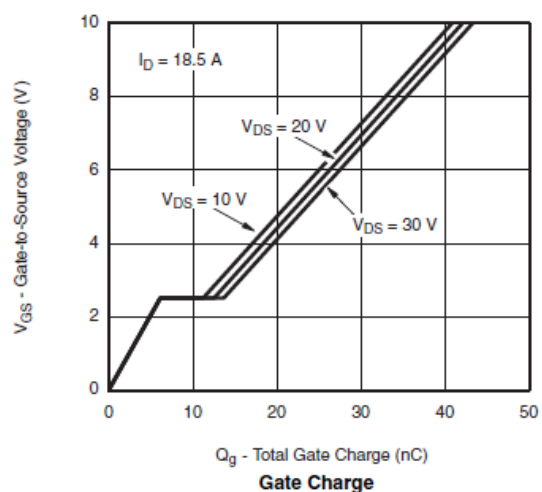
Transfer Characteristics



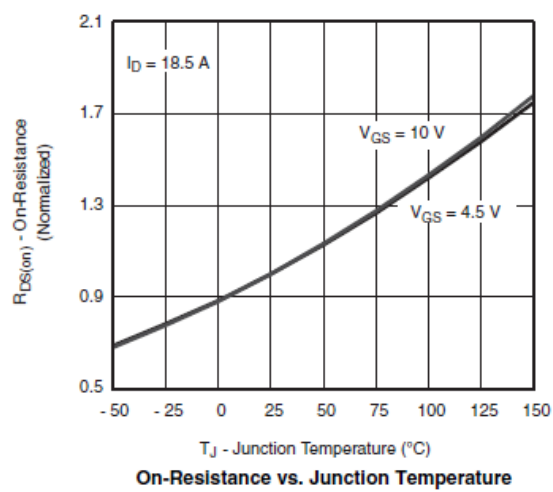
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



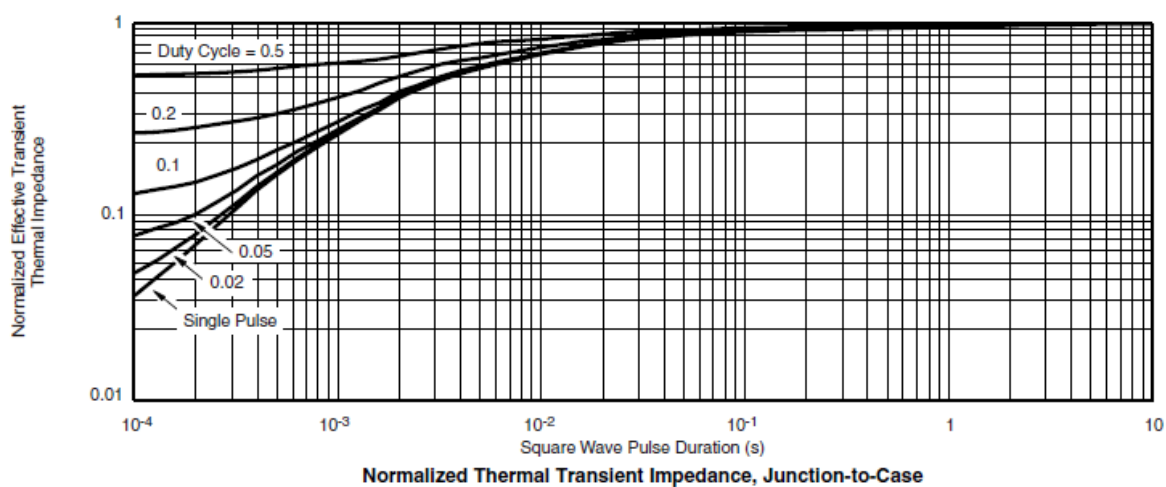
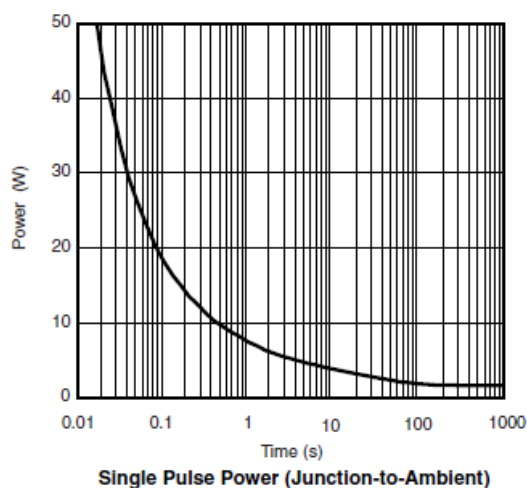
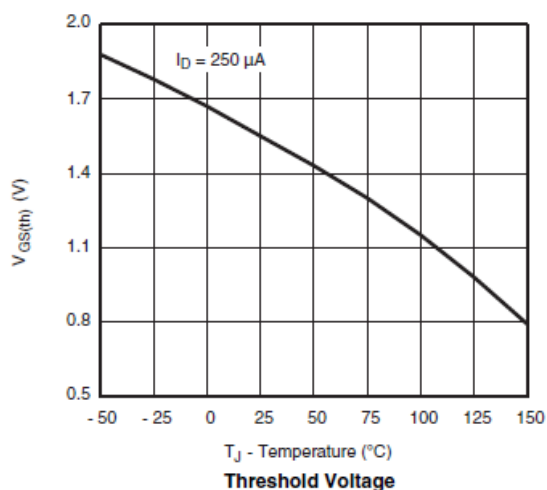
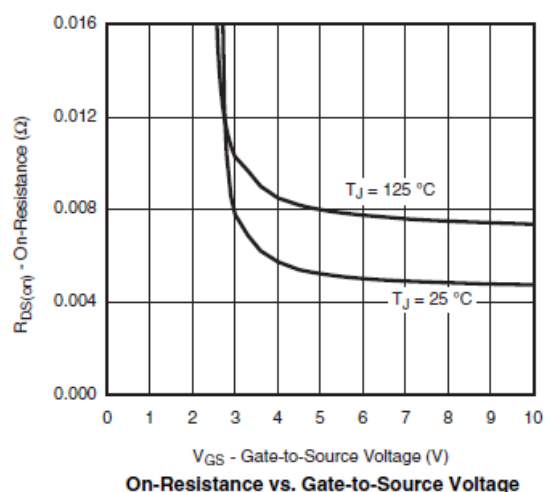
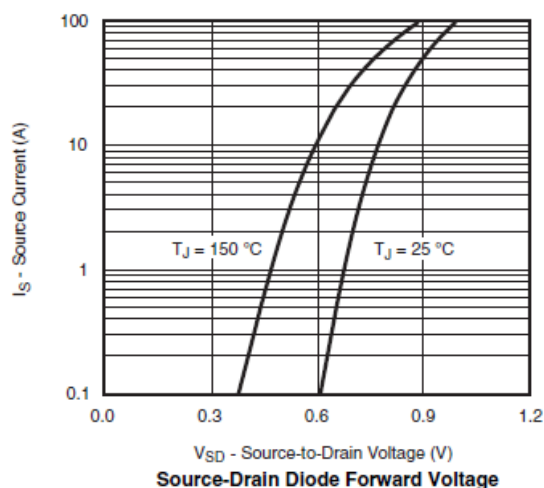
Gate Charge



On-Resistance vs. Junction Temperature



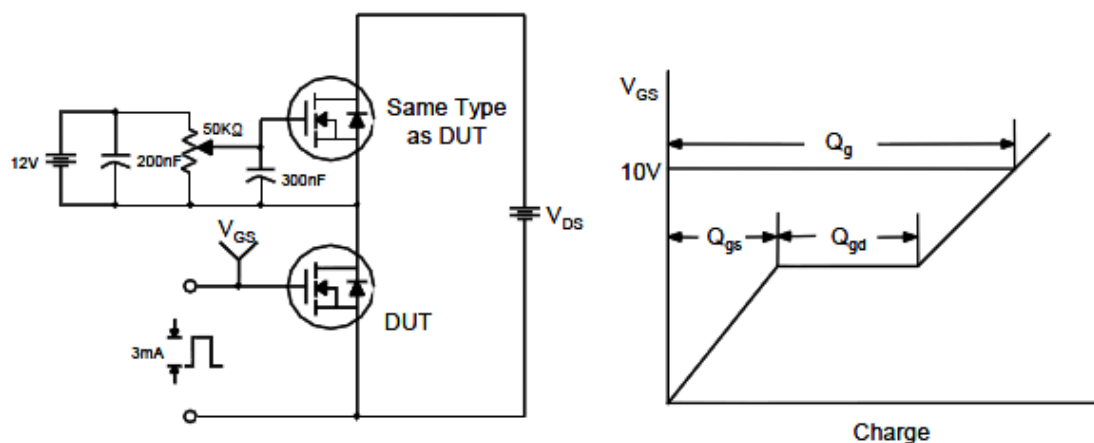
Typical Characteristics



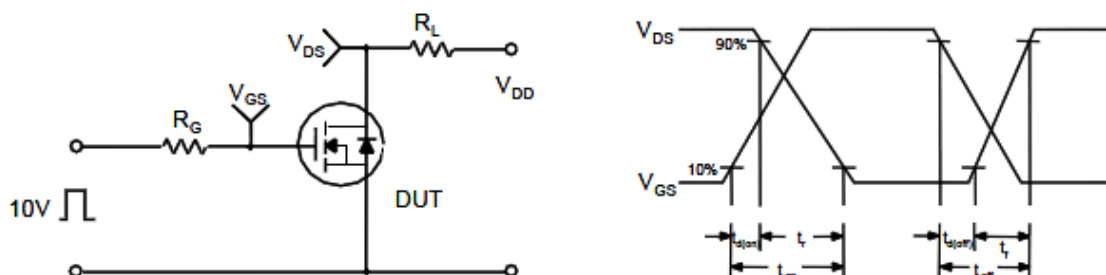


Typical Characteristics

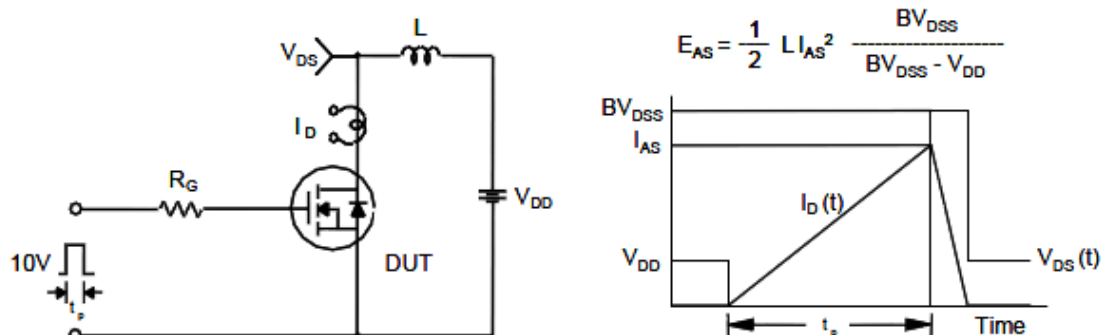
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

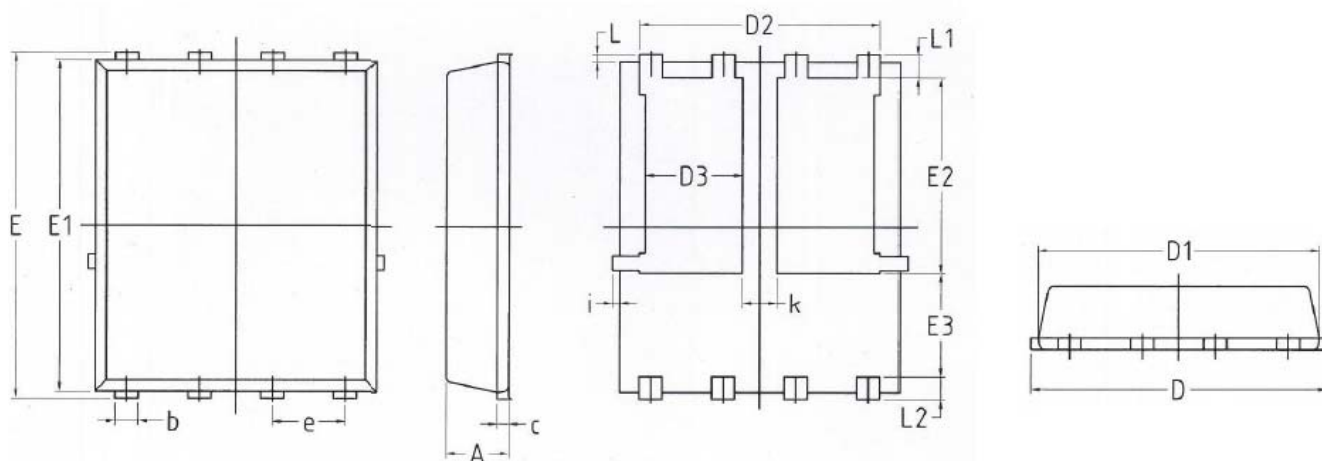


Unclamped Inductive Switching Test Circuit & Waveforms





Package Information (DFN 5X6-8L)



S Y M B O L	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.203 BSC		0.0080 BSC	
D	4.80	5.40	0.1890	0.2126
D1	4.80	5.00	0.1890	0.1969
D2	4.11	4.31	0.1620	0.1700
D3	1.60	1.80	0.0629	0.0708
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	3.30	3.50	0.1300	0.1378
E3	1.70	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0019	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
i	/	0.18	/	0.0070
k	0.5	0.7	0.0197	0.0276

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2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)
Tel : 886 2) 2651 3928
Fax : 886 2) 2786 8483
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