



**Alfa-MOS  
Technology**

**AFN5908W  
20V N-Channel  
Enhancement Mode MOSFET**

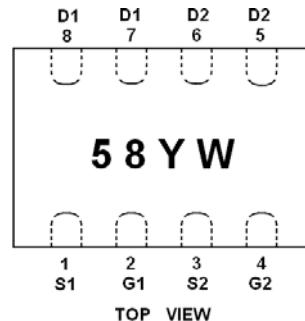
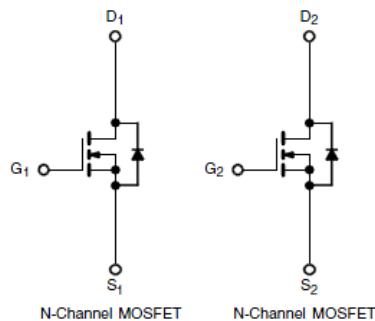
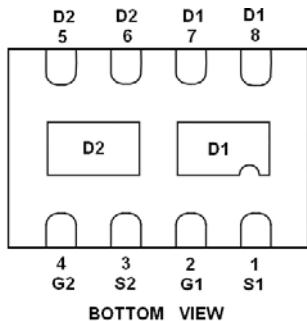
## General Description

AFN5908W, N-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent  $R_{DS(ON)}$ , low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

## Features

- 20V/6.2A,  $R_{DS(ON)}=30m\Omega$  @  $V_{GS}=4.5V$
- 20V/4.6A,  $R_{DS(ON)}=35m\Omega$  @  $V_{GS}=2.5V$
- 20V/3.8A,  $R_{DS(ON)}=45m\Omega$  @  $V_{GS}=1.8V$
- Super high density cell design for extremely low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- DFN3X2-8L package design

## Pin Description ( DFN3X2-8L )



## Application

- Load Switch
- PA Switch
- Battery Switch

## Pin Define

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

## Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFN5908WFN328RG	58YW	DFN3X2-8L	Tape & Reel	4000 EA

※ Y year code

※ W weekh code

※ AFN5908WFN328RG : 7" Tape & Reel ; Pb- Free ; Halogen -Free



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### Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	$V_{DSS}$	20	V
Gate –Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current( $T_J=150^\circ\text{C}$ )	$I_D$	6.2	A
		3.8	
Pulsed Drain Current	$I_{DM}$	30	A
Continuous Source Current(Diode Conduction)	$I_S$	1.6	A
Power Dissipation	$P_D$	6.5	W
		4.2	
Operating Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ\text{C}/\text{W}$

### Electrical Characteristics

( $T_A=25^\circ\text{C}$  Unless otherwise noted)

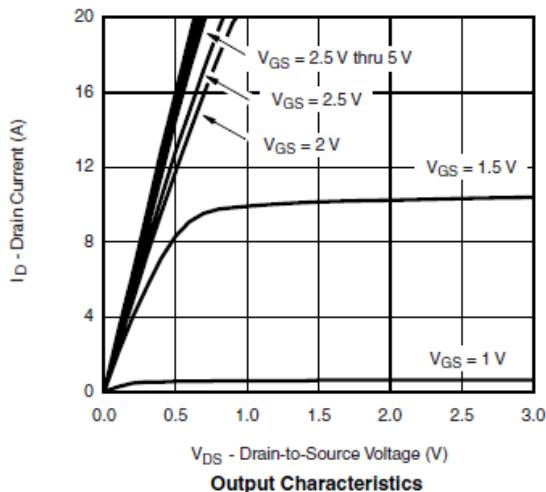
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.3		0.8	
Gate Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=16\text{V}, V_{GS}=0\text{V}$			1	uA
		$V_{DS}=16\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5\text{V}, V_{GS}=4.5\text{V}$	6			A
		$V_{DS} \geq 5\text{V}, V_{GS}=2.5\text{V}$	4			
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=4.5\text{V}, I_D=6.2\text{A}$		24	30	mΩ
		$V_{GS}=2.5\text{V}, I_D=4.6\text{A}$		28	35	
		$V_{GS}=1.8\text{V}, I_D=3.8\text{A}$		34	45	
Forward Transconductance	$g_{FS}$	$V_{DS}=5\text{V}, I_D=3.6\text{A}$		10		S
Diode Forward Voltage	$V_{SD}$	$I_S=1.6\text{A}, V_{GS}=0\text{V}$		0.85	1.2	V
<b>Dynamic</b>						
Total Gate Charge	$Q_g$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}$ $I_D=6.2\text{A}$		6.2	10	nC
Gate-Source Charge	$Q_{gs}$			1.0		
Gate-Drain Charge	$Q_{gd}$			1.6		
Input Capacitance	$C_{iss}$	$V_{DS}=10\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		480		pF
Output Capacitance	$C_{oss}$			160		
Reverse Transfer Capacitance	$C_{rss}$			100		
Turn-On Time	$t_{d(on)}$	$V_{DD}=10\text{V}, R_L=1.9\Omega$ $I_D=5.4\text{A}, V_{GEN}=4.5\text{V}$ $R_G=1\Omega$		10	16	ns
	$t_r$			12	25	
Turn-Off Time	$t_{d(off)}$			25	40	
	$t_f$			12	20	



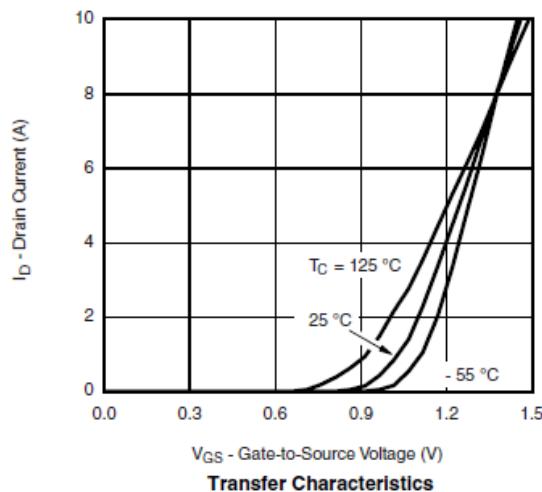
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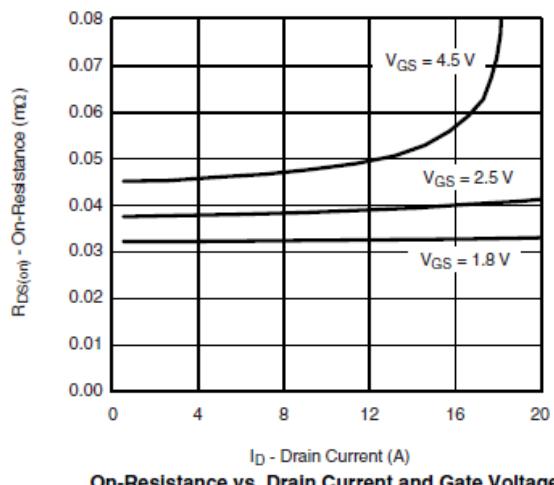
### Typical Characteristics



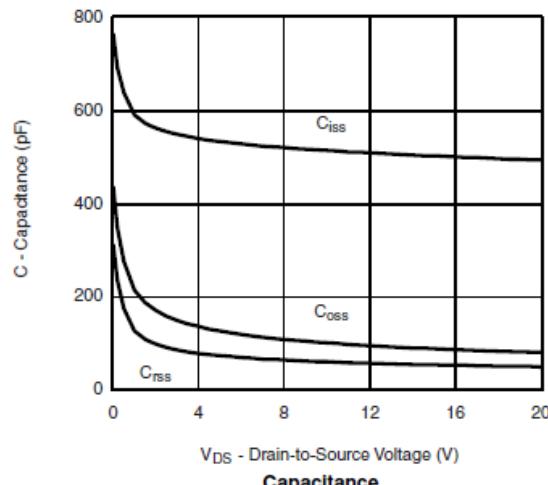
Output Characteristics



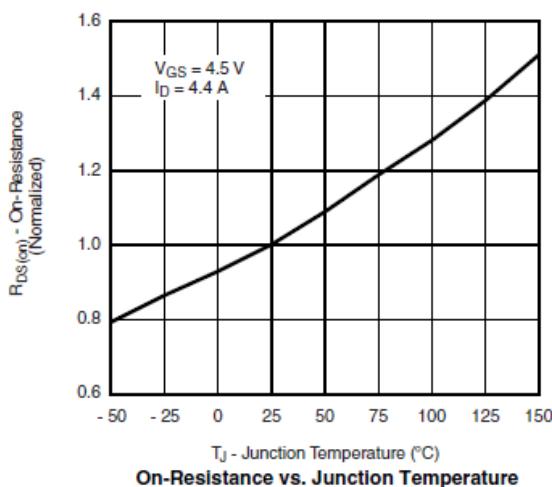
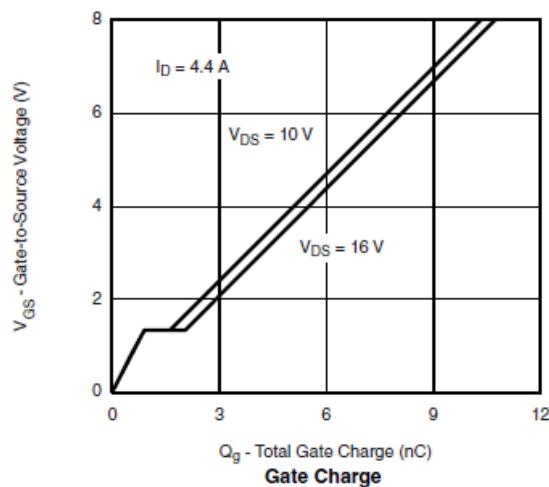
Transfer Characteristics



On-Resistance vs. Drain Current and Gate Voltage



Capacitance



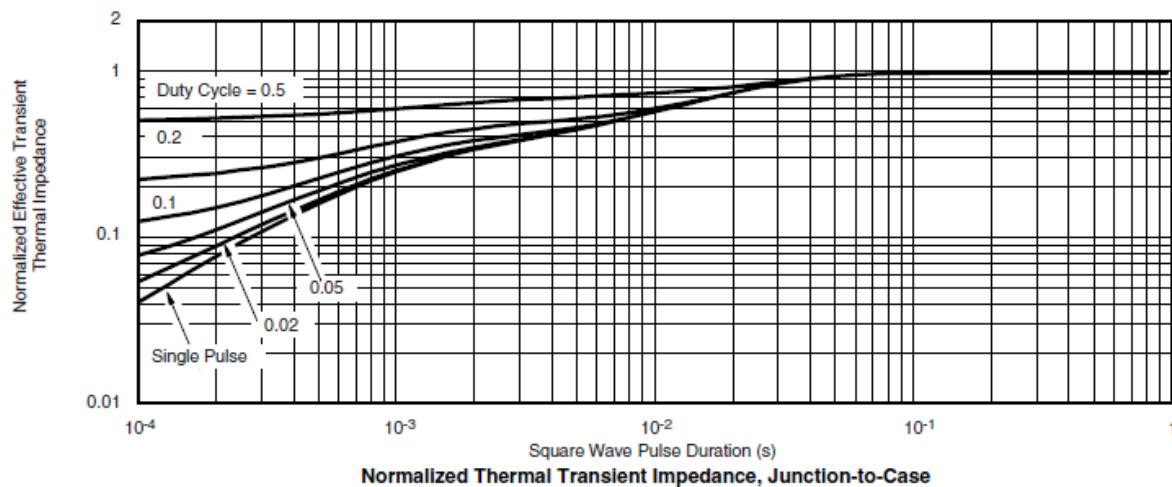
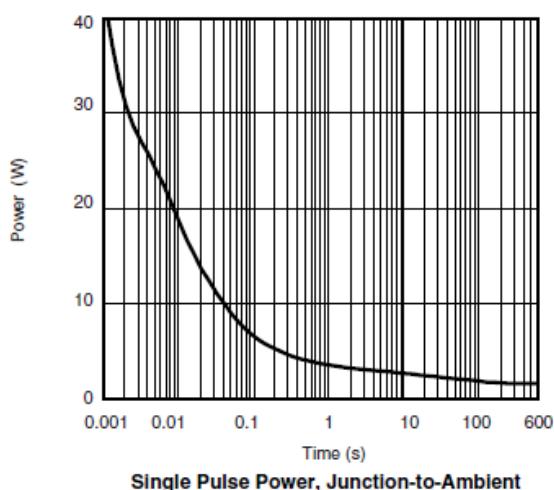
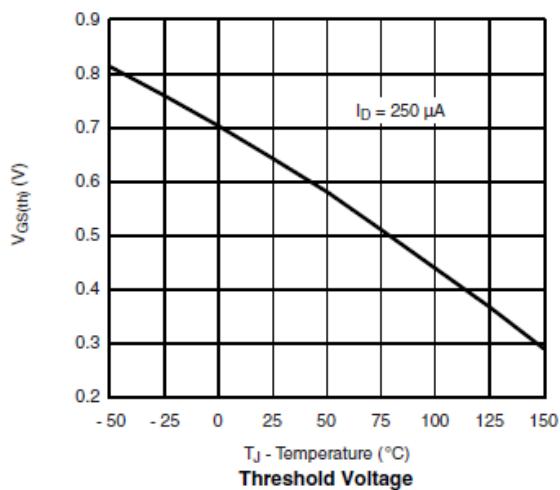
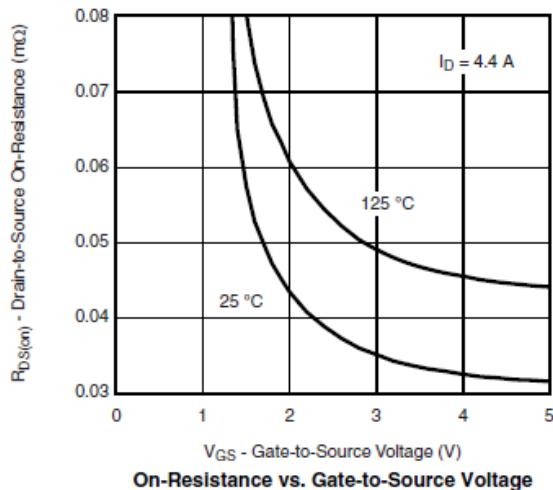
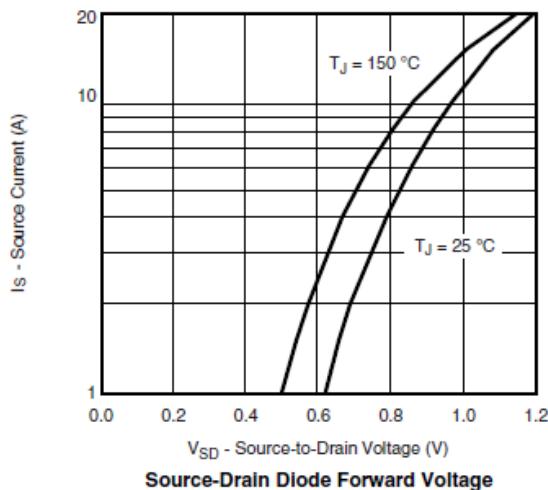
On-Resistance vs. Junction Temperature



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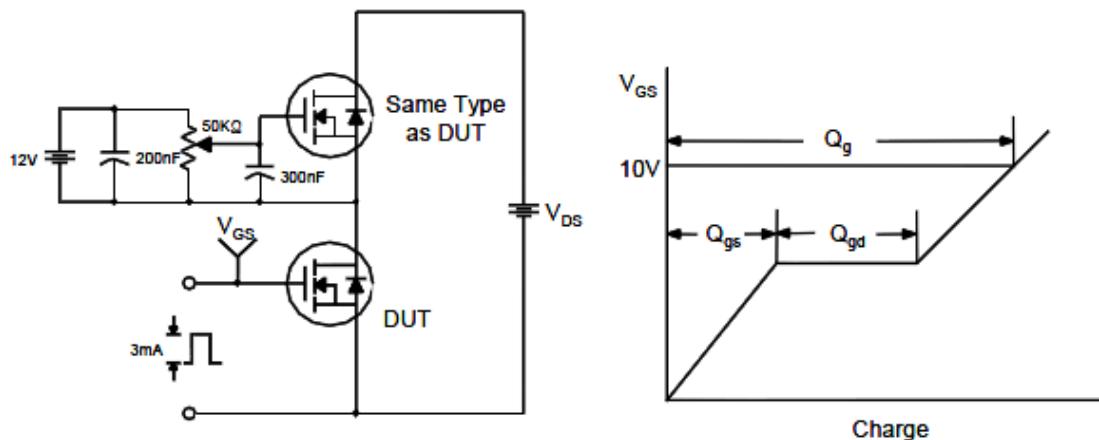
### Typical Characteristics



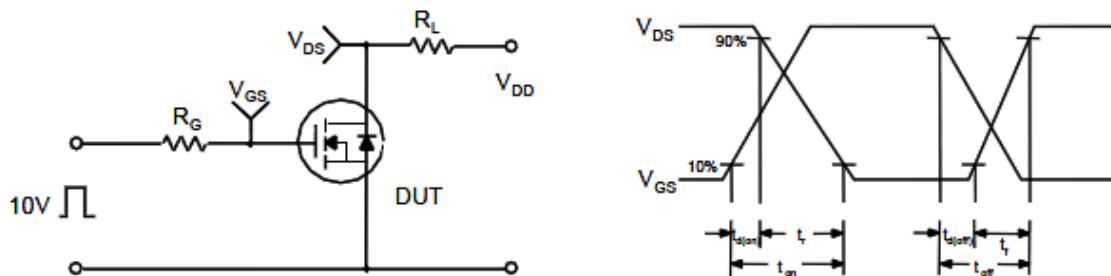


### Typical Characteristics

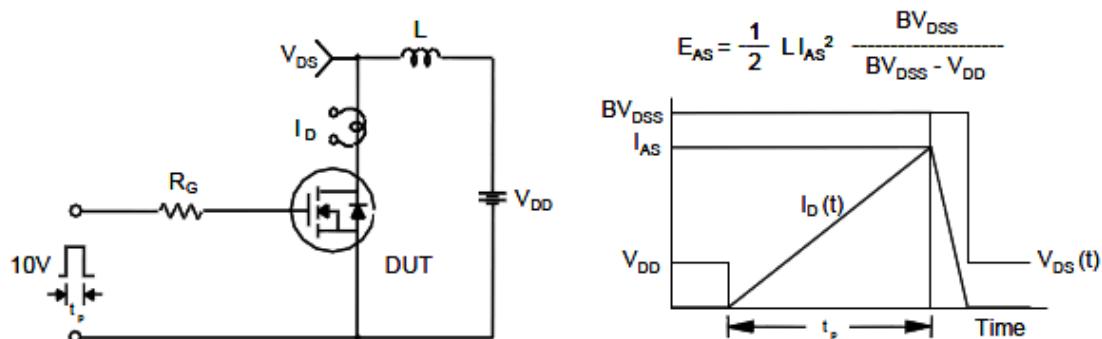
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

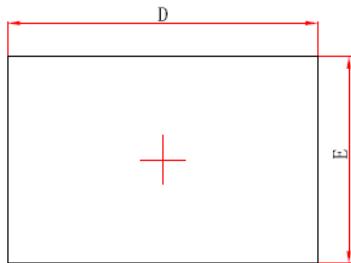




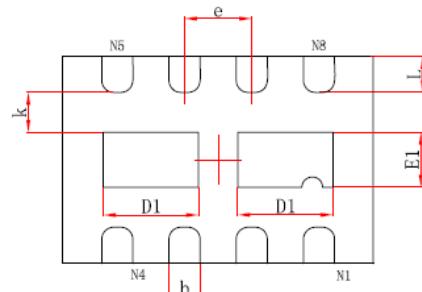
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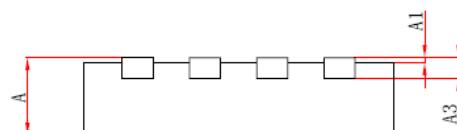
**Package Information ( DFN3X2-8L )**



Top View



Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203REF.		0.008REF.	
D	2.924	3.076	0.115	0.121
E	1.924	2.076	0.076	0.082
D1	0.820	1.020	0.032	0.040
E1	0.430	0.630	0.017	0.025
k	0.200MIN.		0.008MIN.	
b	0.250	0.350	0.010	0.014
e	0.650TYP.		0.026TYP.	
L	0.274	0.426	0.011	0.017

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