



**Alfa-MOS
Technology**

**AFP3415EA
20V P-Channel
Enhancement Mode MOSFET**

General Description

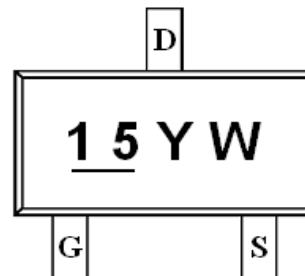
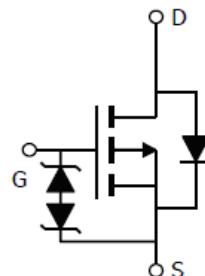
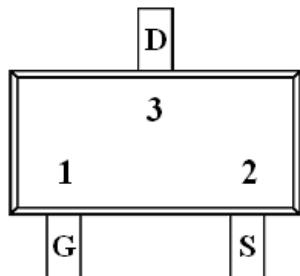
AFP3415EA, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- -20V/-4.5A, $R_{DS(ON)}=41m\Omega$ @ $V_{GS}=4.5V$
- -20V/-3.4A, $R_{DS(ON)}=54m\Omega$ @ $V_{GS}=2.5V$
- -20V/-2.2A, $R_{DS(ON)}=70m\Omega$ @ $V_{GS}=1.8V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- ESD Protected
- SOT-23 package design

Pin Description (SOT-23)



Application

- Portable Equipment
- Battery Powered System
- Net Working System

Pin Define

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

Ordering Information

Part Ordering No.	Part Marking	Package	Unit	Quantity
AFP3415EAS23RG	<u>15</u> YW	SOT-23	Tape & Reel	3000 EA

- ※ 15 parts code
- ※ Y year code (0 ~ 9)
- ※ W week code (A ~ Z = 1 ~ 26 / a ~ z = 27 ~ 52)
- ※ AFP3415EAS23RG : 7" Tape & Reel ; Pb- Free ; Halogen- Free



**Alfa-MOS
Technology**

**AFP3415EA
20V P-Channel
Enhancement Mode MOSFET**

Absolute Maximum Ratings

($T_A=25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V_{DSS}	-20	V
Gate -Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current($T_J=150^\circ\text{C}$)	I_D	-4.5	A
$T_A=70^\circ\text{C}$		-3.9	
Pulsed Drain Current	I_{DM}	-10	A
Continuous Source Current(Diode Conduction)	I_S	-1.6	A
Power Dissipation	P_D	1.25	W
$T_A=70^\circ\text{C}$		0.8	
Operating Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55/150	$^\circ\text{C}$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ\text{C}/\text{W}$

Electrical Characteristics

($T_A=25^\circ\text{C}$ Unless otherwise noted)

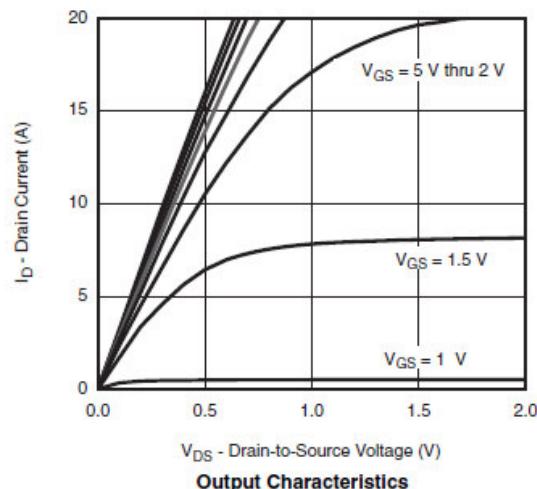
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{V}, I_D=-250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=-250\mu\text{A}$	-0.4	-0.6	-0.9	
Gate Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$			-1	μA
		$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$ $T_J=85^\circ\text{C}$			-10	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq -5\text{V}, V_{GS}=-4.5\text{V}$	-6			A
		$V_{DS} \geq -5\text{V}, V_{GS}=-2.5\text{V}$	-4			
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5\text{V}, I_D=-4.5\text{A}$		35	41	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}, I_D=-3.4\text{A}$		45	54	
		$V_{GS}=-1.8\text{V}, I_D=-2.2\text{A}$		60	70	
Forward Transconductance	g_{FS}	$V_{DS}=-5\text{V}, I_D=-3.6\text{A}$		10		S
Diode Forward Voltage	V_{SD}	$I_S=-1.25\text{A}, V_{GS}=0\text{V}$		-0.85	-1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=-10\text{V}, V_{GS}=-2.5\text{V}$ $I_D=-4.0\text{A}$		10	18	nC
Gate-Source Charge	Q_{gs}			2.5		
Gate-Drain Charge	Q_{gd}			3.5		
Input Capacitance	C_{iss}	$V_{DS}=-10\text{V}, V_{GS}=0\text{V}$ $f=1\text{MHz}$		1050		pF
Output Capacitance	C_{oss}			165		
Reverse Transfer Capacitance	C_{rss}			135		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-10\text{V}, R_L=2.7\Omega$ $I_D=-3.7\text{A}, V_{GEN}=-4.5\text{V}$		15	25	ns
	t_r			25	40	
Turn-Off Time	$t_{d(off)}$			40	65	
	t_f			15	25	



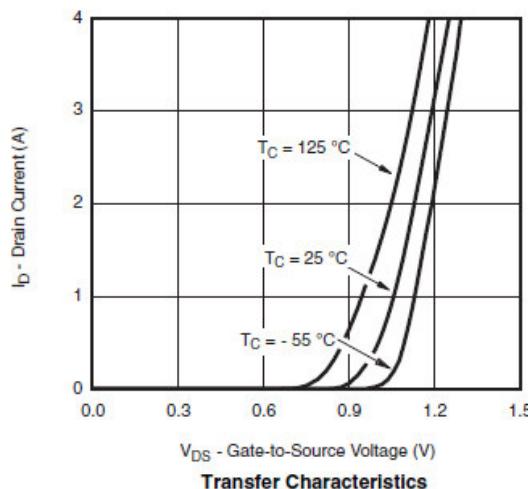
**Alfa-MOS
Technology**

**AFP3415EA
20V P-Channel
Enhancement Mode MOSFET**

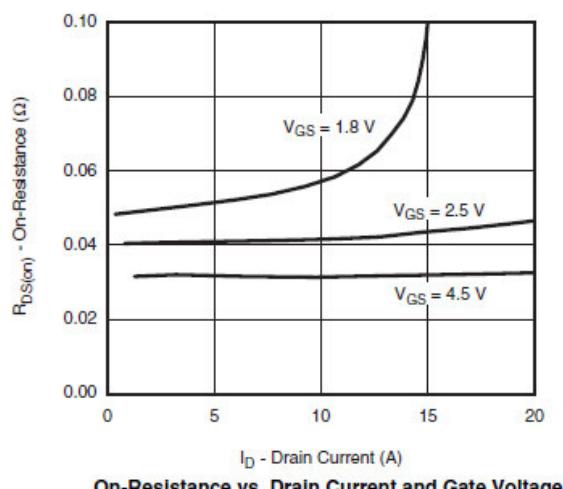
Typical Characteristics



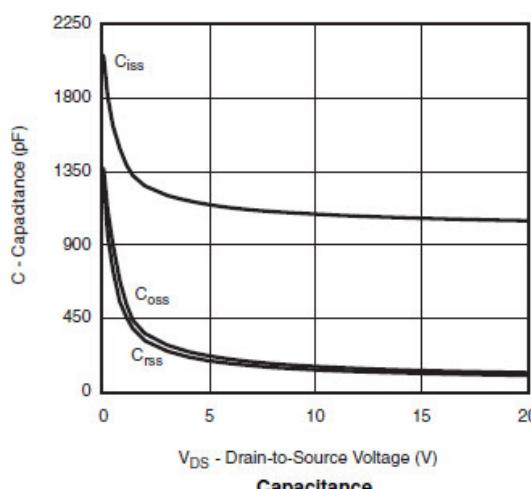
Output Characteristics



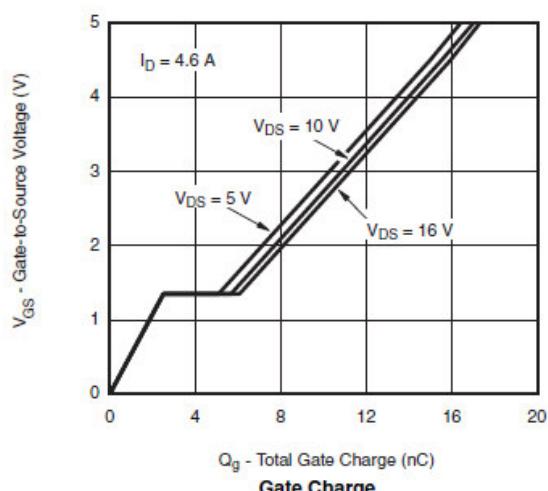
Transfer Characteristics



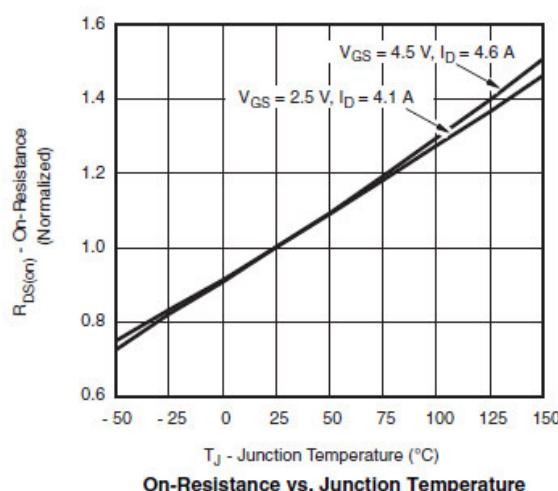
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge



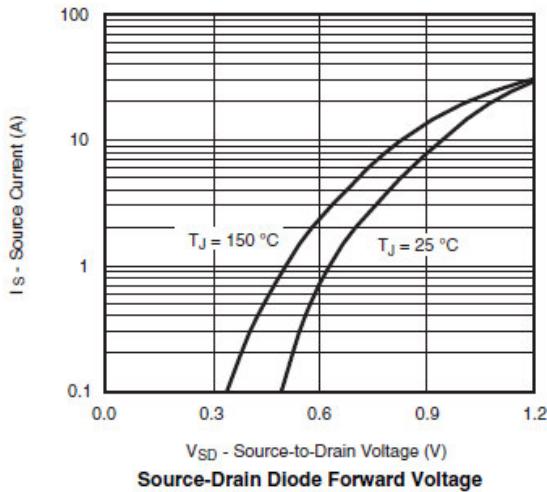
On-Resistance vs. Junction Temperature



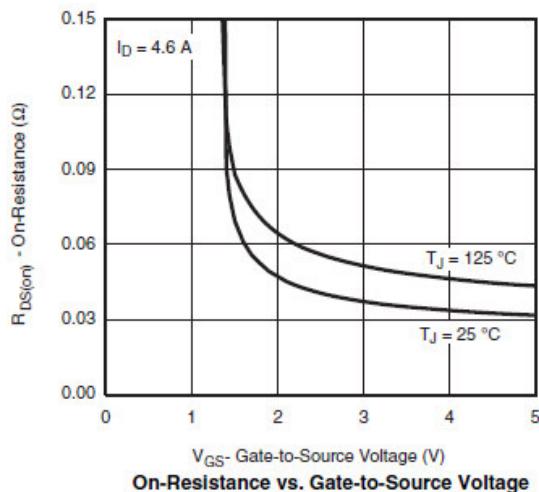
**Alfa-MOS
Technology**

**AFP3415EA
20V P-Channel
Enhancement Mode MOSFET**

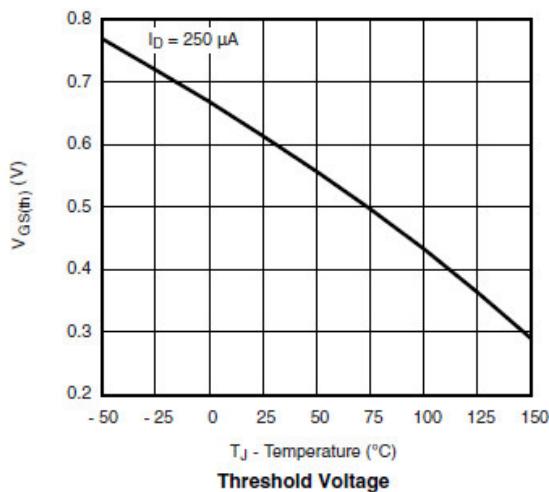
Typical Characteristics



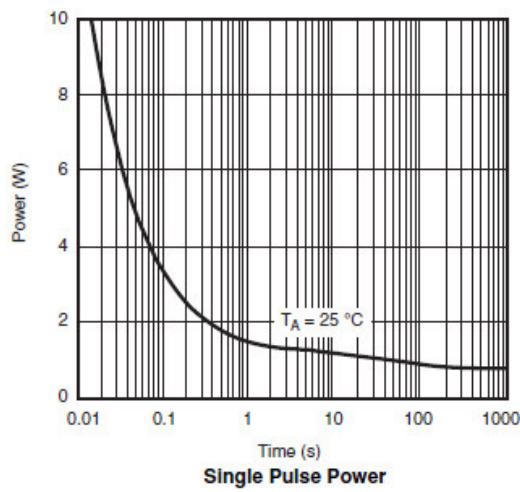
Source-Drain Diode Forward Voltage



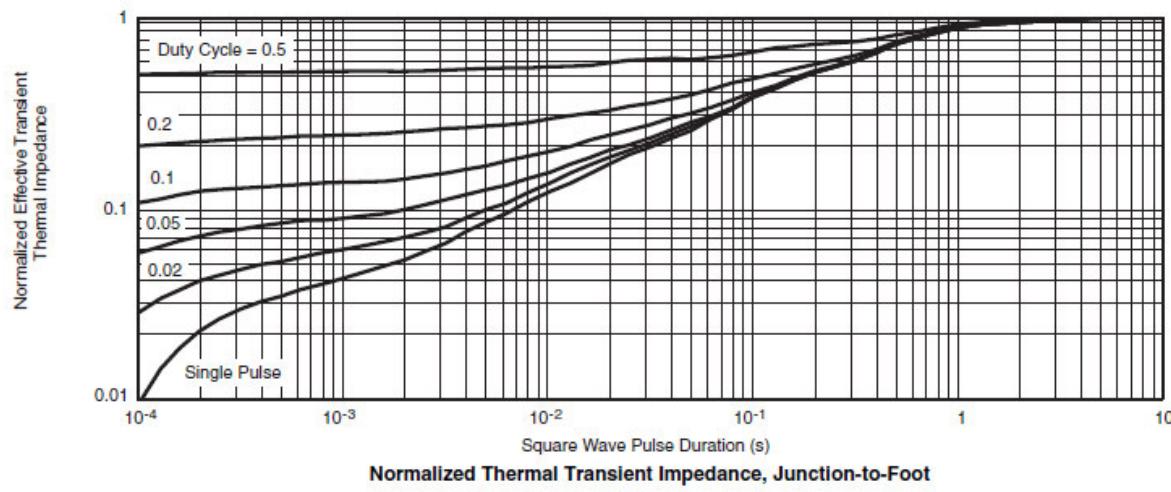
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power



Normalized Thermal Transient Impedance, Junction-to-Foot

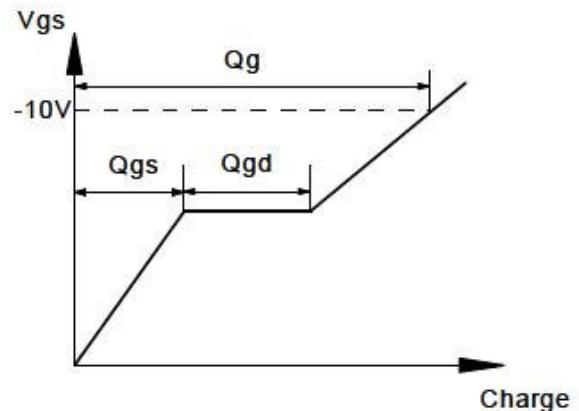
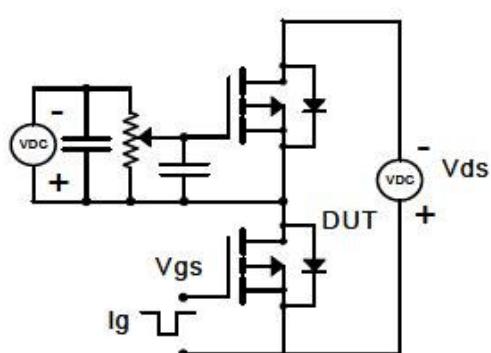


**Alfa-MOS
Technology**

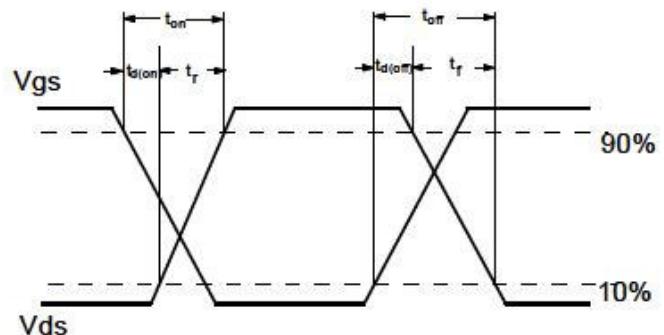
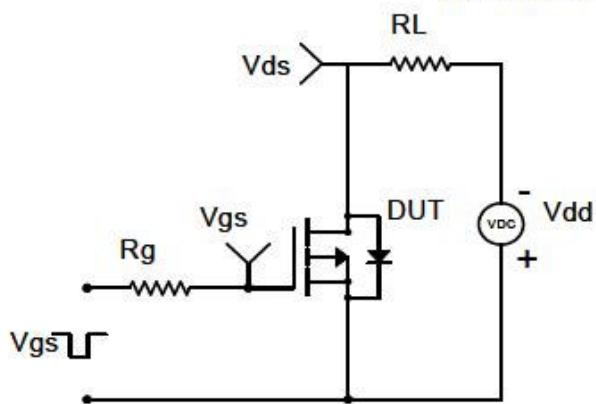
AFP3415EA
20V P-Channel
Enhancement Mode MOSFET

Typical Characteristics

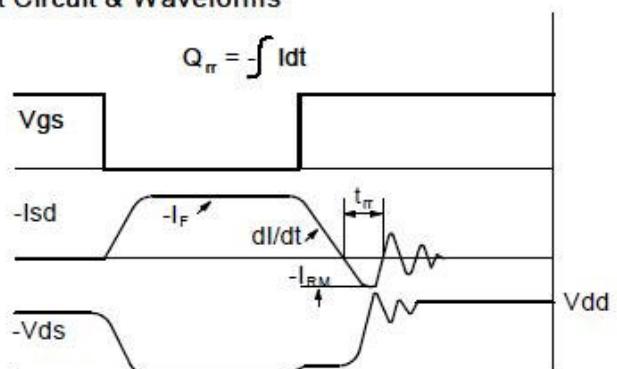
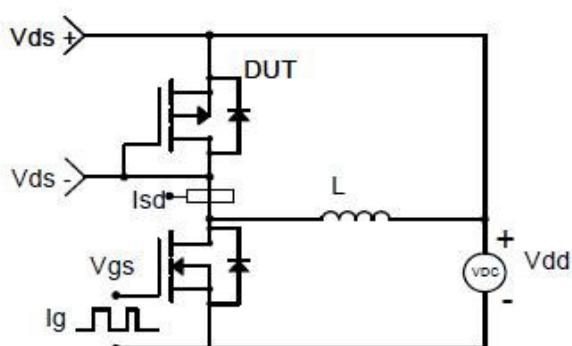
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

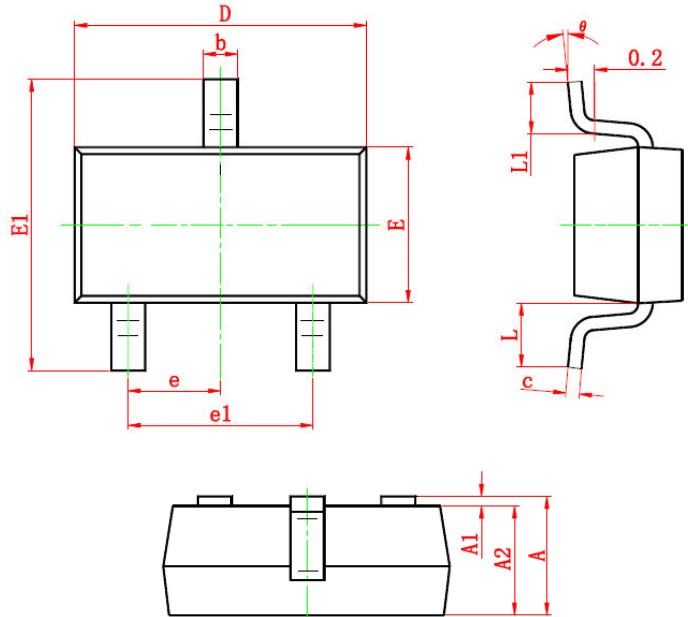




**Alfa-MOS
Technology**

**AFP3415EA
20V P-Channel
Enhancement Mode MOSFET**

Package Information (SOT-23)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	6°

©2010 Alfa-MOS Technology Corp.
2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)
Tel : 886 2) 2651 3928
Fax : 886 2) 2786 8483
©<http://www.alfa-mos.com>