



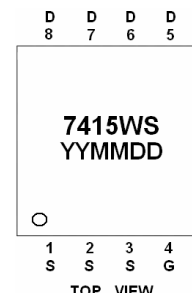
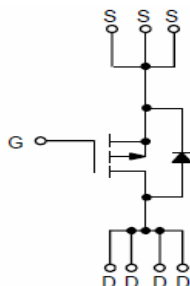
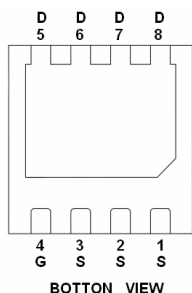
General Description

AFP7415WS, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge. These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

Features

- -60V/-7A, $R_{DS(ON)} = 56m\Omega @ V_{GS} = -10V$
- -60V/-7A, $R_{DS(ON)} = 66m\Omega @ V_{GS} = -4.5V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- DFN3X3-8L package design

Pin Description (DFN3X3-8L)



Application

- Load Switches
- Half-Bridge Motor Drives
- High Voltage Non-Synchronous Buck Converters

Pin Define

| Pin | Symbol | Description |
|-----|--------|-------------|
| 1~3 | S | Source |
| 4 | G | Gate |
| 5~8 | D | Drain |

Ordering Information

| Part Ordering No. | Part Marking | Package | Unit | Quantity |
|-------------------|--------------|-----------|-------------|----------|
| AFP7415WSFN338RG | 7415WS | DFN3X3-8L | Tape & Reel | 5000 EA |

- ※ YY year code
- ※ MM month code
- ※ DD date code
- ※ AFP7415WSFN338RG : 13" Tape & Reel ; Pb- Free ; Halogen -Free



Absolute Maximum Ratings

(T_A=25°C Unless otherwise noted)

| Parameter | Symbol | Typical | Unit |
|---|------------------|----------------------|------|
| Drain-Source Voltage | V _{DSS} | -60 | V |
| Gate –Source Voltage | V _{GSS} | ±20 | V |
| Continuous Drain Current(T _J =150°C) | I _D | T _A =25°C | -14 |
| | | T _A =70°C | -10 |
| Pulsed Drain Current | I _{DM} | -45 | A |
| Continuous Source Current(Diode Conduction) | I _S | -3 | A |
| Power Dissipation | P _D | T _A =25°C | 28 |
| | | T _A =70°C | 18 |
| Operating Junction Temperature | T _J | 150 | °C |
| Storage Temperature Range | T _{STG} | -55/150 | °C |
| Thermal Resistance-Junction to Ambient | R _{θJA} | 26 | °C/W |
| Thermal Resistance-Junction to Case | R _{θJC} | 1.9 | |

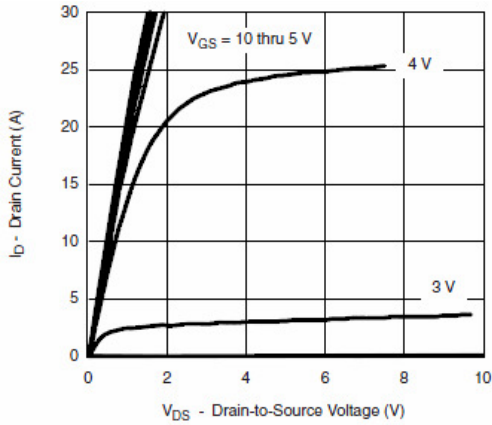
Electrical Characteristics

(T_A=25°C Unless otherwise noted)

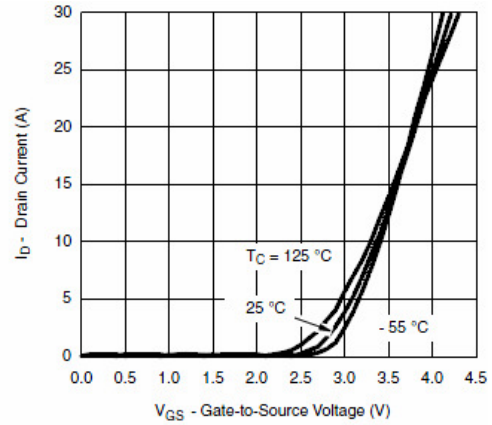
| Parameter | Symbol | Conditions | Min. | Typ | Max. | Unit |
|---------------------------------|----------------------|---|------|------|------|------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | V _{GS} =0V, I _D = -250uA | -60 | | | V |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D = -250uA | -1.0 | | -2.5 | |
| Gate Leakage Current | I _{GSS} | V _{DS} =0V, V _{GS} = ±20V | | | ±100 | nA |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = -48V, V _{GS} =0V | | | -1 | uA |
| | | V _{DS} = -48V, V _{GS} =0V T _J =85°C | | | -20 | |
| On-State Drain Current | I _{D(on)} | V _{DS} ≥ -5V, V _{GS} = -10V | -20 | | | A |
| Drain-Source On-Resistance | R _{DS(on)} | V _{GS} = -10V, I _D =-7A | | 46 | 56 | mΩ |
| | | V _{GS} = -4.5V, I _D =-7A | | 56 | 66 | |
| Forward Transconductance | g _{FS} | V _{DS} = -15V, I _D = -3.2A | | 12 | | S |
| Diode Forward Voltage | V _{SD} | I _S = -3A, V _{GS} =0V | | -0.8 | -1.3 | V |
| Dynamic | | | | | | |
| Total Gate Charge | Q _g | V _{DS} =-30V, V _{GS} =-10V I _D = -10.0A | | 25 | 40 | nC |
| Gate-Source Charge | Q _{gs} | | 5 | | | |
| Gate-Drain Charge | Q _{gd} | | 8 | | | |
| Input Capacitance | C _{iss} | V _{DS} =-25V, V _{GS} =0V f=1MHz | | 1200 | 2000 | pF |
| Output Capacitance | C _{oss} | | 140 | | | |
| Reverse Transfer Capacitance | C _{rss} | | 90 | | | |
| Turn-On Time | t _{d(on)} | V _{DD} =-30V, R _L =3.0Ω I _D ≡-18A, V _{GEN} =-10V R _G =2.5Ω | | 10 | 20 | ns |
| | t _r | | | 10 | 20 | |
| Turn-Off Time | t _{d(off)} | | | 45 | 80 | |
| | t _f | | | 25 | 40 | |



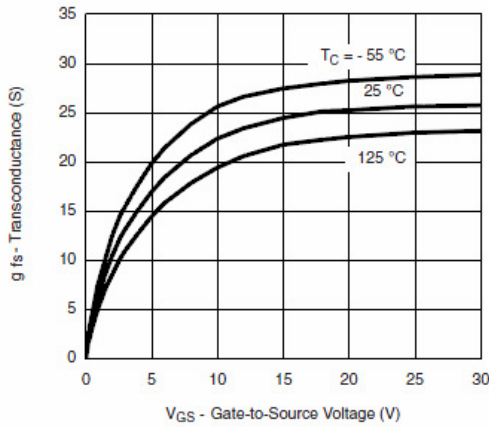
Typical Characteristics



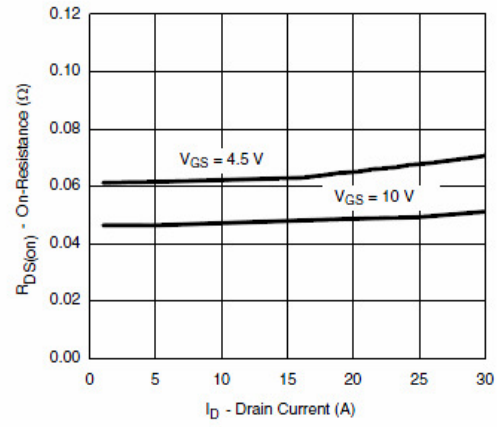
Output Characteristics



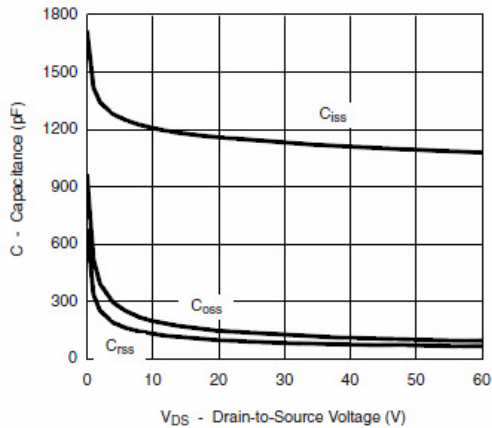
Transfer Characteristics



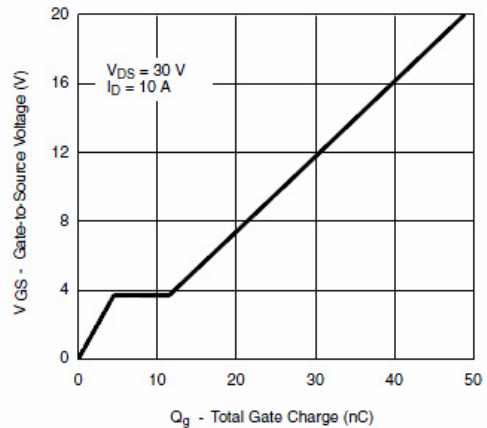
Transconductance



On-Resistance vs. Drain Current



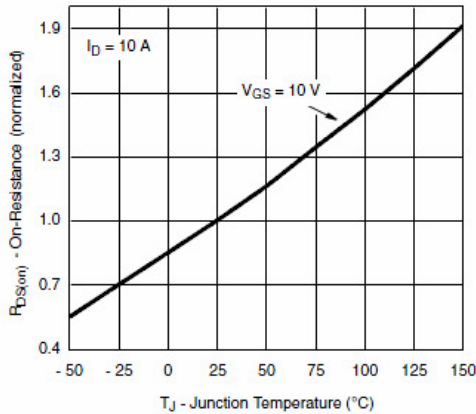
Capacitance



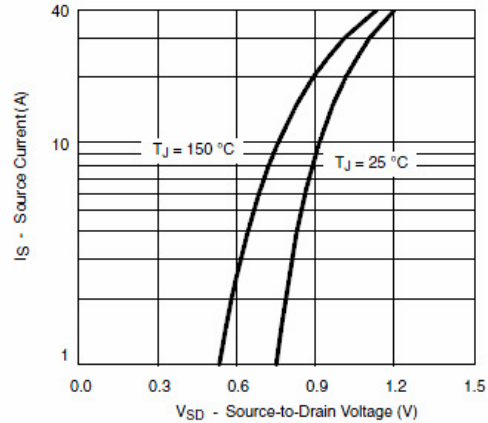
Gate Charge



Typical Characteristics

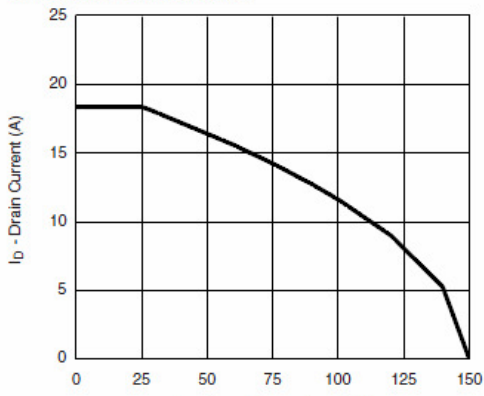


On-Resistance vs. Junction Temperature

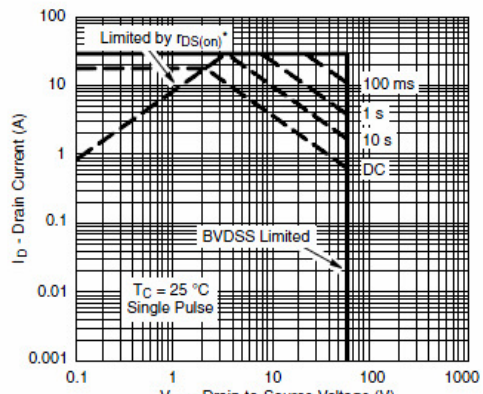


Source-Drain Diode Forward Voltage

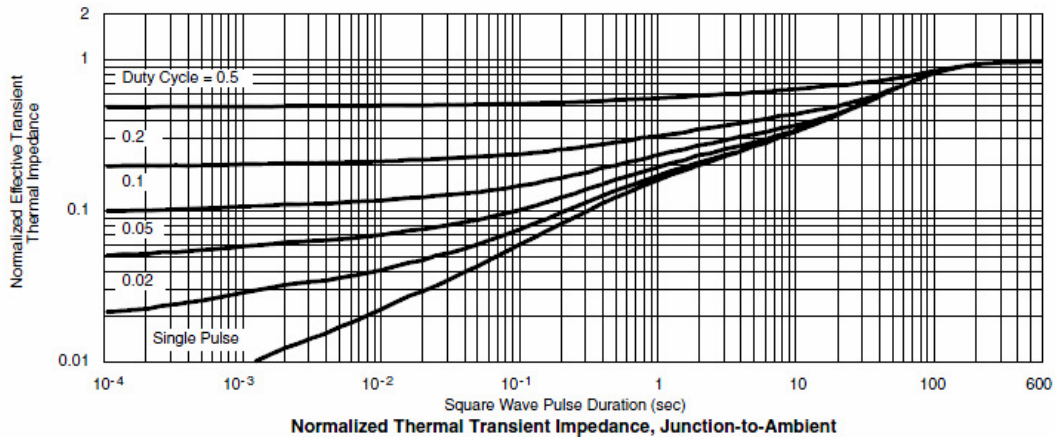
THERMAL RATINGS



Maximum Drain Current vs. Case Temperature



* $V_{GS} >$ minimum V_{GS} at which $r_{DS(on)}$ is specified
Safe Operating Area

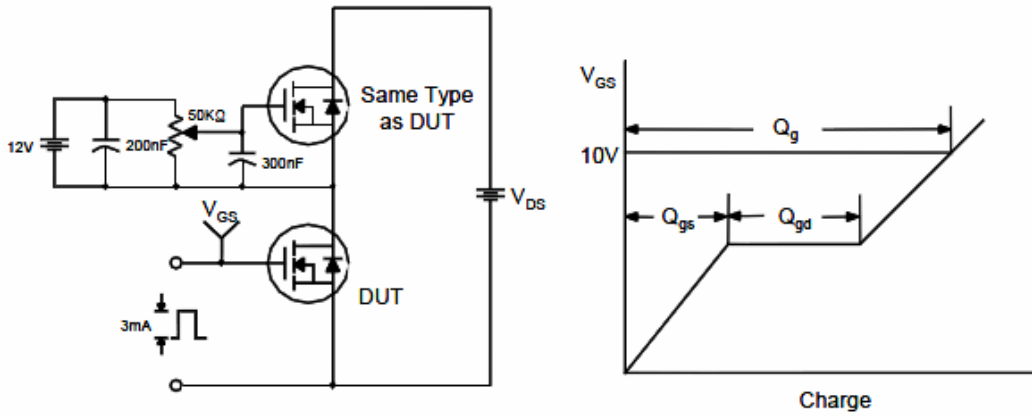


Normalized Thermal Transient Impedance, Junction-to-Ambient

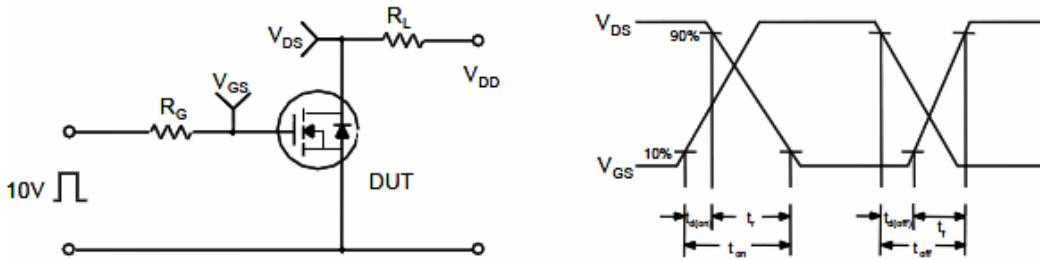


Typical Characteristics

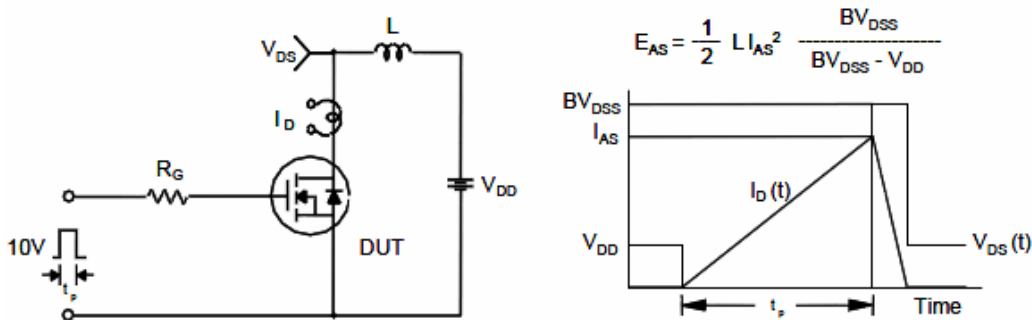
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

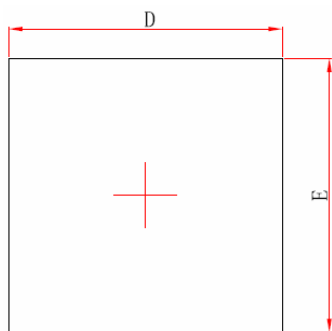


Unclamped Inductive Switching Test Circuit & Waveforms

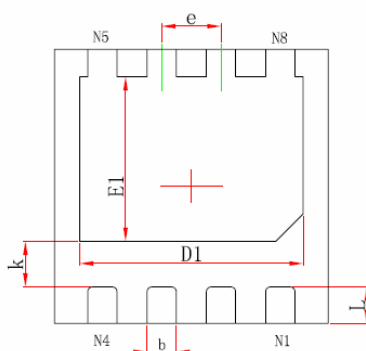




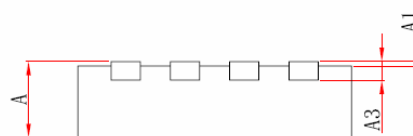
Package Information (DFN3X3-8L)



Top View



Bottom View



Side View

| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 0.800 | 0.900 | 0.031 | 0.035 |
| A1 | 0.000 | 0.050 | 0.000 | 0.002 |
| A3 | 0.203REF. | | 0.008REF. | |
| D | 2.924 | 3.076 | 0.115 | 0.121 |
| E | 2.924 | 3.076 | 0.115 | 0.121 |
| D1 | 2.350 | 2.550 | 0.093 | 0.100 |
| E1 | 1.700 | 1.900 | 0.067 | 0.075 |
| k | 0.450 | 0.550 | 0.018 | 0.022 |
| b | 0.270 | 0.370 | 0.011 | 0.015 |
| e | 0.650TYP. | | 0.026TYP. | |
| L | 0.324 | 0.476 | 0.013 | 0.019 |

©2010 Alfa-MOS Technology Corp.
2F, No.80, Sec.1, Cheng Kung Rd., Nan Kang Dist., Taipei City 115, Taiwan (R.O.C.)
Tel : 886 2) 2651 3928
Fax : 886 2) 2786 8483
©http://www.alfa-mos.com