ORIENT DISPLAY

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SPECIFICATION
FOR
TFT MODULE

MODULE NO:AFS128160TG-1.8-N300001
REVISION NO: 01

Customer's Approval:

	SIGNATURE	DATE
PREPARED BY (RD ENGINEER)	XJZ	2011-12-8
CHECKED BY	YHW	2011-12-8
APPROVED BY	HSH	2011-12-8

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DOCUMENT REVISION HISTORY

Version	DATE	DESCRIPTION	CHANGED BY
00	Nov-06-2010	First Issue	Lhm
01	Mar-20-2012	Changed “Dimensional Outline”	Fr.li

CONTENTS

1. Features & Mechanical Specifications

Item	Contents	Unit
	LCD	
LCD Type	262K TFT / Transmissive / Normal White	--
Viewing direction	12 O'clock	--
Backlight	2 Chip White LED in Parallel	--
Interface	8080-8bit parallel bus interface	--
Driver IC	ST7735R	--
Outline Dimension	34.0(W) × 47.0(H) × 2.5(T)	mm
Glass area (W×H×T)	30.432 ×37.44/40.47 × 0.5	mm
Active area (W×H)	28.032 × 35.04	mm
Number of Dots	128(RGB) × 160	--
Dot pitch (W×H)	0.073 × 0.219	mm
Pixel pitch (W×H)	0.219 × 0.219	mm
Operating Temperature	-10 ～ +70	℃
Storage temperature	-30 ～ +80	℃

2. Dimensional Outline

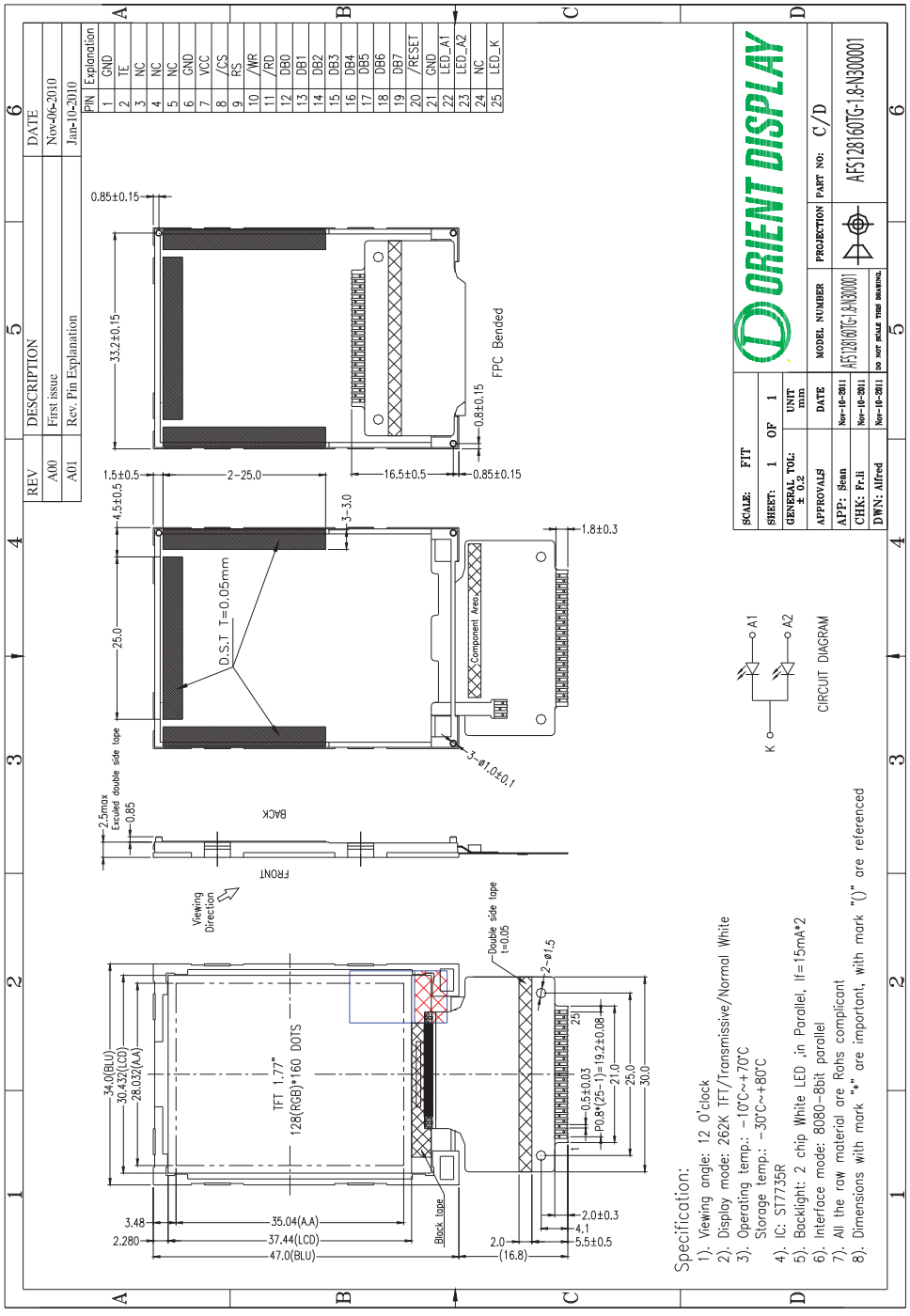


Figure 1. Dimensional outline

3. Block Diagram

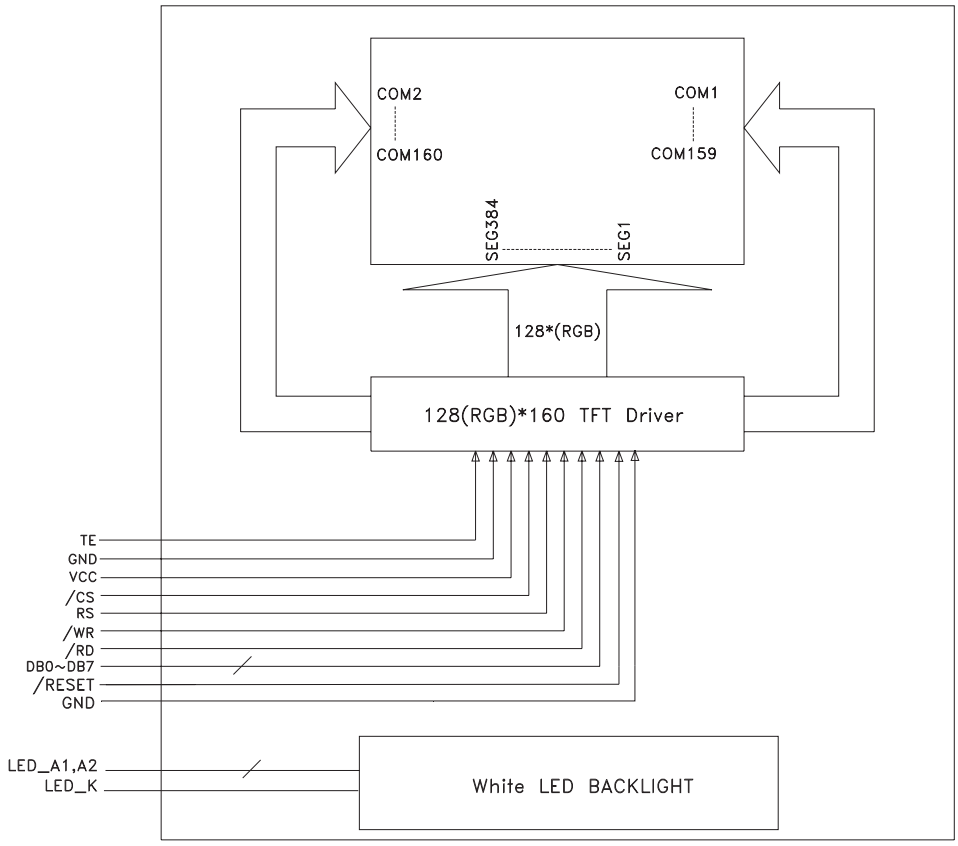


Figure 2. Block diagram

4. Pin Description

PIN No.	SYMBOL	Function
1	GND	Ground
2	TE	Tearing effect output pin
3~5	NC	No Connection
6	GND	Ground
7	VCC	Power supply
8	/CS	Chip Select Signal (Active Low)
9	RS	The signal for register index or register command select Low: Register index or internal status (in read operation) High: Register command.
10	/WR	Write signal.
11	/RD	Read signal.
12~19	DB0~DB7	Data Bus
20	/RESET	Reset pin. (Active Low)
21	GND	Ground
22	LED_A1	Backlight LED1 Anode
23	LED_A2	Backlight LED2 Anode
24	NC	No Connection
25	LED_K	Backlight LED Cathode

5. Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply Voltage range	VCC	-0.3 to +4.6	V
Operating Temperature range	TOP	-10 to +70	℃
Storage Temperature range	TST	-30 to +80	℃

6. Electrical Characteristics

DC Characteristics

Item	Symbol	Min.	Type.	Max.	Unit
Logic Supply Voltage	VCC	2.6	2.75	3.3	V

7. Backlight Characteristics

White LED × 2 **(Ta = 25°C)**

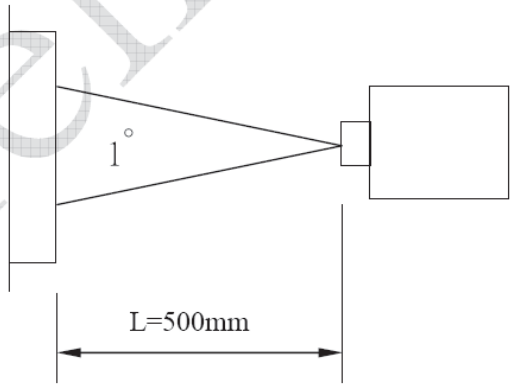
Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	VF	IF = 30mA	-	3.2	-	V
Uniformity	△Bp	-	80	-	-	%
Luminance for LCD	Lv	IF = 30mA	3000	3500	4000	cd/m^2

8. Electro-Optical Characteristics

(Note1，Note2)
(Using CPT LC+ Normal Polarizer+Corresponding Backlight, reference only)

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Transmittance		T		(6.4)	(6.9)		%	
Contrast Ratio		CR	*1)		(250)	-	--	Note 3
Response Time		Tr+ Tf	*3)	-	(30)		ms	Note 4
Viewing Angle	Vertical	θ*2)	(55) (85)	(60)	- -			Note 5
	Horizontal	ψ*2)		(90)	- -			
Color Filter Chromacicity	White	x y Y	(0.288) (0.321) (29.2)	(0.308) (0.341) (32.2)	(0.328) (0.361) (35.2)			Note 6
	Red	x y Y	(0.635) (0.312) (15.2)	(0.655) (0.332) (18.2)	(0.675) (0.352) (21.2)			
	Green	x y Y	(0.294) (0.554) (59.2)	(0.314) (0.574) (62.2)	(0.334) (0.594) (65.2)			
	Blue	x y Y	(0.117) (0.115) (13.1)	(0.137) (0.135) (16.1)	(0.157) (0.155) (19.1)			
	NTSC		-	(58%)	-			

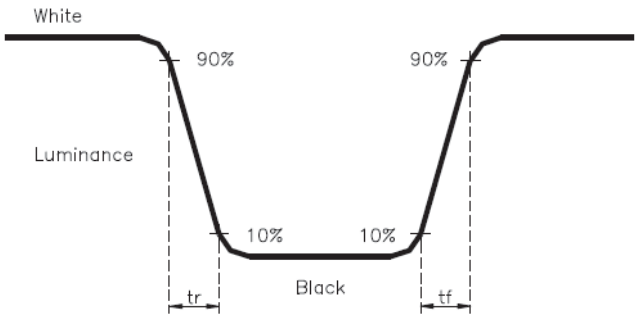
Note 1.Ambient condition：25℃±2℃，60±10%RH，under 10 Lunx in the darkroom。
Note 2.Measure device：BM-5A (TOPCON)，viewing cone= 1°，I_L=20mA。



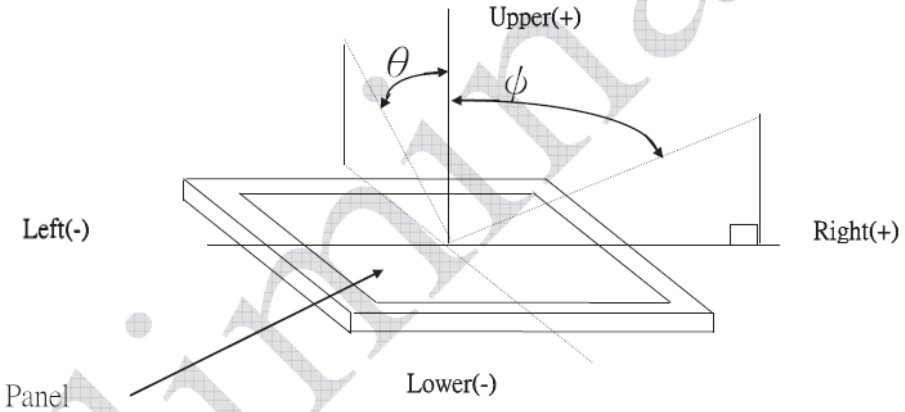
Note 3. Definition of Contrast Ratio：

CR = White Luminance (ON) / Black Luminance (OFF)

Note 4. Definition of response time：The response time is defined as the time interval between the 10% and amplitudes.



Note 5. Definition of view angle(θ，ψ)：



Note 6. Light source: C light.

9. Instruction Description

Panel Function Command List (1)

Instruction	Refer	D/CX	WRX	RDX	D23-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
FRMCTR1	10.2.1	0	↑	1	-	1	0	1	1	0	0	0	1	(B1h)	In normal mode (Full colors)
		1	↑	1	-					RTNA3	RTNA2	RTNA1	RTNA0		RTNA set 1-line period FPA: front porch BPA: back porch
		1	↑	1	-			FPA5	FPA4	FPA3	FPA2	FPA1	FPA0		
		1	↑	1	-			BPA5	BPA4	BPA3	BPA2	BPA1	BPA0		
FRMCTR2	10.2.2	0	↑	1	-	1	0	1	1	0	0	1	0	(B2h)	In Idle mode (8-colors)
		1	↑	1	-					RTNB3	RTNB2	RTNB1	RTNB0		RTNB: set 1-line period FPB: front porch BPB: back porch
		1	↑	1	-			FPB5	FPB4	FPB3	FPB2	FPB1	FPB0		
		1	↑	1	-			BPB5	BPB4	BPB3	BPB2	BPB1	BPB0		
FRMCTR3	10.2.3	0	↑	1	-	1	0	1	1	0	0	1	1	(B3h)	In partial mode + Full colors
		1	↑	1	-					RTNC3	RTNC2	RTNC1	RTNC0		RTNC,RTND: set 1-line period FPC,FPD: front porch BPC,BPD: back porch
		1	↑	1	-			FPC5	FPC4	FPC3	FPC2	FPC1	FPC0		
		1	↑	1	-			BPC5	BPC4	BPC3	BPC2	BPC1	BPC0		
		1	↑	1	-					RTND3	RTND2	RTND1	RTND0		
		1	↑	1	-			FPD5	FPD4	FPD3	FPD2	FPD1	FPD0		
		1	↑	1	-			BPD5	BPD4	BPD3	BPD2	BPD1	BPD0		
INVCTR	10.2.4	0	↑	1	-	1	0	1	1	0	1	0	0	(B4h)	Display inversion control
		1	↑	1	-	0	0	0	0	0	NLA	NLB	NLC		NLA,NLB,NLC set inversion
DISSET5	10.2.5	0	↑	1	-	1	0	1	1	0	1	1	0	(B6h)	Display function setting
		1	↑	1	-	1	0	NO1	NO0	SDT1	SDT0	EQ1	EQ0		SDT: set amount of source delay EQ: set EQ period
		1	↑	1	-	1	1	1	1	PTG1	PTG0	PT1	PT0		

Panel Function Command List (2)

Instruction	Refer	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
PWCTR1	10.2.5	0	↑	1	-	1	1	0	0	0	0	0	0	(C0h)	Power control setting
		1	↑	1	-	AVDD[2]	AVDD[1]	AVDD[0]	VRHP4	VRHP3	VRHP2	VRHP1	VRHP0		VRH: Set the GVDD voltage
		1	↑	1	-	0	0	0	VRHN4	VRHN3	VRHN2	VRHN1	VRHN0		
		1	↑	1		MODE[1]	MODE[0]	0	0	0	1	0	0		
PWCTR2	10.2.7	0	↑	1	-	1	1	0	0	0	0	0	1	(C1h)	Power control setting
		1	↑	1	-	VGH25[1]	VGH25[0]	-	-	VGLSEL[1]	VGLSEL[0]	VGHBT[1]	VGHBT[0]		BT: set VGH/ VGL voltage
PWCTR3	10.2.8	0	↑	1	-	1	1	0	0	0	0	1	0	(C2h)	In normal mode (Full colors)
		1	↑	1	-	DCA9	DCA8	SAPA2	SAPA1	SAPA0	APA2	APA1	APA0		APA: adjust the operational amplifier
					-	DCA7	DCA6	DCA5	DCA4	DCA3	DCA2	DCA1	DCA0		DCA: adjust the booster Voltage
PWCTR4	10.2.9	0	↑	1	-	1	1	0	0	0	0	1	1	(C3h)	In Idle mode (8-colors)
		1	↑	1	-	DCB9	DCB8	SAPB2	SAPB1	SAPB0	APB2	APB1	APB0		APB: adjust the operational amplifier
					-	DCB7	DCB6	DCB5	DCB4	DCB3	DCB2	DCB1	DCB0		DCB: adjust the booster Voltage
PWCTR5	10.2.10	0	↑	1	-	1	1	0	0	0	1	0	0	(C4h)	In partial mode + Full
		1	↑	1	-	DCC9	DCC8	SAPC2	SAPC1	SAPC0	APC2	APC1	APC0		APC: adjust the operational amplifier
		1	↑	1	-	DCC7	DCC6	DCC5	DCC4	DCC3	DCC2	DCC1	DCC0		DCC: adjust the booster circuit for Idle mode
VMCTR1	10.2.11	0	↑	1	-	1	1	0	0	0	1	0	1	(C5h)	VCOM control 1
		1	↑	1	-	-	-	VCOMS5	VCOMS4	VCOMS3	VCOMS2	VCOMS1	VCOMS0		VCOM voltage control
VMOFCTR	10.2.12	0	↑	1	-	1	1	0	0	0	1	1	1	(C7h)	Set VCOM offset control
		1	↑	1	-	-	-	-	VMF4	VMF3	VMF2	VMF1	VMF0		
WRID2	10.2.13	0	↑	1	-	1	1	0	1	0	0	0	1	(D1h)	Set LCM version code
		1	↑	1	-	-	ID2[6]	ID2[5]	ID2[4]	ID2[3]	ID2[2]	ID2[1]	ID2[0]		

"-": Don't care

Note 1: C0h to C7h are fixed for about power controller

Panel Function Command List (3)

Instruction	Refer	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
WRID3	10.2.14	0	↑	1	-	1	1	0	1	0	0	1	0	(D2h)	Customer Project code
		1	↑	1	-	ID37	ID36	ID35	ID34	ID33	ID32	ID31	ID30		Set the project code at ID3
NVCTR1	10.2.15	0	↑	1	-	1	1	0	1	1	0	0	1	(D9)	NVM control status
		1	↑	1	-	0	VMF_EN	ID2_EN	0	0	0	0	EXT_R		
NVCTR2	10.2.16	0	↑	1	-	1	1	0	1	1	1	1	0	(DEh)	NVM Read Command
		1	↑	1	-	1	1	1	1	0	1	0	1	F5	
		1	↑	1	-	1	0	1	0	0	1	0	1	A5	Action code
NVCTR3	10.2.17	0	↑	1	-	1	1	0	1	1	1	1	1	(DFh)	NVM Write Command
		1	↑	1	-	NVM_CMD7	NVM_CMD6	NVM_CMD5	NVM_CMD4	NVM_CMD3	NVM_CMD2	NVM_CMD1	NVM_CMD0		
		1	↑	1	-	1	0	1	0	0	1	0	1	A5	Action code

"-": Don't care

Note 1: The D1h to D3h registers are fixed for about ID code setting.

Note 2: The D9h, DEh and DFh registers are used for NV Memory function controller. (Ex: write, clear, etc.)

Panel Function Command List (4)

Instruction	Refer	D/CX	WRX	RDX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex	Function
GAMCTRP1	10.2.18	0	↑	1	-	1	1	1	0	0	0	0	0	(E0h)	Set
		1	↑	1	-	---	---	VRFP[5]	VRFP[4]	VRFP[3]	VRFP[2]	VRFP[1]	VRF0P[0]		Gamma adjustment (+ polarity)
		1	↑	1	-	---	---	VOS0P[5]	VOS0P[4]	VOS0P[3]	VOS0P[2]	VOS0P[1]	VOS0P[0]		
		1	↑	1	-	---	---	PKP0[5]	PKP0[4]	PKP0[3]	PKP0[2]	PKP0[1]	PKP0[0]		
		1	↑	1	-	---	---	PKP1[5]	PKP1[4]	PKP1[3]	PKP1[2]	PKP1[1]	PKP1[0]		
		1	↑	1	-	---	---	PKP2[5]	PKP2[4]	PKP2[3]	PKP2[2]	PKP2[1]	PKP2[0]		
		1	↑	1	-	---	---	PKP3[5]	PKP3[4]	PKP3[3]	PKP3[2]	PKP3[1]	PKP3[0]		
		1	↑	1	-	---	---	PKP4[5]	PKP4[4]	PKP4[3]	PKP4[2]	PKP4[1]	PKP4[0]		
		1	↑	1	-	---	---	PKP5[5]	PKP5[4]	PKP5[3]	PKP5[2]	PKP5[1]	PKP5[0]		
		1	↑	1	-	---	---	PKP6[5]	PKP6[4]	PKP6[3]	PKP6[2]	PKP6[1]	PKP6[0]		
		1	↑	1	-	---	---	PKP7[5]	PKP7[4]	PKP7[3]	PKP7[2]	PKP7[1]	PKP7[0]		
		1	↑	1	-	---	---	PKP8[5]	PKP8[4]	PKP8[3]	PKP8[2]	PKP8[1]	PKP8[0]		
		1	↑	1		---	---	PKP9[5]	PKP9[4]	PKP9[3]	PKP9[2]	PKP9[1]	PKP9[0]		
		1	↑	1	-	---	---	SELV0P[5]	SELV0P[4]	SELV0P[3]	SELV0P[2]	SELV0P[1]	SELV0P[0]		
		1	↑	1	-	---	---	SELV1P[5]	SELV1P[4]	SELV1P[3]	SELV1P[2]	SELV1P[1]	SELV1P[0]		
		1	↑	1		---	---	SELV62P[5]	SELV62P[4]	SELV62P[3]	SELV62P[2]	SELV62P[1]	SELV62P[0]		
		1	↑	1	-	---	---	SELV63P[5]	SELV63P[4]	SELV63P[3]	SELV63P[2]	SELV63P[1]	SELV63P[0]		
GAMCTRN1	10.2.19	0	↑	1	-	1	1	1	0	0	0	0	1	(E1h)	Set
		1	↑	1	-	---	---	VRF0N[5]	VRF0N[4]	VRF0N[3]	VRF0N[2]	VRF0N[1]	VRF0N[0]		Gamma adjustment (- polarity)
		1	↑	1	-	---	---	VOS0N[5]	VOS0N[4]	VOS0N[3]	VOS0N[2]	VOS0N[1]	VOS0N[0]		
		1	↑	1	-	---	---	PKN0[5]	PKN0[4]	PKN0[3]	PKN0[2]	PKN0[1]	PKN0[0]		
		1	↑	1	-	---	---	PKN1[5]	PKN1[4]	PKN1[3]	PKN1[2]	PKN1[1]	PKN1[0]		
		1	↑	1	-	---	---	PKN2[5]	PKN2[4]	PKN2[3]	PKN2[2]	PKN2[1]	PKN2[0]		
		1	↑	1	-	---	---	PKN3[5]	PKN3[4]	PKN3[3]	PKN3[2]	PKN3[1]	PKN3[0]		
		1	↑	1	-	---	---	PKN4[5]	PKN4[4]	PKN4[3]	PKN4[2]	PKN4[1]	PKN4[0]		
		1	↑	1	-	---	---	PKN5[5]	PKN5[4]	PKN5[3]	PKN5[2]	PKN5[1]	PKN5[0]		
		1	↑	1	-	---	---	PKN6[5]	PKN6[4]	PKN6[3]	PKN6[2]	PKN6[1]	PKN6[0]		
		1	↑	1	-	---	---	PKN7[5]	PKN7[4]	PKN7[3]	PKN7[2]	PKN7[1]	PKN7[0]		
		1	↑	1	-	---	---	PKN8[5]	PKN8[4]	PKN8[3]	PKN8[2]	PKN8[1]	PKN8[0]		
		1	↑	1	-	---	---	PKN9[5]	PKN9[4]	PKN9[3]	PKN9[2]	PKN9[1]	PKN9[0]		
		1	↑	1	-	---	---	SELV0N[5]	SELV0N[4]	SELV0N[3]	SELV0N[2]	SELV0N[1]	SELV0N[0]		
		1	↑	1	-	---	---	SELV1N[5]	SELV1N[4]	SELV1N[3]	SELV1N[2]	SELV1N[1]	SELV1N[0]		
		1	↑	1	-	---	---	SELV62N[5]	SELV62N[4]	SELV62N[3]	SELV62N[2]	SELV62N[1]	SELV62N[0]		
		1	↑	1	-	---	---	SELV63N[5]	SELV63N[4]	SELV63N[3]	SELV63N[2]	SELV63N[1]	SELV63N[0]		

"-": Don't care

Note 1: E0-E1 registers are fixed for adjusting Gamma

10. AC Characteristics

Parallel interface characteristics: 18, 16, 9 or 8-bit bus (8080 series MCU interface)

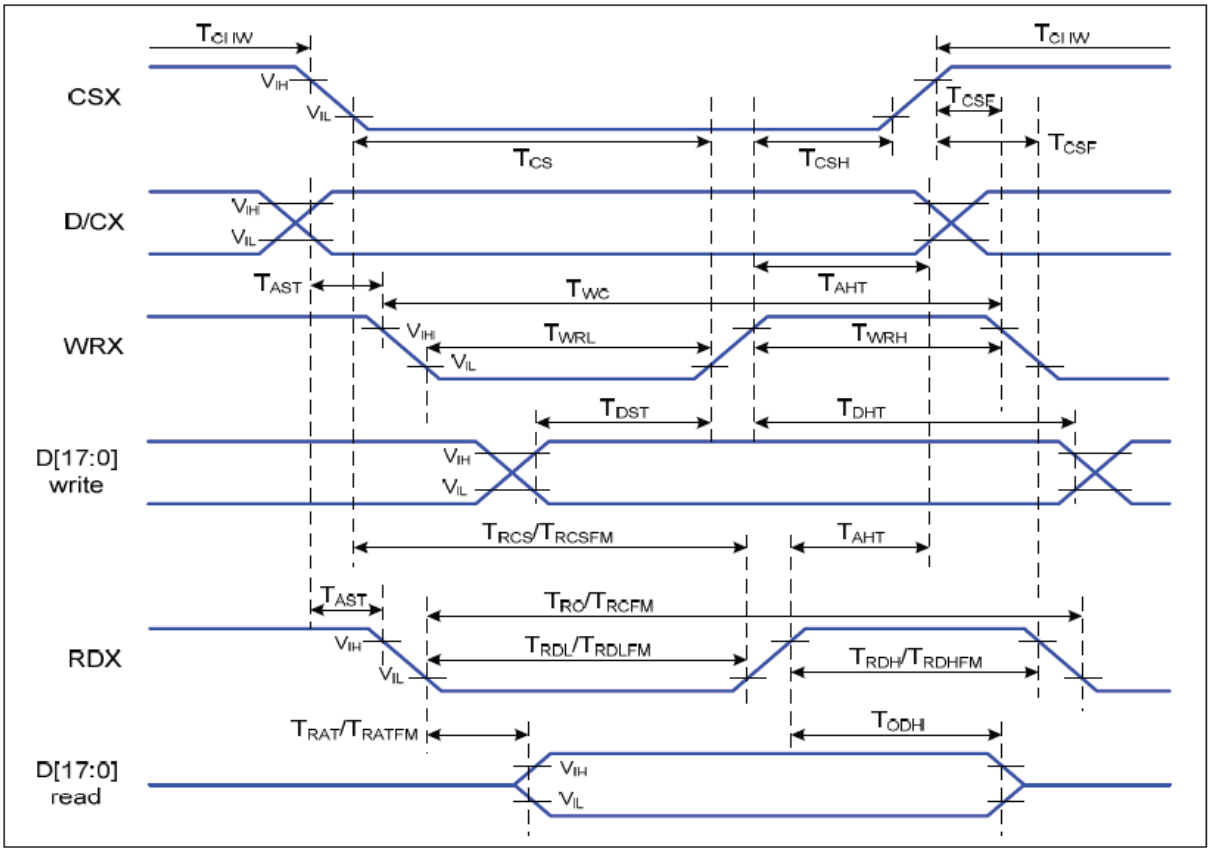


Fig. 8.1.1 Parallel interface timing characteristics (8080 series MCU interface)

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	TAST	Address setup time	10		ns	-
	TAHT	Address hold time (Write/Read)	10		ns	
CSX	TCHW	Chip select "H" pulse width	0		ns	-
	TCS	Chip select setup time (Write)	15		ns	
	TRCS	Chip select setup time (Read ID)	45		ns	
	TRCSFM	Chip select setup time (Read FM)	350		ns	
	TCSF	Chip select wait time (Write/Read)	10		ns	
	TCSH	Chip select hold time	10		ns	

WRX	TWC	Write cycle	100		ns	
	TWRH	Control pulse “H” duration	30		ns	
	TWRL	Control pulse “L” duration	30		ns	
RDX (ID)	TRC	Read cycle (ID)	160		ns	When read ID data
	TRDH	Control pulse “H” duration (ID)	90		ns	
	TRDL	Control pulse “L” duration (ID)	45		ns	
RDX (FM)	TRCFM	Read cycle (FM)	450		ns	When read from frame memory
	TRDHFM	Control pulse “H” duration (FM)	150		ns	
	TRDLFM	Control pulse “L” duration (FM)	150		ns	
D[17:0]	TDST	Data setup time	10		ns	For CL=30pF
	TDHT	Data hold time	10		ns	
	TRAT	Read access time (ID)		40	ns	
	TRATFM	Read access time (FM)		40	ns	
	TODH	Output disable time		80	ns	

Table 8.1.1 Parallel Interface Characteristics

Note: VDDI=1.65 to 3.3V, VDD=2.6 to 3.3V, AGND=DGND=0V, Ta=25 ℃

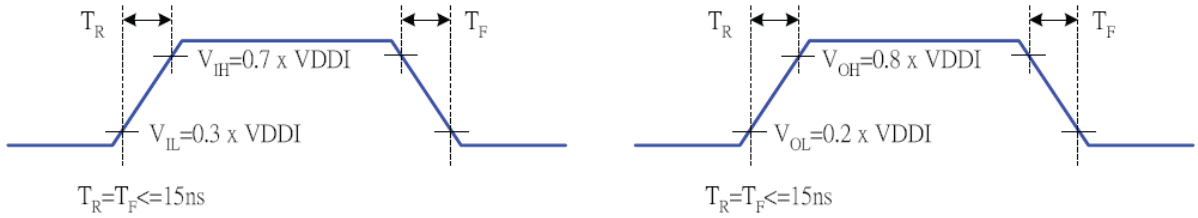


Fig. 8.1.2 Rising and falling timing for input and output signal

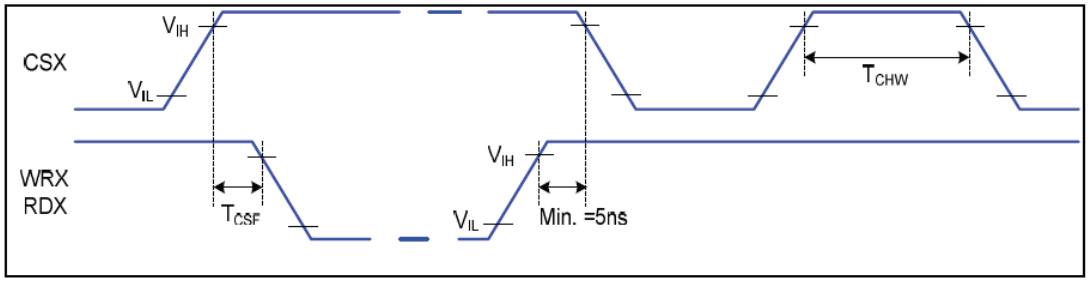


Fig. 8.1.3 Chip selection (CSX) timing

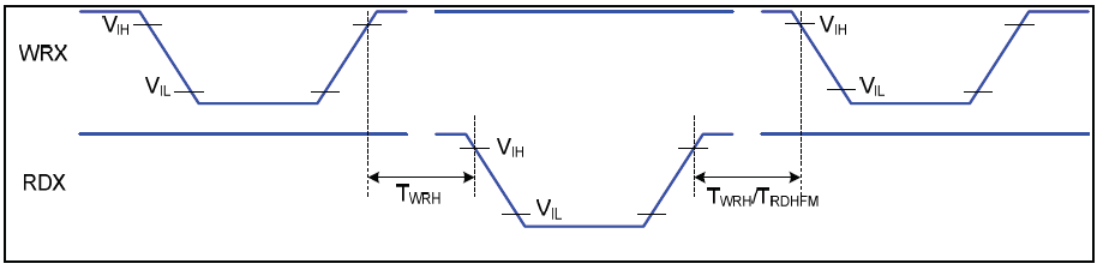


Fig. 8.1.4 Write-to-read and read-to-write timing

Note: The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.

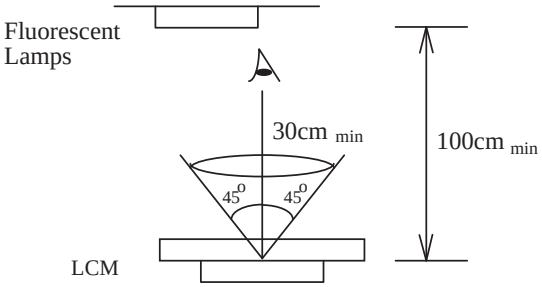
11.Quality Specifications

All The raw material are Rohs complicant.

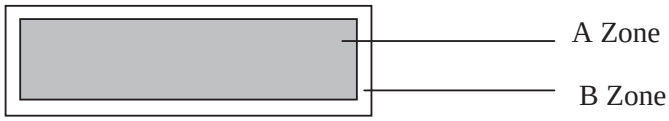
11.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: viewing area
B Zone: outside viewing area

11.2 Specification of quality assurance

AQL inspection standard

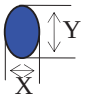
Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: * is not including)

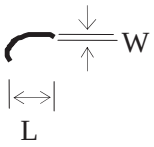
Classify	Item		Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display state	Background color deviation	2	1.0
		Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
	Polarizer	Protruded	12	
		Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

Note on defect classification

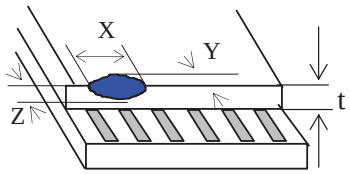
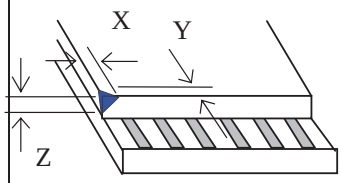
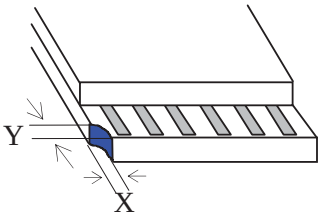
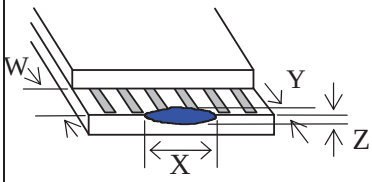
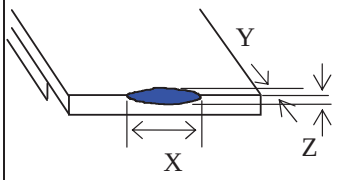
<table>
<tr><th>No.</th><th>Item</th><th>Criterion</th></tr>
<tr><td rowspan="6">1</td><td>Short or open circuit</td><td rowspan="6">Not allow</td></tr>
<tr><td>LC leakage</td></tr>
<tr><td>Flickering</td></tr>
<tr><td>No display</td></tr>
<tr><td>Wrong viewing direction</td></tr>
<tr><td>Wrong Back-light</td></tr>
<tr><td rowspan="2">2</td><td>Contrast defect</td><td rowspan="2">Refer to approval sample</td></tr>
<tr><td>Background color deviation</td></tr>
<tr><td>3</td><td>Point defect,
Black spot, dust
(including Polarizer)

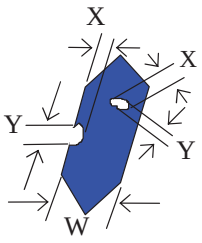
$\phi = (X+Y)/2$</td><td> 

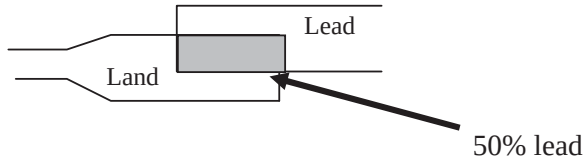
<table>
<tr><th>Point
Size</th><th>Acceptable Qty.</th></tr>
<tr><td>$\phi \leq 0.10$</td><td>Disregard</td></tr>
<tr><td>$0.10 < \phi \leq 0.20$</td><td>3</td></tr>
<tr><td>$0.20 < \phi \leq 0.25$</td><td>2</td></tr>
<tr><td>$0.25 < \phi \leq 0.30$</td><td>1</td></tr>
<tr><td>$\phi > 0.30$</td><td>0</td></tr>
</table>
Unit：mm</td></tr>
<tr><td>4</td><td>Line defect,

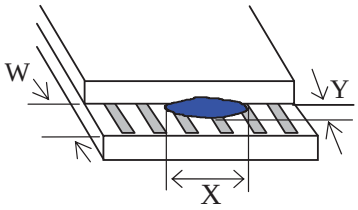
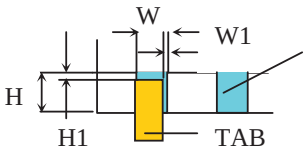
Scratch</td><td> 

<table>
<tr><th colspan="2">Line</th><th>Acceptable Qty.</th></tr>
<tr><td>L</td><td>W</td><td></td></tr>
<tr><td>---</td><td>0.015≥W</td><td>Disregard</td></tr>
<tr><td>3.0≥L</td><td>0.03≥W</td><td rowspan="2">2</td></tr>
<tr><td>2.0≥L</td><td>0.05≥W</td></tr>
<tr><td>1.0≥L</td><td>0.1＞W</td><td>1</td></tr>
<tr><td>---</td><td>0.05<W</td><td>Applied as point defect</td></tr>
</table>
Unit: mm</td></tr>
<tr><td>5</td><td>Rainbow</td><td>Not more than two color changes across the viewing area.</td></tr>
</table>

No	Item	Criterion
6	Chip Remark: X: Length direction Y: Short direction Z: Thickness direction t: Glass thickness W: Terminal Width	 Acceptable criterion X: ⩽2; Y: 0.5mm; Z: ⩽t/2  Acceptable criterion X: ⩽2; Y: 0.5mm; Z: ⩽t  Acceptable criterion X: ⩽3; Y: ⩽2; Z: ⩽t shall not reach to ITO  Acceptable criterion X: Disregard; Y: ⩽0.2; Z: ⩽t  Acceptable criterion X: ⩽5; Y: ⩽2; Z: ⩽t/3

<table>
<tr><th>No.</th><th>Item</th><th>Criterion</th></tr>
<tr><td>7</td><td>Segment
pattern
W = Segment width
ϕ = (X+Y)/2</td><td>(1) Pin hole
ϕ < 0.10mm is acceptable.
 

<table>
<tr><th>Point Size</th><th>Acceptable Qty</th></tr>
<tr><td>ϕ≤1/4W</td><td>Disregard</td></tr>
<tr><td>1/4W< ϕ≤1/2W</td><td>1</td></tr>
<tr><td>ϕ>1/2W</td><td>0</td></tr>
</table>
Unit: mm</td></tr>
<tr><td>8</td><td>Back-light</td><td>(1) The color of backlight should correspond its specification.
(2) Not allow flickering</td></tr>
<tr><td>9</td><td>Soldering</td><td>(1) Not allow heavy dirty and solder ball on PCB.
(The size of dirty refer to point and dust defect)
(2) Over 50% of lead should be soldered on Land.
 
</td></tr>
<tr><td>10</td><td>Wire</td><td>(1) Copper wire should not be rusted
(2) Not allow crack on copper wire connection.
(3) Not allow reversing the position of the flat cable.
(4) Not allow exposed copper wire inside the flat cable.</td></tr>
<tr><td>11*</td><td>PCB</td><td>(1) Not allow screw rust or damage.
(2) Not allow missing or wrong putting of component.</td></tr>
</table>

No	Item	Criterion
12	Protruded W: Terminal Width	 Acceptable criteria: Y ≤ 0.4
13	TAB	1. Position  W1≤1/3W H1≤1/3H 2 FPC bonding strength test  P (=F/FPC bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.

11.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	60°C	48	No abnormalities in functions and appearance
High temp. Operating	50°C	48	
Low temp. Storage	-10°C	48	
Low temp. Operating	0°C	48	
Humidity	40°C/ 90%RH	48	
Temp. Cycle	-10°C ← 25°C →60°C (60 min ← 5 min → 60min)	10cycles	

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (20±8°C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
3. Do not tamper in any way with the tabs on the metal frame.
4. Do not made any modification on the PCB without consulting Regal Honour.
5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
5. Only properly grounded soldering irons should be used.
6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
7. The normal static prevention measures should be observed for work clothes and working benches.
8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

1. Soldering should be performed only on the I/O terminals.
2. Use soldering irons with proper grounding and no leakage.
3. Soldering temperature: 280°C$\pm$10°C
4. Soldering time: 3 to 4 second.
5. Use eutectic solder with resin flux filling.
6. If flux is used, the LCD surface should be protected to avoid spattering flux.
7. Flux residue should be removed.

Operation Precautions:

1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
4. Response time increases with decrease in temperature.
5. Display color may be affected at temperatures above its operational range.
6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

Regal Honour LCDs and modules are not consumer products, but may be incorporated by Regal Honour's customers into consumer products or components thereof, Regal Honour does not warrant that its LCDs and components are fit for any such particular purpose.

1. The liability of Regal Honour is limited to repair or replacement on the terms set forth below. Regal Honour will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Regal Honour and the customer, Regal Honour will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Regal Honour general LCD inspection standard. (Copies available on request)
2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
3. In returning the LCD/LCM, they must be properly packaged; there should be detailed description of the failures or defect.