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SPECIFICATION FOR TFT MODULE

MODULE NO: AFS240320TG-2.8-U100001 REVISION NO: 00

Customer's Approval:	

	SIGNATURE	DATE
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DOCUMENT REVISION HISTORY

Version	DATE	DESCRIPTION	CHANGED BY
00	Dec-01-2011	First Issue	

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1. Features & Mechanical Specifications

Itom	Contents LCD TFT / Transmissive / Normal White 12 O'clock 4 Chip White LED in Series 3-wine SPI+RGB interface ILI9341 50.0(W) × 69.2(H) × 4.0(T) mm 46.2 × 64.75 × 1.0 mm 43.2 × 57.6 mm 240(RGB) × 320 0.06 × 0.180 mm 0.180 × 0.180 mm re -20 ~ +70 °C -30 ~ +80 °C	
Item LCD Type TFT / Transmissive / Normal White Viewing direction 12 O'clock Backlight 4 Chip White LED in Series Interface 3-wine SPI+RGB interface Driver IC ILI9341 Outline Dimension 50.0(W) × 69.2(H) × 4.0(T) Glass area (W×H×T) 46.2 × 64.75 × 1.0 Active area (W×H) 43.2 × 57.6 Number of Dots 240(RGB) × 320 Dot pitch (W×H) 0.06 × 0.180 Pixel pitch (W×H) 0.180 × 0.180 Operating Temperature -20 ~ +70	Cint	
LCD Type	TFT / Transmissive / Normal White	
Viewing direction	12 O'clock	
Backlight	4 Chip White LED in Series	
Interface	3-wine SPI+RGB interface	
Driver IC	ILI9341	
Outline Dimension	$50.0(W) \times 69.2(H) \times 4.0(T)$	mm
Glass area (W×H×T)	$46.2 \times 64.75 \times 1.0$	mm
Active area (W×H)	43.2 × 57.6	mm
Number of Dots	240(RGB) × 320	
Dot pitch (W×H)	0.06 × 0.180	mm
Pixel pitch (W×H)	0.180 × 0.180	mm
Operating Temperature	-20 ~ +70	°C
Storage temperature	-30 ∼ +80	°C

2. Dimensional Outline

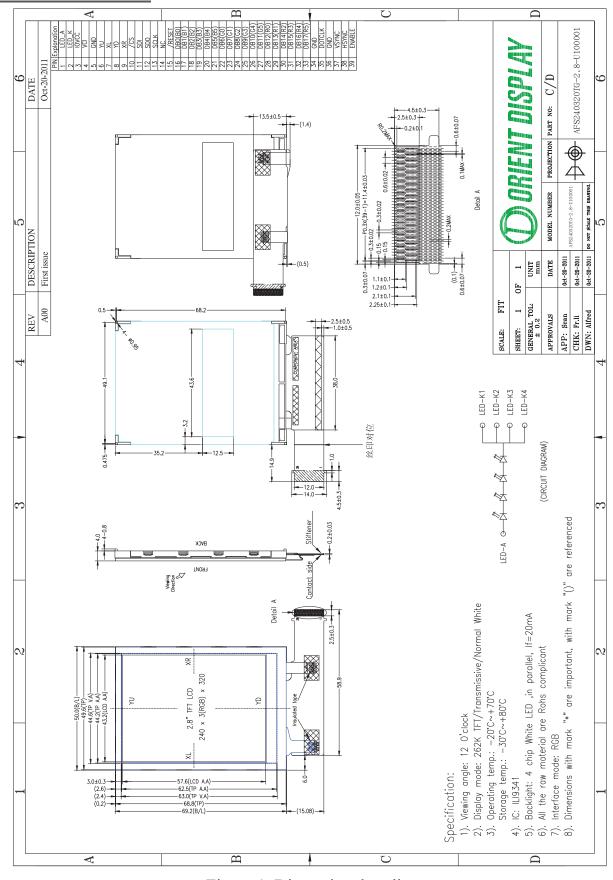


Figure 1. Dimensional outline

2. Block Diagram

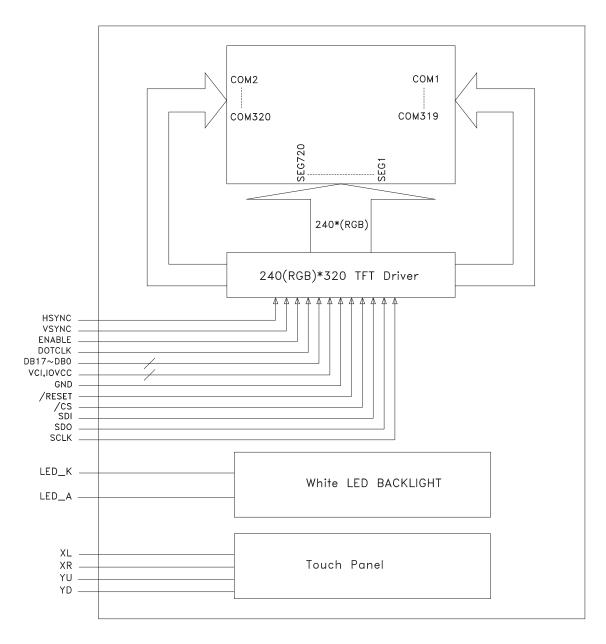


Figure 2. Block diagram

4. Pin Description

PIN No.	SYMBOL	Function
1	LED_A	Backlight LED Anode
2	LED_K	Backlight LED Cathode
3	IOVCC	power supply for interface logic circuits
4	VCI	power supply for analog circuit blocks
5	GND	Ground
6	YU	Touch Panel YU Position
7	XL	Touch Panel XL Position
8	YD	Touch Panel YD Position
9	XR	Touch Panel XR Position
10	/CS	Chip select input pin ("Low" enable).
11	SDI	Serial input signal.
12	SDO	Serial output signal.
13	SCLK	Serial clock
14	NC	No Connection
15	/RESET	Reset pin. (Active Low)
16~33	DB0~DB17	Data Bus
34	GND	Ground
35	DOTCLK	Dot clock signal for RGB interface operation
36	GND	Ground
37	VSYNC	Frame synchronizing signal for RGB interface operation
38	HSYNC	Line synchronizing signal for RGB interface operation.
39	ENABLE	Data enable signal for RGB interface operation.

5. Absolute Maximum Ratings

Item	Symbol	Rating	Unit
Supply Voltage range	VCI	-0.3 to +4.6	V
Supply Voltage range	IOVCC	-0.3 to +4.6	V
Operating Temperature range	TOP	-20 to +70	$^{\circ}$ C
Storage Temperature range	TST	-30 to +80	$^{\circ}$ C

6. Electrical Characteristics

DC Characteristics

Item	Symbol	Min.	Type.	Max.	Unit
Logic Supply Voltage	VCI	2.5	2.8	3.3	V
I/O Supply Voltage	IOVCC	1.65	2.8	3.3	V

7. Backlight Characteristics

White LED \times 4 in Series

$(Ta = 25^{\circ})$	C)
---------------------	----

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	VF	IF = 20mA	-	12.8	-	V
Uniformity	△Bp	-	80	1	ı	%
Luminance for LCD	Lv	IF = 20mA	3000	1	ı	cd/m ²

8. Electro-Optical Characteristics

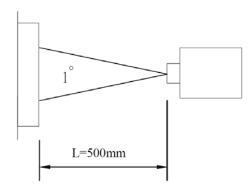
(Taransmittance · contrast · RT · viewing angle results are using CPT LC+ CPT EWV

Polarizer +Corresponding Backlight, reference only)

ITEM			CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Transmitt	ance	Т			(5.8)		%	
Contrast I	Ratio	CR	θ =ψ= 0°		(300)			Note 3
Response	Time	T CR Tr+Tf θ x y Y x y Y x y Y	θ =ψ= 0°		(25)		ms	Note 4
	Upper	Д			(60)		0	
Viewing	Down		CR≧10		(55)		۰	Note 5
Angle R	Right		CR≦10		(50)		0	Note 5
	Left] Ψ			(50)		۰]
	White	Х		(0.288)	(0.308)	(0.328)		
		y Y	$\theta = \phi = 0^{\circ}$	(0.319)	(0.339)	(0.359)		
				(27.1)	(30.1)	(33.1)		
		Х	θ=φ= 0°	(0.632)	(0.652)	(0.672)		
	Red	y Y		(0.311)	(0.331)	(0.351)		
				(14.8)	(17.8)	(20.8)		
Color Filter		Х		(0.294)	(0.314)	(0.334)		Note 6
Chromacicity	Green	у	$\theta = \phi = 0^{\circ}$	(0.555)	(0.575)	(0.595)		11016 0
		Υ		(53.7)	(57.7)	(61.7)]
		Х		(0.118)	(0.138)	(0.158)]
	Blue	у	$\theta = \phi = 0$ °	(0.112)	(0.132)	(0.152)		
		Y		(11.8)	(14.8)	(17.8)]
	NTSC			1	(61)		%	

Note 1. Ambient condition : $25^{\circ}C \pm 2^{\circ}C$, $60\pm 10\%RH$, under 10 Lunx in the darkroom \circ Lighting the LCM and measuring after 10 minutes.

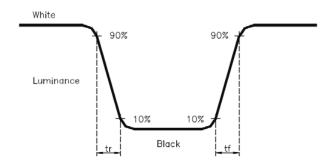
Note 2.Measure device : BM-5AS (TOPCON) , viewing cone=1° , I_{Led}=20 mA ∘



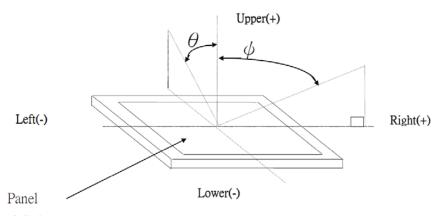
Note 3. Definition of Contrast Ratio :

CR = White Luminance (ON) / Black Luminance (OFF)

Note 4. Definition of response time: The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 5. Definition of view angle(θ , ψ):



Note 6. Light source: C light.

9. Instruction Description

egulative Command Set Command Function	D/CX	BDY	WRX	D17-8	D7	D6	D5	D4	D3	D2	D1	Do	Не
No Operation	0	1	VV HA	XX	0	0	0	0	0	0	0	0	00
Software Reset	0	1	1	XX	0	0	0	0	0	0	0	1	01
Software Neset	0	1	+	XX	0	0	0	0	0	1	0	0	04
	1	<u> </u>	1	XX	X	X	X	X	X	X	X	X	X
Read Display Identification	1	1	1	XX	_ ^	, A		ID1 [X
Information	1	1	1	XX	ID2 [7:0]						X		
	1	+	1	XX				ID3 [_				X
	0	1	1	XX	0	0	0	0	1	0	0	1	0
	1	<u>,</u>	1	XX	X	X	X	X	X	X	X	X	X
	1	1	1	XX	<u> </u>			[31:25]				X	(
Read Display Status	1	1	1	XX	X		D [22:20			D [1	9:16]		
	1	+	1	XX	X	X	X	Х	X	_	D [10:8]		
	1	1	1	XX		D [7:5]		X	X	Х	X	Х	
	0	1	<u>,</u>	XX	0	0	0	0	1	0	1	0	0,
Read Display Power Mode	1	<u> </u>	1	XX	X	X	X	X	X	X	X	X	>
ricad Diopidy i ower wiede	1	1	1	XX	_ ^		D [7				0	0	(
	0	1	<u>†</u>	XX	0	0	0	0	1	0	1	1	0
Read Display MADCTL	1	<u> </u>	1	XX	X	X	X	X	X	X	X	X	>
riodd Diopidy Windows	1	+	1	XX	<u> </u>		D [7			_ ^	0	0	
	0	1	·	XX	0	0	0	0	1	1	0	0	0
Read Display Pixel Format	1	<u>†</u>	1	XX	X	X	X	X	X	X	X	X	>
riedu Dispilay i ixer i offilat	1	<u></u>	1	XX	RIM		DPI [2:0		X		DBI [2:0]		
	0	1	<u>†</u>	XX	0	0	0	0	1	1	0	1	0
Read Display Image Format	1	<u> </u>	1	XX	X	X	X	X	X	X	X	X	>
	1	<u></u>	1	XX	X	X	X	X	X	^	D [2:0]	_ ^	
	0	1	1	XX	0	0	0	0	1	1	1	0	0
Read Display Signal Mode	1	<u> </u>	1	XX	X	X	X	X	X	×	X	X	>
riedu Dispidy Olgital Mode	1	1	1	XX	_^		D [7			_ ^	0	0	
	0	1	1	XX	0	0	0	0	1	1	1	1	0
Read Display Self-Diagnostic	1	<u>†</u>	1	XX	X	X	X	X	X	X	X	X	>
Result	1	1	1	XX	D [7		X	X	X	x	X	X	
Enter Sleep Mede	0	1	1	XX	0	0	0	1	0	0	0	0	1
Enter Sleep Mode		1				0		1	0		0		_
Sleep OUT	0			XX	0		0			0		1	1
Partial Mode ON	0	1	1	XX	0	0	0	1	0	0	1	0	1
Normal Display Mode ON	0	1	1	XX	0	0	0	1	0	0	1	1	1
Display Inversion OFF	0	1	1	XX	0	0	1	0	0	0	0	0	2
Display Inversion ON	0	1	1	XX	0	0	1	0	0	0	0	1	2
Gamma Set	0	1	1	XX	0	0	1	0 0	0	1	1	0	2
D: 1 OFF	1	1	1	XX	GC [7:0]						(
Display OFF	0	1	1	XX	0	0	1	0	1	0	0	0	2
Display ON	0	1	1	XX	0	0	1	0	1	0	0	1	2
	0	1	1	XX	0	0	1	0	1	0	1	0	2
	1	1	1	XX				SC [1)
Column Address Set	1	1	1	XX	-			SC [)
	1	1	1	XX				EC [1)
	1	1	1	XX				EC [)
	0	1	1	XX	0	0	1	0	1	0	1	1	2
	1	1	1	XX				SP [1					>
Page Address Set	1	1	1	XX				SP [7:0]				>
	1	1	1	XX				EP [1	5:8]				>
	1	1	1	XX				EP [7:0])

Memory Write	0	1	1	XX	0	0	1	0 [17:0]	1	1	0	0	2Ch
		1	1	VV		_			-	_			_
	0	1	1	XX	0	0	1	0	1	1	0	1	2DI
	1	1	1	XX	-					00 [5:0]			XX
	1	1	1	XX	Rnn [5:0]								XX
	1	1	1	XX	-	-				31 [5:0]			XX
Color SET	1	1	1	XX	-					00 [5:0]			XX
	1	1	1	XX		_				nn [5:0]			XX
	1	1	1	XX	-					64 [5:0]			XX
	11	1	1	XX		_				00 [5:0]			XX
	1	1	1	XX						nn [5:0]			XX
	1	1	1	XX					В	31 [5:0]			XX
	0	1	1	XX	0	0	1	0	1	1	1	0	2Eh
Memory Read	1	1	1	XX	X	X	X	X	X	X	X	X	XX
	1	1	1					[17:0]					XX
	0	1	1	XX	0	0	1	1	0	0	0	0	30h
	1	1	1	XX				SI	R [15:8]				00
Partial Area	1	1	1	XX				S	R [7:0]				00
	1	1	1	XX				El	R [15:8]				01
	1	1	1	XX				E	R [7:0]				3F
	0	1	1	XX	0	0	1	1	0	0	1	1	33h
	1	1	Î	XX				TF	A [15:8]				00
	1	1	1	XX				TI	FA [7:0]				00
Vertical Scrolling Definition	1	1	1	XX	VSA [15:8]								01
	1	1	1	XX				V	SA [7:0]				40
	1	1	1	XX					A [15:8]				00
	1	1	1	XX					FA [7:0]				00
Tearing Effect Line OFF	0	1	1	XX	0	0	1	1	0	1	0	0	34h
-	0	1	<u> </u>	XX	0	0	1	1	0	1	0	1	35h
Tearing Effect Line ON	1	1	1	XX	X	X	Х	X	X	Х	X	М	00
	0	1	1	XX	0	0	1	1	0	1	1	0	36h
Memory Access Control	1	1	1	XX	MY	MX	MV	ML	BGR	МН	X	X	00
	0	1	1	XX	0	0	1	1	0	1	1	1	37h
Vertical Scrolling Start Address	1	1	+	XX	_			_	P [15:8]				00
tottoal coloning ctart / taal coo	1	1	1	XX					SP [7:0]				00
Idle Mode OFF	0	1		XX	0	0	1	1	1	0	0	0	38h
Idle Mode ON	0	1		XX	0	0	1	1	1	0	0	1	39h
Idle Mode ON	0	1	1	XX	0	0	1	1	1	0	1	0	3Ah
Pixel Format Set	1	1								0	DBI [2:0		
		_	<u>↑</u>	XX	0 0	0	DPI [2:0		X	-		1	66
Write Memory Continue	0	1		XX	0	U		1	1	1	0	0	3Ch
	1	1	1					[17:0]				_	XX
Bood Mamari Carthern	0	1	1	XX	0	0	1	1	1	1	1	0	3Eh
Read Memory Continue	1	1	1	XX	X	Х	X	X	X	X	X	X	XX
	1	1	1	101	_			[17:0]	-				XX
	0	1	1	XX	0	1	0	0	0	1	0	0	44h
Set Tear Scanline	1	1	1	XX	X	X	X	X	X	X	X	STS [8]	00
	1	1	1	XX			I		TS [7:0]	I	1		00
	0	1	1	XX	0	1	0	0	0	1	0	1	45h
Get Scanline	1	1	1	XX	X	Х	Х	X	X	Х	X	X	XX
GOL GOGIIIIIO	1	1	1	XX	X	X	X	X	X	X	GTS	6 [9:8]	00
	1	1	XX				G	TS [7:0]				00	
Write Dieplay Brightness	0	1	1	XX	0	1	0	1	0	0	0	1	51h
Write Display Brightness	1	1	1	XX				DI	BV [7:0]				00

	0	1	1	XX	0	1	0	1	0	0	1	0	52h
Read Display Brightness	1	1	1	XX	X	X	X	Х	Х	Х	X	Х	XX
	1	1	1	XX		DBV [7:0]							00
Write CTRL Display	0	1	1	XX	0	1	0	1	0	0	1	1	53h
Write CTHL Display	1	1	1	XX	X	Χ	BCTRL	X	DD	BL	X	Х	00
	0	1	1	XX	0	1	0	1	0	1	0	0	54h
Read CTRL Display	1	1	1	XX	X	X	X	X	X	X	X	X	XX
	1	1	1	XX	X	X	BCTRL	X	DD	BL	X	X	00
Write Content Adaptive	0	1	1	XX	0	1	0	1	0	1	0	1	55h
Brightness Control	1	1	1	XX	Х	X	Х	X	X	X	C[1:0]	00
Deed Content Adentics	0	1	1	XX	0	1	0	1	0	1	1	0	56ł
Read Content Adaptive Brightness Control	1	1	1	XX	X	X	X	X	X	X	X	X	XX
Brighthess Schiller	1	1	1	XX	Х	X	X	X	X	X	C[1:0]	00
Write CABC Minimum	0	1	1	XX	0	1	0	1	1	1	1	0	5EI
Brightness	1	1	1	XX				CMB	[7:0]				00
Read CABC Minimum	0	1	1	XX	0	1	0	1	0	1	1	1	5FI
Brightness	1	1	1	XX	X	X	X	X	X	X	X	X	XX
Diigitalooo	1	1	1	XX				CMB	[7:0]				00
	0	1	1	XX	1	1	0	1	1	0	1	0	DA
Read ID1	1	1	1	XX	X	X	X	X	X	X	X	X	XX
	1	1	1	XX			Modu	ıle's Mar	nufacture	e [7:0]			XX
	0	1	1	XX	1	1	0	1	1	0	1	1	DBI
Read ID2	1	1	1	XX	X	X	X	Х	X	Х	X	Х	XX
	1	1	1	XX			LCD Mo	dule / Dr	iver Ver	sion [7:0]		XX
	0	1	1	XX	1	1	0	1	1	1	0	0	DC
Read ID3	1	1	1	XX	X	X	X	X	X	Х	X	X	XX
	1	1	1	XX			LCD	Module /	Driver II	D [7:0]			XX

Extended Command Set													
Command Function	D/CX	RDX	WRX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	Hex
RGB Interface	0	1	1	XX	1	0	1	1	0	0	0	0	B0h
Signal Control	1	1	1	XX	ByPass_MODE	RCM	[1:0]	X	VSPL	HSPL	DPL	EPL	40
Frame Control	0	1	1	XX	1	0	1	1	0	0	0	1	B1h
(In Normal Mode)	1	1	1	XX	X	Χ	X	X	X	X	DIVA	[1:0]	00
(III Nollilai Mode)	1	1	1	XX	Х	Χ	X		F	TNA [4:0	0]		1B
Frame Control	0	1	1	XX	1	0	1	1	0	0	1	0	B2h
(In Idle Mode)	1	1	1	XX	X	Χ	Х	Х	X	X	DIVE	[1:0]	00
(III Idle Mode)	1	1	1	XX	X	Χ	X	RTNB [4:0]				1B	
Frame Control	0	1	1	XX	1	0	1	1	0	0	1	1	B3h
(In Partial Mode)	1	1	1	XX	X	Χ	X	X	X	X	DIVO	[1:0]	00
(III Fartial Mode)	1	1	1	XX	X	Χ	X		R	TNC [4:0	0]		1B
Dieplay Inversion Central	0	1	1	XX	1	0	1	1	0	1	0	0	B4h
Display Inversion Control	1	1	1	XX	X	Χ	Х	Х	X	NLA	NLB	NLC	02
	0	1	1	XX	1	0	1	1	0	1	0	1	B5h
	1	1	1	XX	0				VFP [6:	0]			02
Blanking Porch Control	1	1	1	XX	0	VBP [6:0]			02				
	1	1	1	XX	0	0	0			HFP [4:0]		0A
	1	1	1	XX	0	0	0			HBP [4:0)]		14

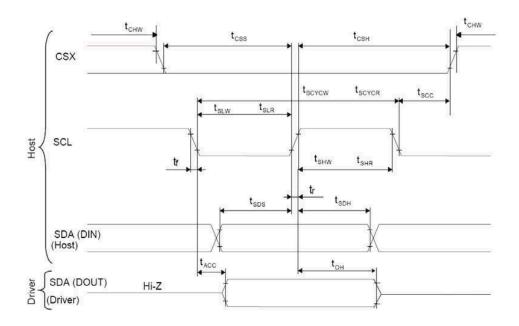
	0	1	1	XX	1	0	1	1	0	1	1	0	B6
Disales Frankis Control	1	1		XX	X	X	X	X	PIG	[1:0]		[1:0]	0/
Display Function Control	1	1	1	XX	REV							82	
	1	1		XX	X	X NL [5:0] X PCDIV [5:0]						27 XX	
	0	1	1	XX	1 X	0 0	1	1	0	אוטכ <u>ן</u> 1	1	1	B7
Entry Mode Set	1	1		XX	X	X	X	X	0	GON	DTE	GAS	07
	0	1	<u></u>	XX	1	0	1	1	1	0	0	0	B8
Backlight Control 1	1	1	<u> </u>	XX	X	X	X	X	X	X	X	X	X
	1	1	1	XX	X	X	X	X			LUI [3:0]		04
	0	1	1	XX	1	0	1	1	1	0	0	1	Bs
Backlight Control 2	1	1	1	XX	X	×	Х	X	Х	X	Х	Х	X
	1	1	1	XX		TH_MV	[3:0]			TH	_ST [3:0]		В
	0	1	1	XX	1	0	1	1	1	0	1	0	ВА
Backlight Control 3	1	1	1	XX	X	X	Х	X	Х	X	Х	X	X
	1	1	1	XX	X	X	Х	X		DTI	H_UI [3:0]		04
	0	1	1	XX	1	0	1	1	1	0	1	1	BE
Backlight Control 4	1	1	1	XX	X	X	X	X	X	X	X	X	X
	1	1	1	XX		DTH_M	V [3:0]			DTH	LST [3:0]		C
	0	1	1	XX	1	0	1	1	1	1	0	0	BC
Backlight Control 5	1	1	1	XX	X	X	X	X	X	X	X	X	X
	1	1	1	XX		DIM2	[3:0]		X		DIM1 [2:	[0]	44
Backlight Control 7	0	1	1	XX	1	0	1	1	1	1	1	0	BE
Bashingth Common 7	1	1	1	XX				PWM	_DIV [7	:0]			OF
Backlight Control 8	0	1	1	XX	1	0	1	1	1	1	1	1	BF
	1	1		XX	X	X	X	X	X		LEDONPOL	LEDPWMOPL	. 00
Power Control 1	0	1		XX	1	1	0	0	0	0	0	0	CO
	1	1	1	XX	X	X				/RH [5:0]			26
Power Control 2	0	1	1	XX	1	1	0	0	0	0	0	1	C1
	1	1	1	XX	X	X	X	Х	Х		BT [2:	T*	00
	0	1		XX	1	1	0	0	0	1	0	1	C5
VCOM Control 1	1	1		XX	X				VMH	• •			3
	1	1	1	XX	X				VML				30
VCOM Control 2	0	1		XX	1	1	0	0	0	1	1	1	C7
	1	1		XX	nVM				VMF				C
NN/ NA	0	1	1	XX	1	1	0	1	0	0	0	0	DO
NV Memory Write	1	1	1	XX	X	X	X	X	X		GM_ADR	[2:0]	00
	0	1	1	XX	-			PGM_ 1	DATA [X
		1	1	XX	1	1	0		0	0	0	1	D1
NV Memory Protection Key	1	1		XX	+				/ [23:16 / [15:0]				5:
	1	1	<u></u>	XX	+				Y [15:8]				6
	0	1	T	XX	1	1	0	1	Y [7:0] 0	0	1	0	D2
	1	1	1	XX	X	X	X	X	X	X	X	X	X
NV Memory Status Read	1	1	1	XX	X		CNT		X		D1_CNT		X
	1	<u> </u>	1	XX	BUSY		_CNT		X		D3_CNT		X

	0	1	1	XX	1	1	0	-1	0	0	1	1	D3I
	1	1	1	XX	X	X	X	X	X	Х	X	X	XX
Read ID4	1	1	1	XX	0	0	0	0	0	0	0	0	00
	1	1	1	XX	1	0	0	1	0	0	1	1	93
	1	1	1	XX	0	1	0	0	0	0	0	1	41
	0	1	1	XX	1	1	1	0	0	0	0	0	EO
	1	1	1	XX	X								08
	1	1	1	XX	X	X		30.	VP1 [5		- 00 - XX		OE
	1	1	1	XX	X	X		Mil	VP2 [5	-			12
	1	1	1	XX	X	×	X	X		VP	4 [3:0]		05
	1	1	1	XX	X	X	X		V	P6 [4	W. C. C. C. C. C.		03
	1	1	1	XX	X	X	X	X		VP	3 [3:0]		09
Positive Gamma	1	1	1	XX	×			V	P20 [6:0]				47
Correction	1	1	1	XX		VP36	[3:0]			VP2	27 [3:0]		86
	1	1	†	XX	X		7	V	P43 [6:0]				28
	1	1	+	XX	X	X	X	Х		VPS	50 [3:0]		OE
	1	1	1	XX	X	X	X		VF	257 [4	1:0]		04
	1	1	1	XX	Х	х	X	х		25000000	[0:8]		00
	1	1	1	XX	X	×			VP61 [5	5:0]			00
	1	1	1	XX	X	X			VP62 [5	5:0]			00
	1	1	1	XX	X	X	X	X		VP	3 [3:0]		00
	0	1	†	XX	1	1	1	0	0	0	0	1	E1
	1	1	1	XX	X	X	X	Х		VN	0 [3:0]		08
	1	1	1	XX	X	X			VN1 [5	:0]			1/
	1	1	1	XX	Х	X			VN2 [5	6:0]			20
	1	1	1	XX	X	X	X	X		VN	4 [3:0]		07
	1	1	1	XX	X	X	X		V	N6 [4	:0]		OE
	1	1	1	XX	X	X	X	X		VN1	3 [3:0]		05
Negative Gamma	1	1	1	XX	X			V	N20 [6:0]	-			34
Correction	1	1	1	XX		VN36	[3:0]			VN	27 [3:0]		8/
	1	1	1	XX	X	V	50 82-	٧	N43 [6:0]				40
	1	1	1	XX	X	X	Х	Х		VNS	50 [3:0]		04
	1	1	22.	XX	X	X	Х		٧V	N57 [4	37.59		18
	1	1		XX	X	X	Х	Х		VNS	9 [3:0]		OF
	1	1	+	XX	X	X		100	VN61 [5	5:0]	-557-71		3F
	1	1	1	XX	X	X			VN62 [5	5:0]			3F
	1	1	1	XX	X	X	X	X		VNE	63 [3:0]		OF
Digital Gamma Control 1	0	1	†	XX	1	1	1	0	0	0	1	0	E2
1 st Parameter	1	1	1	XX		RCA0	[3:0]			BCA	0 [3:0]	98	X
:	1	1	1	XX		RCAx	The section is a section of the sect			IN THE PARTY.	1x [3:0]		XX
16 th Parameter	1	1	1	XX		RCA15	Citional -			- 10 top 1	15 [3:0	No.	X
Digital Gamma Control 2	0	1	†	XX	1	1	1	0	0	0	1	1	E3
1 st Parameter	1	1	1	XX		RFAO	[3:0]			BFA	0 [3:0]		X
	1	1	1	XX		RFAx	[3:0]			BFA	4x [3:0]		XX
64 th Parameter	1	1	1	XX		RFA63				DOMESTIC OF	63 [3:0	1	X
	0	1	1	XX	1	1	1	1	0	1	1	0	F6
	1	1	1	XX	MY_EOR	MX_EOR	MV_EOR	Х	BGR_EOR	Х	X	WEMODE	01
Interface Control	1	1	1	XX	X	х	EPF [1:0]	X	Х	ME	OT [1:0]	00
	1	1	+	XX	X	X	ENDIAN	X	DM [1:		RM	RIM	00

Note 1: Undefined commands are treated as NOP (00h) command.

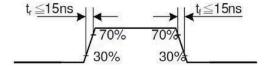
10. AC Characteristics

10.1 Serial data Transfer Interface Characteristics

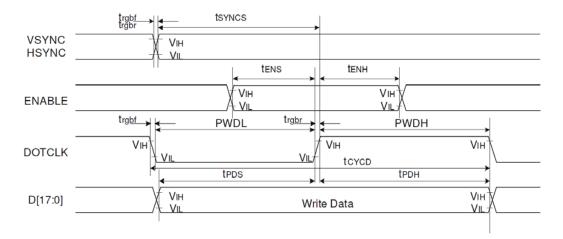


Signal	Symbol	Parameter	min	max	Unit	Description
	tscycw	Serial Clock Cycle (Write)	100	12	ns	
	tshw	SCL "H" Pulse Width (Write)	40	-	ns	
SCL	tslw	SCL "L" Pulse Width (Write)	40	- 12	ns	
SCL	tscycr	Serial Clock Cycle (Read)	150	15	ns	
	tshr	SCL "H" Pulse Width (Read)	60	- 3	ns	
	tslr	SCL "L" Pulse Width (Read)	60	12	ns	
SDA/SDI	tsds	Data setup time (Write)	30	-	ns	
(Input)	tsdh	Data hold time (Write)	30	25	ns	
SDA/SDO	tacc	Access time (Read)	10	15	ns	
(Output)	toh	Output disable time (Read)	10	50	ns	
	tscc	SCL-CSX	20	74	ns	
CCV	tchw	CSX "H" Pulse Width	40	12	ns	
CSX	tcss	CSX-SCL Time	60		ns	
	tcsh	CSX-SCL TITTE	65		ns	

Note: Ta = 25 °C, VDDI=1.65V to 3.3V, VCI=2.5V to 3.3V, AGND=VSS=0V

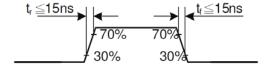


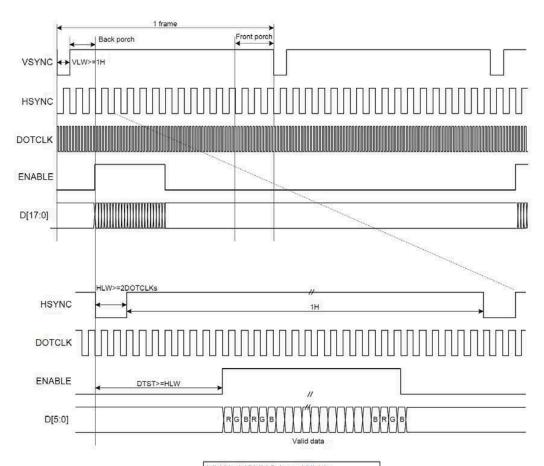
10.2 RGB Interface Characteristics



Signal	Symbol	Parameter	min	max	Unit	Description
VSYNC /	tsyncs	VSYNC/HSYNC setup time	15	-	ns	
HSYNC	tsynch	VSYNC/HSYNC hold time	15		ns	
DE	t _{ENS}	DE setup time	15	-	ns	
DE	t _{ENH}	DE hold time	15	-	ns	
D[17:0]	tpos	Data setup time	15	-	ns	18/16-bit bus RGB
D[17.0]	t _{PDH}	Data hold time	15	-	ns	interface mode
	PWDH	DOTCLK high-level period	15	-	ns	
DOTCLK	PWDL	DOTCLK low-level period	15	•	ns	
DOTCER	tcycp	DOTCLK cycle time	100	-	ns	
	t _{rgbr} , t _{rgbf}	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns	
VSYNC /	tsyncs	VSYNC/HSYNC setup time	15	-	ns	
HSYNC	tsynch	VSYNC/HSYNC hold time	15		ns	
DE	t _{ENS}	DE setup time	15	-	ns	
DE	t _{ENH}	DE hold time	15	1	ns	
D[17:0]	t _{POS}	Data setup time	15	1	ns	6-bit bus RGB
D[17:0]	t _{PDH}	Data hold time	15	,	ns	interface mode
	PWDH	DOTCLK high-level pulse period	15	-	ns	
DOTCLK	PWDL	DOTCLK low-level pulse period	15	٠	ns	
DOTOLK	tcyco	DOTCLK cycle time		-	ns	
	t _{rgbr} , t _{rgbf}	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns	

Note: Ta = -30 to 70 °C, VDDI=1.65V to 3.3V, VCI=2.5V to 3.3V, AGND=VSS=0V





VLW: VSYNC Low Width HLW : HSYNC Low Width DTST : Data Transfer Startup Time

Parameters	Symbols	Condition	Min.	Тур.	Max.	Units
Horizontal Synchronization	Hsync		2	10	16	DOTCLK
Horizontal Back Porch	HBP		2	20	24	DOTCLK
Horizontal Address	HAdr		-	240	-	DOTCLK
Horizontal Front Porch	HFP		2	10	16	DOTCLK
Vertical Synchronization	Vsync		1	2	4	Line
Vertical Back Porch	VBP		1	2	-	Line
Vertical Address	VAdr		-	320	-	Line
Vertical Front Porch	VFP		3	4	-	Line

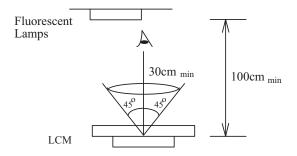
11.Quality Specifications

All The raw material are Rohs complicant.

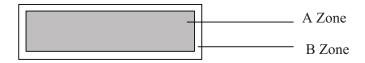
11.1 Standard of the product appearance test

Manner of appearance test: The inspection should be performed in using 20W x 2 fluorescent lamps. Distance between LCM and fluorescent lamps should be 100 cm or more. Distance between LCM and inspector eyes should be 30 cm or more.

Viewing direction for inspection is 45° from vertical against LCM.



Definition of zone:



A Zone: viewing area

B Zone: outside viewing area

11.2 Specification of quality assurance

AQL inspection standard

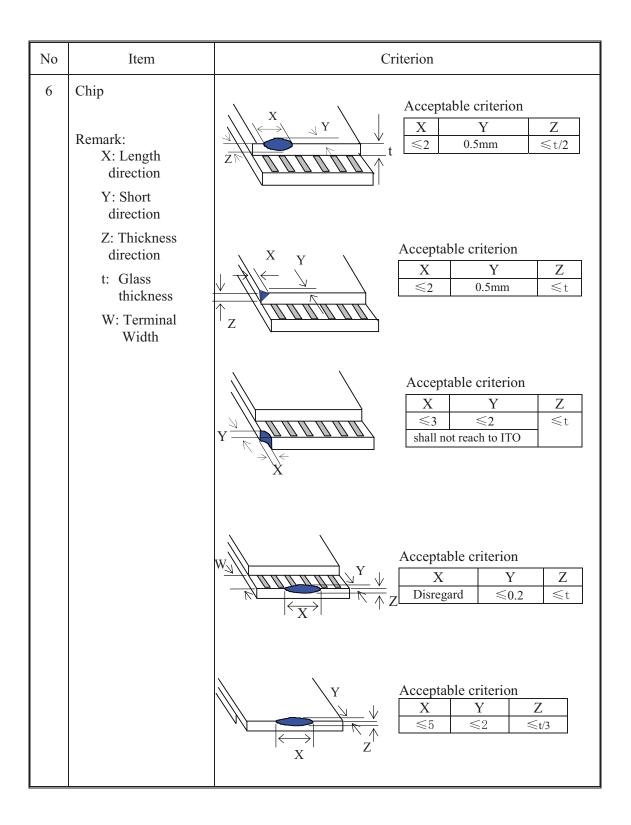
Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification (Note: * is not including)

Classify		Item	Note	AQL
Major	Display state	Short or open circuit		0.65
		LC leakage		
		Flickering	1	
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	2	
		Back-light	1,8	
	Non-display	Flat cable or pin reverse	10	
		Wrong or missing component	11	
Minor	Display state	Background color deviation	2	1.0
		Black spot and dust	3	
		Line defect, Scratch	4	
		Rainbow	5	
		Chip	6	
		Pin hole	7	
	Polarizer	Protruded	12	
		Bubble and foreign material	3	
	Soldering	Poor connection	9	
	Wire	Poor connection	10	
	TAB	Position, Bonding strength	13	

Note on defect classification

No.	Item				Criterion		
1	Short or open circuit				Not allow		
	LC leakage						
	Flickering						
	No display						
	Wrong viewing direction						
	Wrong Back-light						
2	Contrast defect		Refe	r to	approval san	nple	
	Background color deviation						
3	Point defect, Black spot, dust	↓ 			Point Size	Acceptable Qty.	
	(including Polarizer)	\ X	}		φ <u><</u> 0.10	Disregard	
			-		$.10 < \phi \le 0.20$ $.20 < \phi \le 0.25$	2	
	$\phi = (X+Y)/2$		-		.25<φ≤0.23	1	
			ŀ		φ>0.30	0	
			Uni	t:	mm		
4	Line defect,	\longrightarrow W					
	Scratch	↑ · · · · · · · · · · · · · · · · · · ·	L		Line W	Acceptable Qty.	
		⇔ T	L		0.015≥W	Disregard	
		L	3.0≥	L	0.03≥W		
			2.0>		0.05≥W	2	
			1.0≥	L	0.1>W	1	
					0.05 <w< td=""><td>Applied as point defect</td></w<>	Applied as point defect	
		Unit: mm					
5	Rainbow	Not more than two color changes across the viewing area.					



No.	Item	Criterion								
7	Segment pattern $W = \text{Segment width}$ $\phi = (X+Y)/2$	(1) Pin hole $\phi < 0.10 \text{mm is acceptable.}$ $X \longrightarrow // \searrow X$								
		Point Size Acceptable Qty								
		$Y \xrightarrow{V} Y \qquad \phi \leqslant 1/4W \qquad Disregard \\ 1/4W < \phi \leqslant 1/2W \qquad 1$								
		$ \downarrow \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \qquad \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \qquad \qquad \downarrow \qquad \qquad \qquad \qquad \qquad \downarrow \qquad \qquad$								
		Unit: mm								
8	Back-light	(1) The color of backlight should correspond its specification.								
		(2) Not allow flickering								
9	Soldering	(1) Not allow heavy dirty and solder ball on PCB.								
		(The size of dirty refer to point and dust defect)								
		(2) Over 50% of lead should be soldered on Land.								
		Lead 50% lead								
10	Wire	(1) Copper wire should not be rusted								
		(2) Not allow crack on copper wire connection.								
	(3) Not allow reversing the position of the flat cable.									
		(4) Not allow exposed copper wire inside the flat cable.								
11*	PCB	(1) Not allow screw rust or damage.								
		(2) Not allow missing or wrong putting of component.								

No	Item	Criterion
12	Protruded W: Terminal Width	Acceptable criteria: $Y \le 0.4$
13	TAB	1. Position $\begin{array}{cccccccccccccccccccccccccccccccccccc$
		2 FPC bonding strength test FPC FPC P (=F/FPC bonding width) ≥650gf/cm ,(speed rate: 1mm/min) 5pcs per SOA (shipment)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.

11.3 Reliability of LCM

Reliability test condition:

Item	Condition	Time (hrs)	Assessment
High temp. Storage	60°C	48	
High temp. Operating	50°C	48	No abnormalities in functions and appearance
Low temp. Storage	-20°C	48	
Low temp. Operating	-10°C	48	
Humidity	40°C/ 90%RH	48	
Temp. Cycle	-20°C ← 25°C →60°C	10cycles	
	$(60 \min \leftarrow 5 \min \rightarrow 60 \min)$		

Recovery time should be 24 hours minimum. Moreover, functions, performance and appearance shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ($20\pm8^{\circ}$ C), normal humidity (below 65% RH), and in the area not exposed to direct sun light.

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11.4 Precaution for using LCD/LCM

LCD/LCM is assembled and adjusted with a high degree of precision. Do not attempt to make any alteration or modification. The followings should be noted.

General Precautions:

- 1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure onto the surface of display area.
- 2. The polarizer used on the display surface is easily scratched and damaged. Extreme care should be taken when handling. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isoproply alcohol, ethyl alcohol or trichlorotriflorothane, do not use water, ketone or aromatics and never scrub hard.
- 3. Do not tamper in any way with the tabs on the metal frame.
- 4. Do not made any modification on the PCB without consulting Orient Display.
- 5. When mounting a LCM, make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- 6. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 7. Be careful not to touch or swallow liquid crystal that might leak from a damaged cell. Any liquid crystal adheres to skin or clothes, wash it off immediately with soap and water.

Static Electricity Precautions:

- 1. CMOS-LSI is used for the module circuit; therefore operators should be grounded whenever he/she comes into contact with the module.
- 2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 4. The modules should be kept in anti-static bags or other containers resistant to static for storage.
- 5. Only properly grounded soldering irons should be used.
- 6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 7. The normal static prevention measures should be observed for work clothes and working benches.
- 8. Since dry air is inductive to static, a relative humidity of 50-60% is recommended.

Soldering Precautions:

- 1. Soldering should be performed only on the I/O terminals.
- 2. Use soldering irons with proper grounding and no leakage.
- 3. Soldering temperature: 280°C+10°C
- 4. Soldering time: 3 to 4 second.
- 5. Use eutectic solder with resin flux filling.
- 6. If flux is used, the LCD surface should be protected to avoid spattering flux.
- 7. Flux residue should be removed.

Operation Precautions:

- 1. The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- 2. Since applied DC voltage causes electro-chemical reactions, which deteriorate the display, the applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 3. Driving voltage should be kept within specified range; excess voltage will shorten display life.
- 4. Response time increases with decrease in temperature.
- 5. Display color may be affected at temperatures above its operational range.
- 6. Keep the temperature within the specified range usage and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or generate bubbles.
- 7. For long-term storage over 40°C is required, the relative humidity should be kept below 60%, and avoid direct sunlight.

Limited Warranty

Orient DisplayLCDs and modules are not consumer products, but may be incorporated by Orient Display's customers into consumer products or components thereof, Orient Displaydoes not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of Orient Display is limited to repair or replacement on the terms set forth below. Orient Display will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between Orient Display and the customer, Orient Display will only replace or repair any of its LCD which is found defective electrically or visually when inspected in accordance with Orient Display general LCD inspection standard. (Copies available on request)
- 2. No warranty can be granted if any of the precautions state in handling liquid crystal display above has been disregarded. Broken glass, scratches on polarizer mechanical damages as well as defects that are caused accelerated environment tests are excluded from warranty.
- 3. In returning the LCD/LCM, they must be properlypackaged; there should be detailed description of the failures or defect.