



ALGOLTEK

AG9310-MEQ

**Type C to HDMI
Converter**

Data Sheet

Official Version

V1.0

Mar., 2019



Revision History

Version	Date	Notes
1.0	2019/3/29	First release

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I. General Description

ALGOLTEK AG9310 is a single chip solution to implement USB Type-C to HDMI data converter. With USB Type-C DisplayPort Alternate Mode supporting, AG9310 can transmit both video and audio streams from USB Type-C interface to HDMI Port. In AG9310, DisplayPort v1.2 receiver can support different configurations by 1, 2 lanes @1.62Gbps, 2.7Gbps and 5.4Gbps frequency input, and HDMI supports 4K2K@30Hz output. Product manufactures can easily implement Type-C video conversion dongles and dockings by using AG9310 application.

II. Features

- Embedded 16 bit MCU
- EDID and MCCS pass-through supported
- Supports Hot Plug Detection
- External SPI flash supported for firmware upgrade
- Embedded HDCP 1.4 supported
- 1.2V Core power and 3.3V I/O power
- Embedded 5V/1.2V high efficient Regulator
- Simultaneously display via HDMI outputs
- 2KV ESD performance
- Down Spread Spectrum Clock (SSC) supported

III. Device Information

Part Number	Package	Body Size	Note
AG9310-MEQ	QFN-48	6x6 mm ²	/wo I ² S audio output

IV. Application

- USB Type-C dongle
- USB Type-C docking station
- TV/Projector Type-C interface

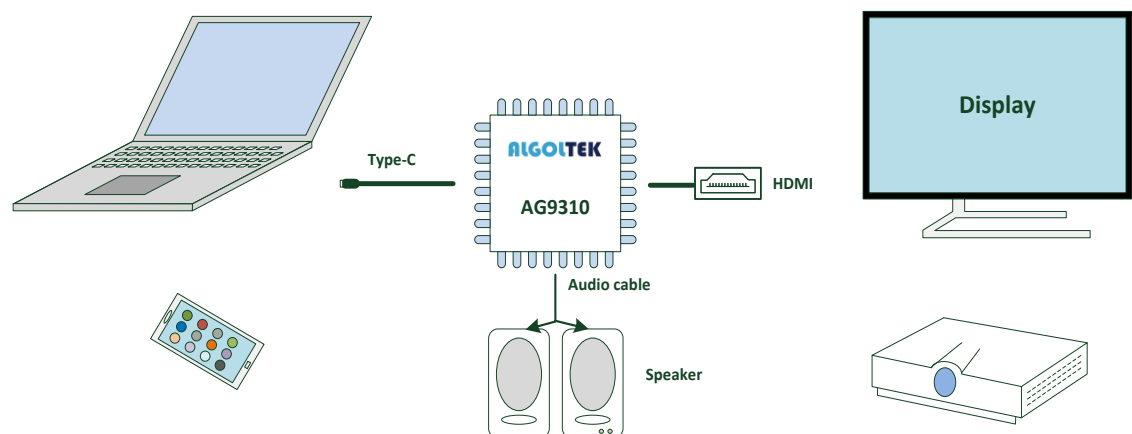


Figure 1 Application for Type-C to HDMI Dongle

V. System Block Diagram

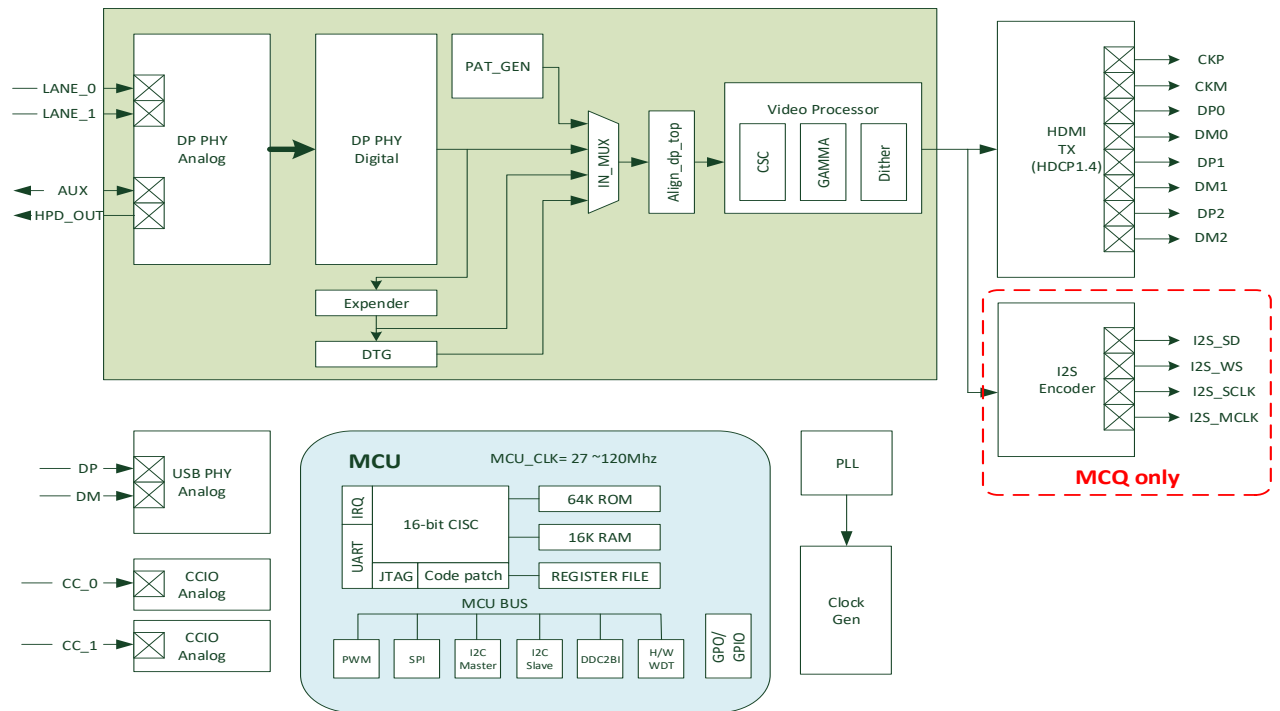


Figure 2 System Block Diagram

VI. Pin Mapping and Description

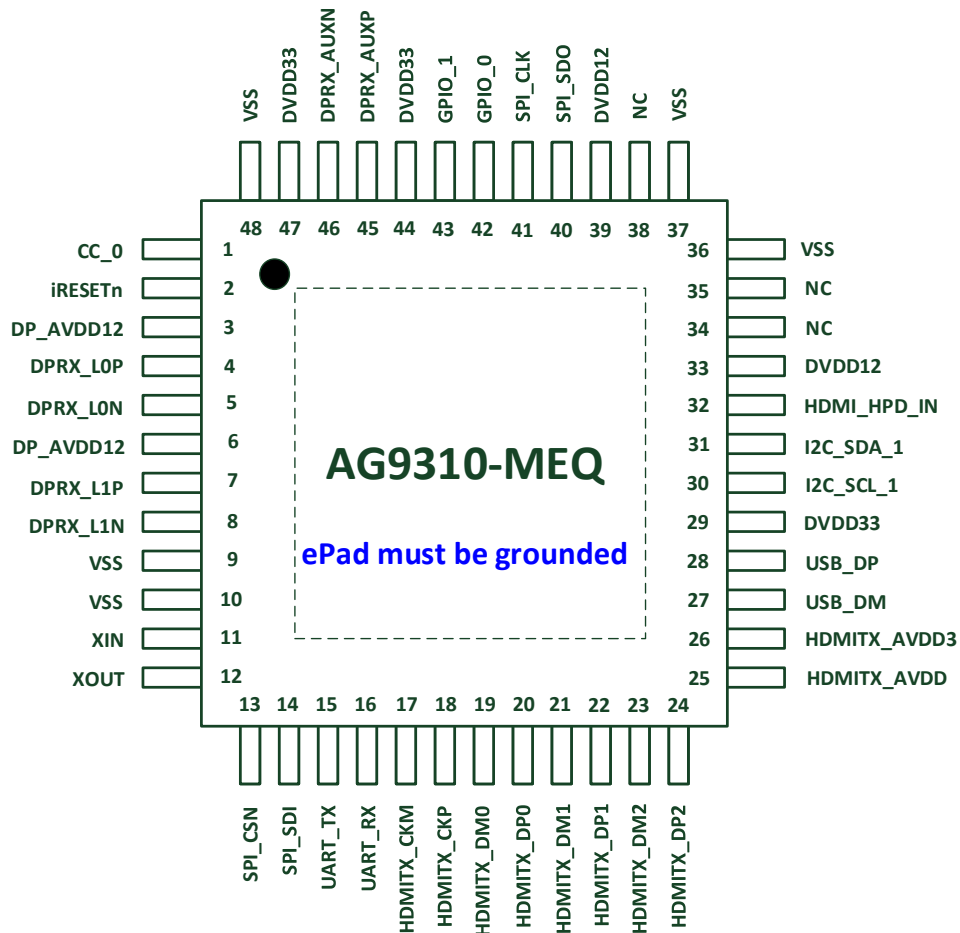


Figure 3 AG9310-MEQ PIN Mapping

Table 1 PIN Description

Pin Name	PIN NO.	Type	Description
	MEQ		
POWER SUPPLY			
DP_AVDD12	3,6	Power	Analog 1.2V power input for DP RX
DVDD33	29, 44, 47	Power	Digital 3.3V I/O power input
DVDD12	33, 39	Power	Digital 1.2V core power input



VSS	9, 10, 48, 36, 37	Power	Digital ground
HDMITX_AVDD	25	Power	Analog 1.2V power for HDMI TX
HDMITX_AVDD3	26	Power	Analog 3.3V power for HDMI TX
NC	34, 35, 38	Power	NC
DIFFERENTIAL HIGH-SPEED IO			
DPRX_LOP	4	Input	DP RX lane0 positive
DPRX_LON	5	Input	DP RX lane0 negative
DPRX_L1P	7	Input	DP RX lane1 positive
DPRX_L1N	8	Input	DP RX lane1 negative
HDMITX_CKM	17	Output	HDMI TX clock channel negative
HDMITX_CKP	18	Output	HDMI TX clock channel positive
HDMITX_DM0	19	Output	HDMI TX data channel 0 negative
HDMITX_DP0	20	Output	HDMI TX data channel 0 positive
HDMITX_DM1	21	Output	HDMI TX data channel 1 negative
HDMITX_DP1	22	Output	HDMI TX data channel 1 positive
HDMITX_DM2	23	Output	HDMI TX data channel 2 negative
HDMITX_DP2	24	Output	HDMI TX data channel 2 positive
USB_DM	27	Bidirectional	USB Type-C D-
USB_DP	28	Bidirectional	USB Type-C D+
DPRX_AUXN	46	Bidirectional	DisplayPort AUX channel N
DPRX_AUXP	45	Bidirectional	DisplayPort AUX channel P
Digital IO			
XIN	11	Input	Crystal oscillator clock input
XOUT	12	Output	Crystal oscillator clock output
SPI_CSN	13	Output	SPI chip select.
SPI_SDI	14	Input	SPI data input
UART_TX	15	Output	UART TX
UART_RX	16	Input	UART RX
I2C_SCL_1	30	Bidirectional	HDMI I2C SCL



I2C_SDA_1	31	Bidirectional	HDMI I2C SDA
HDMI_HPD_IN	32	Input	HDMI cable detection
SPI_SDO	40	Output	SPI data output.
SPI_CLK	41	Output	SPI clock.
GPIO_0	42	Bidirectional	General Purpose I/O
GPIO_1	43	Bidirectional	General Purpose I/O
CC_0	1	Input	Type-C configuration channel
iRESETN	2	Input	H/W Reset signal, active low. Need connect to Resistor and Capacitor on board.



VII. Specification

i. Absolute Maximum Rating

Symbol	Parameter	Min	Max	Unit
VDD5	5V Power Input	-0.5	5.5	V
VDD33/AVDD33	3.3V supply voltage	-0.5	3.63	V
VDD/AVDD	1.2V supply voltage	-0.5	1.32	V

Table 2 Absolute Maximum Rating

ii. DC Characteristics/Operating Conditions

Symbol	Parameter	Min	Typ.	Max	Unit
VDD5	5V Power Input	4.5	5	5.5	V
VDD33	Digital I/O supply voltage	3	3.3	3.6	V
VDD	Digital core supply voltage	1.16	1.2		V
AVDD3	Analog I/O supply voltage	3	3.3	3.6	V
AVDD	Analog core supply voltage	1.16	1.2		V
I _{VDD33+AVDD3}	3.3V I/O Supply Current(4K2K/30Hz)		80		mA
I _{VDD+AVDD}	1.2V Core supply Current(4K2K/30Hz)		380		mA
V _{OH}	Output High Voltage	2.4			V
V _{OL}	Output Low Voltage			0.4	V
V _{IH}	Input High Voltage	2.0		5.5	V
V _{IL}	Input Low Voltage	-0.3		0.8	V
I _{LI}	Input leakage Cur.			±1	uA
T _A	Ambient Temperature	0		70	°C

Table 3 Digital I/O Specification



iii. AC Characteristics

DisplayPort Main Link AC Characteristics

Symbol	Parameter	Min	Typ.	Max	Unit
UI_HBR2	Unit interval for HBR2(5.4-Gbps/lane)		185		ps
UI_HBR	Unit interval for HBR(2.7-Gbps/lane)		370		ps
UI_RBR	Unit interval for RBR(1.62-Gbps/lane)		617		ps
Down_Spread_Amp.	Link clock down-spreading	0		0.5	%
Down_Spread_Frequency	Link clock down-spreading frequency	30		33	kHz
T _{RX-MEDIAN-to-MAX-JITTER}	Max time between the jitter median and max. deviation from the median at Rx package pins for HBR (2.7-Gbps)			0.265	UI
T _{RX-MEDIAN-to-MAX-JITTER}	Max. time between the jitter median and max. deviation from the median at Rx package pins for RBR (1.62-Gbps)			0.39	UI
V _{RX-DC-CM}	RX DC Common Mode Voltage	0		2	V
I _{RX-SHORT}	RX Short Circuit Current Limit			50	mA

Table 4 DisplayPort Input Timing

DisplayPort AUX-CH AC Characteristics

Symbol	Parameter	Min	Typ.	Max	Unit
UIMAN	AUX (Manchester transaction) unit interval	0.4	0.5	0.6	us
Pre-charge Pulses	Number of pre-charge pulses	10		16	
T _{AUX-BUS-PEAK}	AUX CH bus park time	10			ns
T _{cycle-to-cycle jitter}	Maximum allowable variation for adjacent bit times within a single transaction at connector pins of a receiving device			0.05	UI
V _{AUX-DIFFp-p RX}	AUX peak-to-peak voltage at a transmitting device when receiving	0.29		1.38	V
V _{AUX-DIFFp-p RX}	AUX peak-to-peak voltage at a receiving device when receiving	0.27		1.36	V
V _{AUX_TERM_R}	AUX CH termination DC resistance		100		Ω
V _{AUX_DC_CM}	AUX DC common mode voltage	0		2.0	V
V _{AUX_TURN_CM}	AUX turn around common mode voltage			0.3	V
I _{AUX_SHORT}	AUX short circuit current			90	mA
C _{AUX}	AUX AC coupling	75		200	nF

Table 5 DisplayPort AUX Channel I/O Specification

**USB PD BMC Receiver Normative Requirements**

Name	Description	Min	Typ.	Max	Unit
cReceiver	CC receiver capacitance	200		600	pF
nTransitionCount	Transitions for signal detect	3			
tRxFilter	Rx bandwidth limiting filter (digital or analog)	100			ns
tTransitionWindow	Time window for detecting non-idle	12		20	Us
zBmcRx	Receiver Input Impedance	1			MΩ
vNoiseActive	Noise amplitude when BMC is active.			165	mV
vNoiseIdle	Noise amplitude when BMC is idle.			300	mV
vIRDropGNDC	Cable Ground IR Drop			250	mV

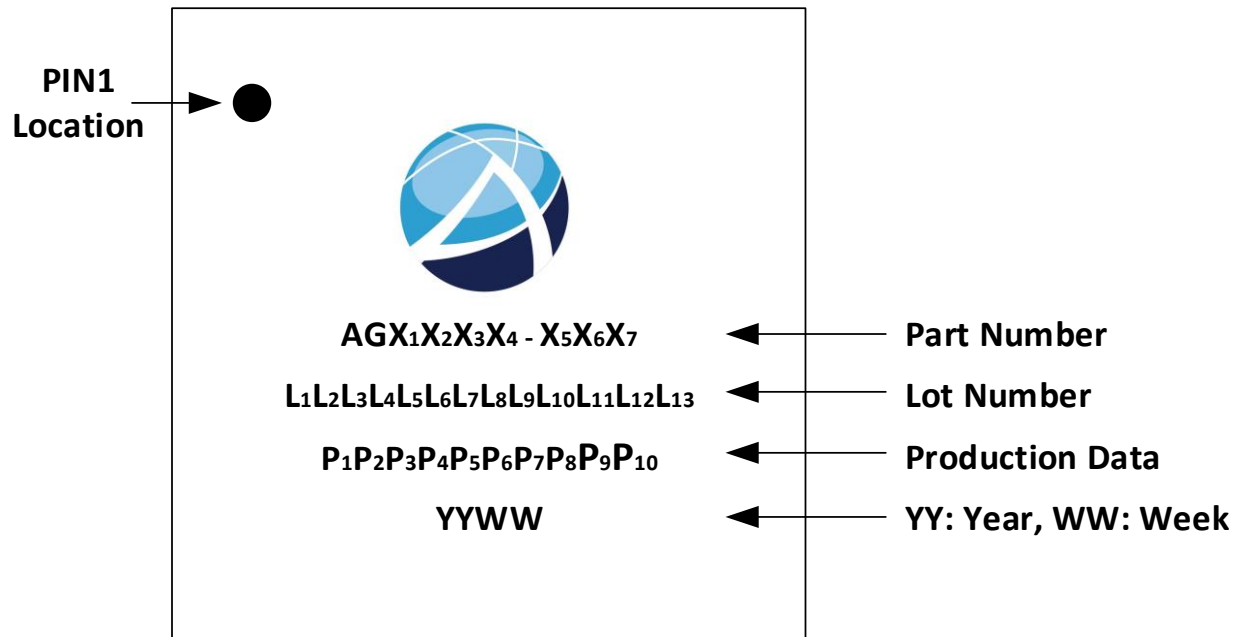
Table 6 USB I/O Specification**USB PD BMC Transmitter Normative Requirements**

Name	Description	Min	Typ.	Max	Unit
tEndDriveBMC	Time to cease driving the line after the end of the last bit of the Frame.			23	us
tFall	Fall Time	300			ns
tHoldLowBMC	Time to cease driving the line after the final high-to-low transition.	1			us
tRise	Rise time	300			ns
vSwing	Voltage Swing	1.05	1.12 5	1.2	V
zDriver	Transmitter output impedance	33		75	Ω

Table 7 USB PD Specification

VIII. Packing and Marking Specification

- Marking





AG9310
Type C to HDMI converter

● Package Dimension
AG9310-MEQ: QFN-48L 6x6mm

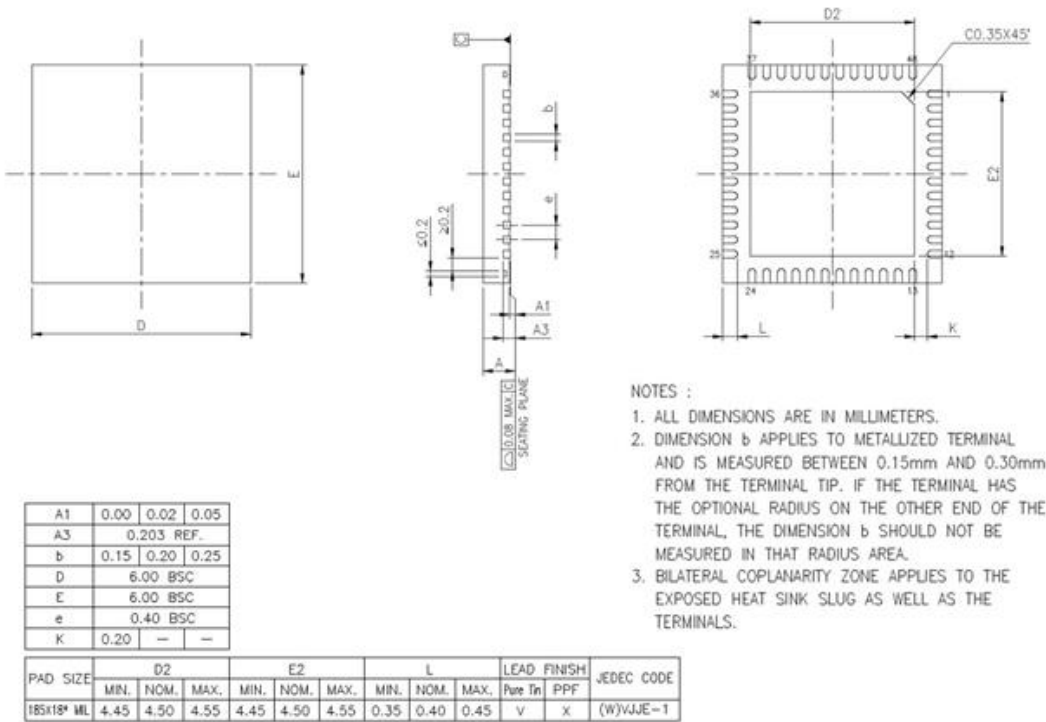


Figure 4 AG9310-MEQ Package Dimension



IX. References

- DisplayPort™ Standard 1.2b Specification
- HDMI 1.4b Specification

X. Contact Information

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