

### ● General Description

The AGM042N10D combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### ● Features

- Advance high cell density Trench technology

- Low  $R_{DS(ON)}$  to minimize conductive loss

- Low Gate Charge for fast switching

- Low Thermal resistance

### ● Application

- MB/VGA Vcore

- SMPS 2<sup>nd</sup> Synchronous Rectifier

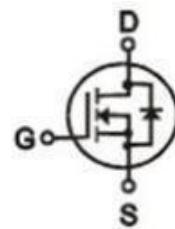
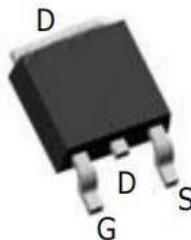
- POL application

- BLDC Motor driver

### Product Summary

BVDSS	RDS(on)	ID
100V	4.1mΩ	110A

### TO-252 Pin Configuration



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM042N10D	AGM042N10D	TO-252	---	---	2500

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	100	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	110	A
	Drain Current-Continuous(Tc=100°C)	69.5	A
IDM (pulse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	440	A
PD	Maximum Power Dissipation(Tc=25°C)	125	W
	Maximum Power Dissipation(Tc=100°C)	50	W
EAS	Avalanche energy <b>(Note 3)</b>	120	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	1.0	°C/W

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

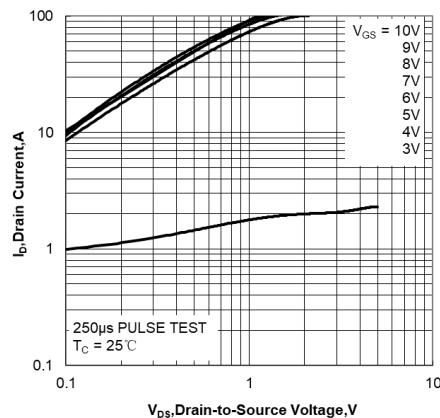
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	100	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=100V, VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	1.2	1.8	2.5	V
gFS	Forward Transconductance	VDS=5V, ID=10A	--	--	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A	--	4.1	4.8	mΩ
		VGS=4.5V, ID=10A	--	6.2	7.5	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=50V, VGS=0V, F=1MHZ	--	3580	--	pF
Coss	Output Capacitance		--	1230	--	pF
Crss	Reverse Transfer Capacitance		--	25	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	2.35	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V, VDS=50V, ID=50A, RGEN=3Ω	--	28	--	nS
tr	Turn-on Rise Time		--	24	--	nS
td(off)	Turn-Off Delay Time		--	64	--	nS
tf	Turn-Off Fall Time		--	22	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=50V, ID=50A	--	65.5	--	nC
Qgs	Gate-Source Charge		--	16	--	nC
Qgd	Gate-Drain Charge		--	19.5	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	110	A
VSD	Forward on Voltage	VGS=0V, IS=20A	--	0.8	1.0	V
trr	Reverse Recovery Time	Isd=20A , dl/dt=100A/μs , TJ=25°C	--	60	--	ns
Qrr	Reverse Recovery Charge		--	90	--	nc

Notes 1.The maximum current rating is package limited.

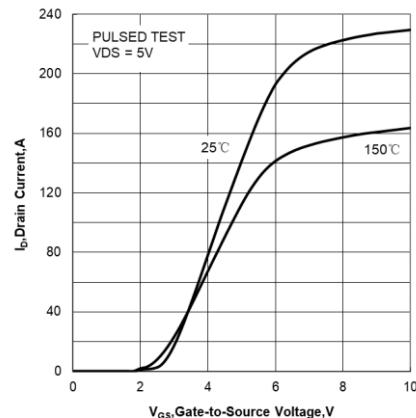
Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

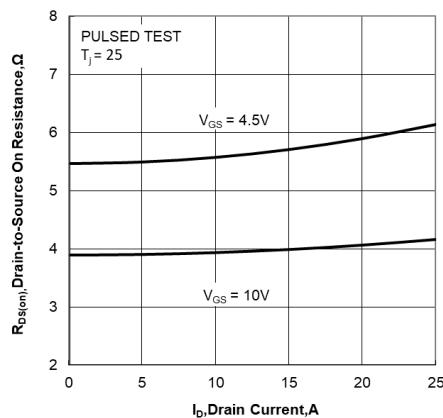
## Typical Performance Characteristics



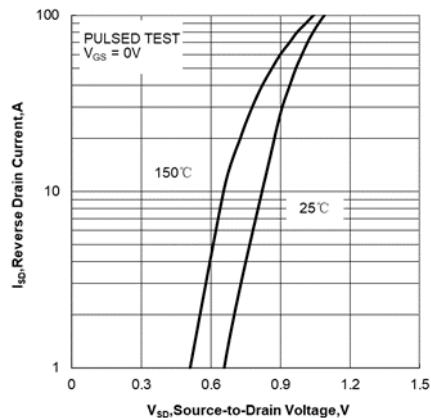
**Figure 1. Output Characteristics**



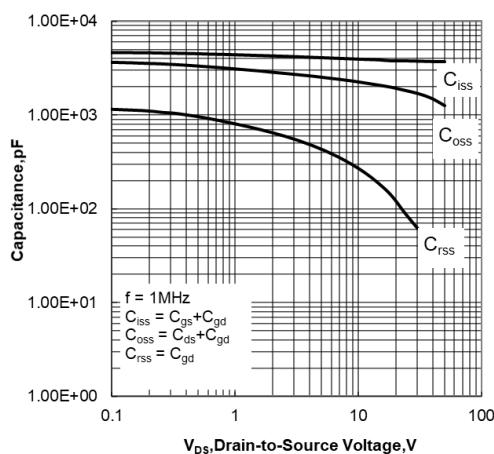
**Figure 2. Transfer Characteristics**



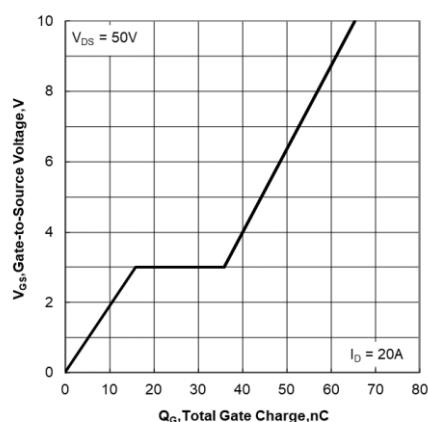
**Figure 3. Drain-to-Source On Resistance vs Drain Current**



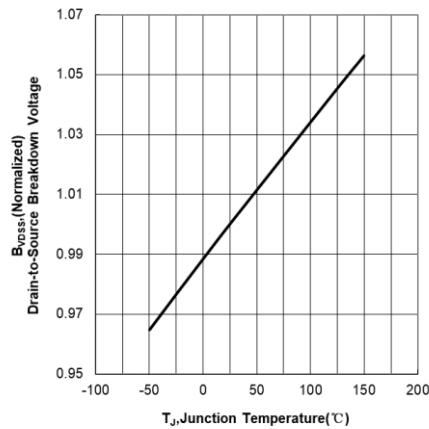
**Figure 4. Body Diode Forward Voltage vs Source Current and Temperature**



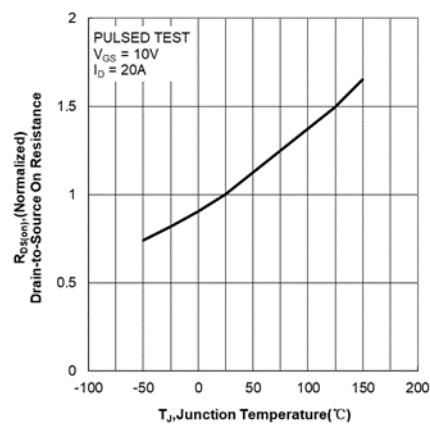
**Figure 5. Capacitance Characteristics**



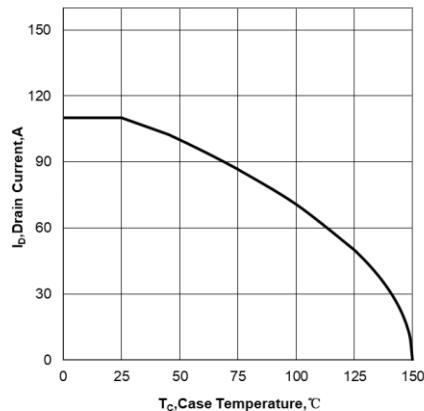
**Figure 6. Gate Charge Characteristics**



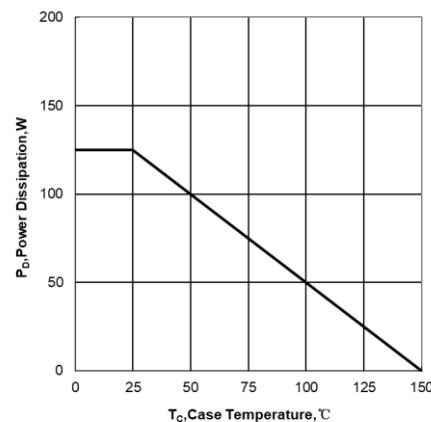
**Figure 7. Normalized Breakdown Voltage  
vs Junction Temperature**



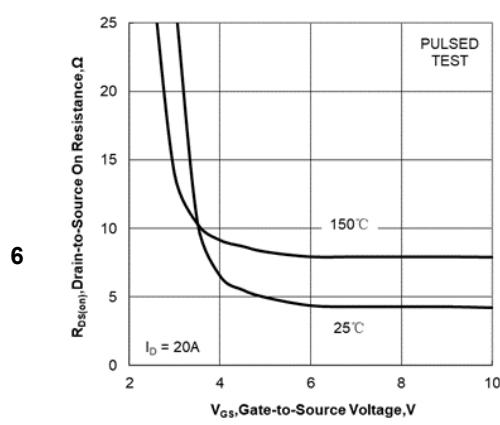
**Figure 8. Normalized On Resistance vs  
Junction Temperature**



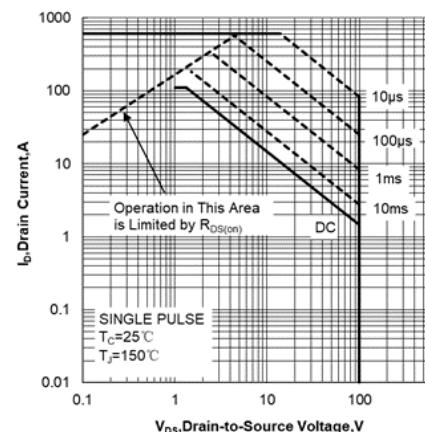
**Figure 9. Maximum Continuous Drain Current  
vs Case Temperature**



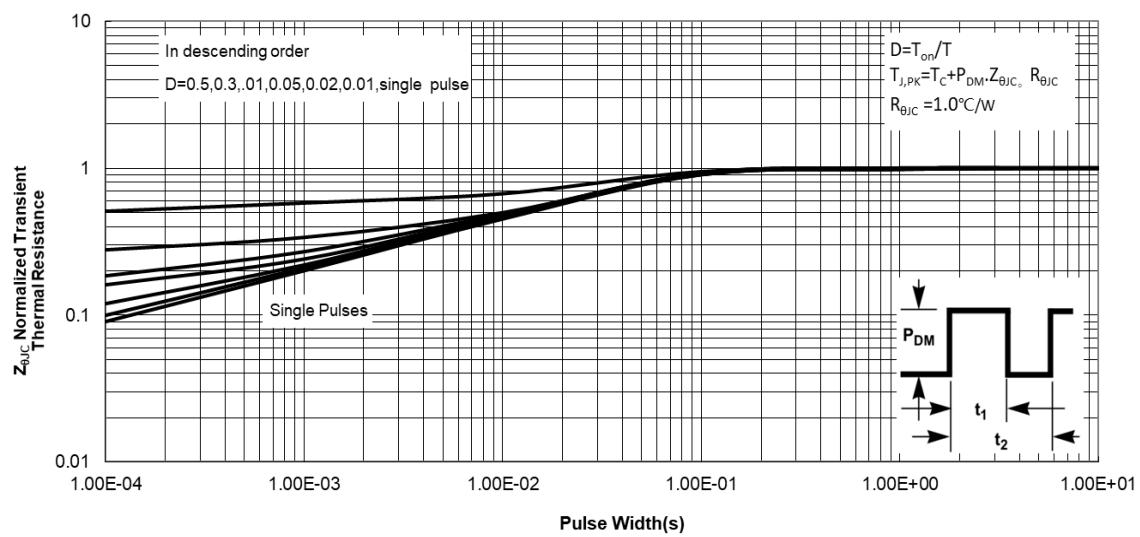
**Figure 10. Maximum Power Dissipation  
vs Case Temperature**



**Figure11. Drain-to-Source On Resistance vs Gate  
Voltage and Drain Current**



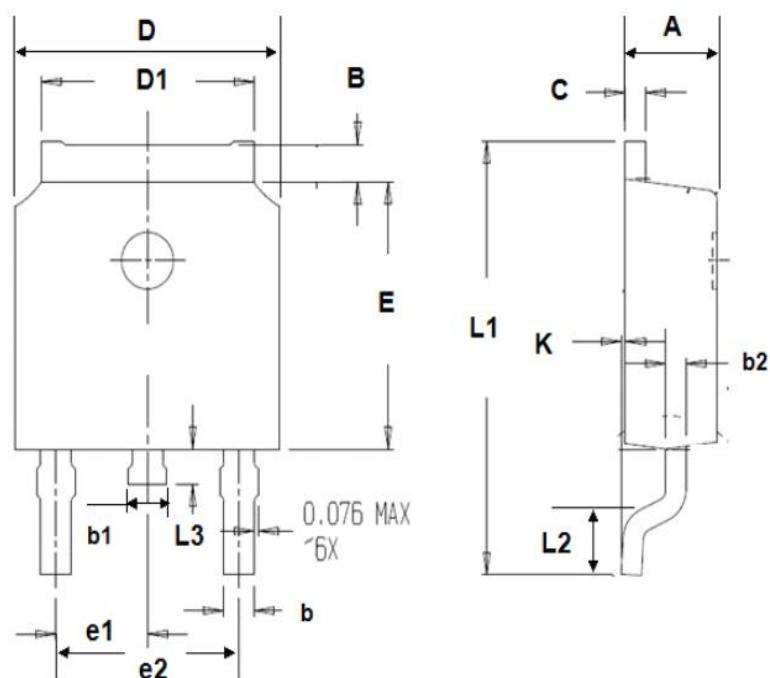
**Figure 12. Maximum Safe Operating Area**



**Figure 13. Maximum Effective Transient Thermal Impedance, Junction-to-Case**

## • Dimensions

SYMBOL	min	max	SYMBOL	min	max
A	2.10	2.50	B	0.85	1.25
b	0.50	0.80	b1	0.50	0.90
b2	0.45	0.70	C	0.45	0.70
D	6.30	6.75	D1	5.10	5.50
E	5.30	6.30	e1	2.25	2.35
L1	9.20	10.60	e2	4.45	4.75
L2	0.90	1.75	L3	0.60	1.10
K	0.00	0.23			



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