

AZ DISPLAYS

SPECIFICATIONS FOR LIQUID CRYSTAL DISPLAY MODULE

MODEL NO.: AGM1264R-FLB-FBW

DATE: NOV.23.2011

Approved	Checked	Department

CUSTOMER:

MODEL NO.:

DATE:

Approved	Checked	Department

DATE NOV .23 .2011

TECHNICAL SPECIFICATION

LCM

YES

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I . General Specifications

1.Features

- A. Drive Method: 1/64 Duty, 1/9 Bias
- B. The Module Operating Voltage: 3.3V;
- C. The LCD Operating Voltage : 9.7V;
- D. Viewing Direction: 6:00
- E. Operating Temperature: -20℃~70℃
- F. Storage Temperature: -30℃~85℃
- G. Display type: FSTN mode, Transflective, Positive type display

2.Mechanical Data and Conditions:

- (1) Module Size-----72.0(w) *52.0(h)mm
- (2) Viewing Area ----- 66.4(w)* 39.4(h)mm
- (3) Dot Size -----0.45(w)*0.53(h) mm
- (4) Dot Quality -----128*64 DOTS
- (5) Outline Dimensions-----See Attached Drawing

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3.Pin Connections:

Pin No.	Symbol	Function
1	P/S	This is the parallel data input/serial data input switch terminal
2	C86	This is the MPU interface switch terminal
3-7	V5-V1	A multi-level power supply for the liquid crystal drive.
8	CAP2+	DC/DC voltage converter
9	CAP2-	DC/DC voltage converter
10	CAP1-	DC/DC voltage converter
11	CAP1+	DC/DC voltage converter
12	CAP3-	DC/DC voltage converter
13	VOUT	dc/dc voltage converter
14	VSS	Ground
15	VDD	Power supply
16	D7(SI)	Data bus
17	D6(SCL)	Data bus
18-23	D5-D0	Data bus
24	$\overline{RD}(E)$	8080:RD signal 6800:Enable clock input terminal
25	$\overline{WR}(R/W)$	8080:WR signal 6800:when RWP="H":Read when RWP="L":Write
26	A0	This is connected to the least significant bit of the normal MPU address bus, and it determines whether the data bits are data or a command.
27	/RES	Reset signal
28	/CS1	The chip select signal

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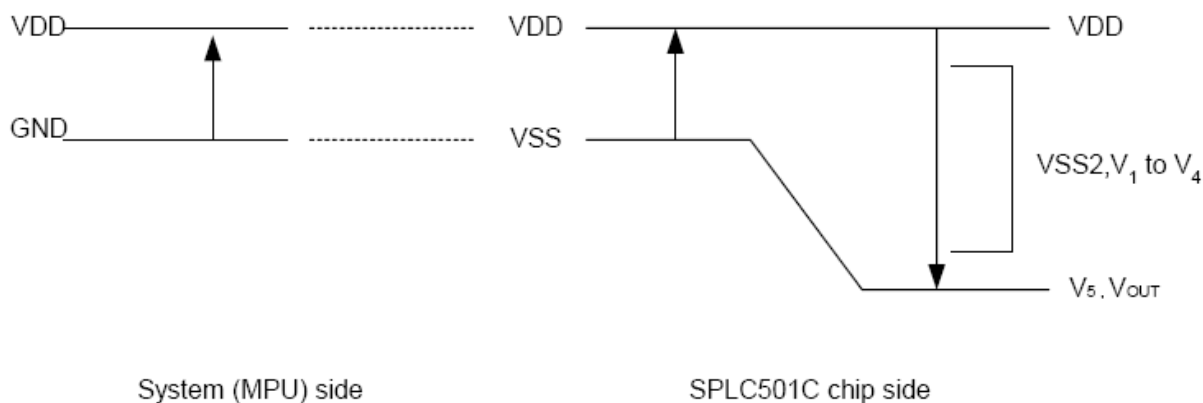
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4. Absolute Maximum Ratings

(Unless otherwise noted, VSS = 0V)

Parameter		Symbol	Conditions	Unit
Power Supply Voltage		VDD	-0.3 to + 7.0	V
Power supply voltage (2) (VDD standard)		VSS2	-7.0 to +0.3	V
	With Triple step-up		-4.0 to +0.3	
	With Quad step-up		-3.0 to +0.3	
Power supply voltage (3) (VDD standard)		V_5, V_{OUT}	-12.0 to +0.3	V
Power supply voltage (4) (VDD standard)		V_1, V_2, V_3, V_4	V_5 to +0.3	V
Input voltage		V_{IN}	-0.3 to VDD +0.3	V
Output voltage		V_O	-0.3 to VDD +0.3	V



System (MPU) side

SPLC501C chip side

Notes and Cautions:

1. The VSS2, V_1 to V_5 and V_{OUT} are relative to the VDD = 0V reference.
2. Insure that the voltage levels of V_1 , V_2 , V_3 , and V_4 are always such that $VDD \geq V_1 \geq V_2 \geq V_3 \geq V_4 \geq V_5$.
3. Permanent damage to the LSI may result if the LSI is used outside of the absolute maximum ratings. Moreover, it is recommended that in normal operation the chip be used at the electrical characteristic conditions, and use of the LSI outside of these conditions may not only result in malfunctions of the LSI, but may have a negative impact on the LSI reliability as well.

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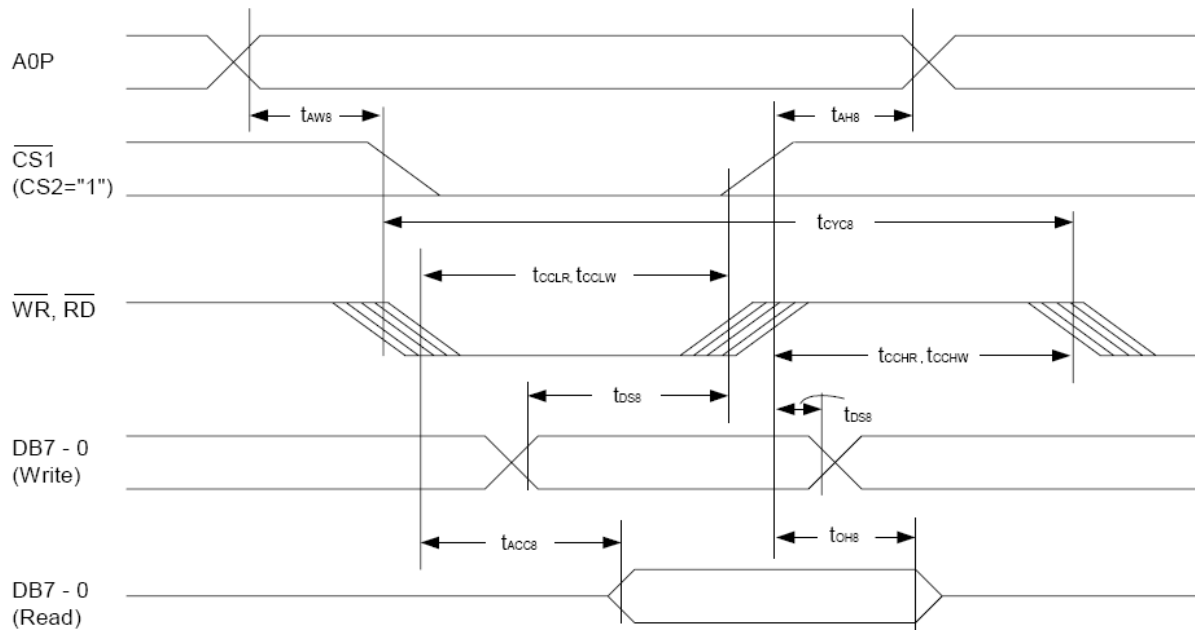
YES

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5. Timing Characteristics:

System bus read/write characteristics 1 (For the 8080 Series MPU)



(VDD = 2.7V to 4.5V, T_A = 25°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Address hold time	A0P	t _{AHS}		0	-	ns
Address setup time	A0P	t _{AWS}		0	-	ns
System cycle time	A0P	t _{CYC8}		300	-	ns
Control L pulse width ($\overline{\text{WR}}$)	$\overline{\text{WR}}$	t _{CCLW}		60	-	ns
Control L pulse width ($\overline{\text{RD}}$)	$\overline{\text{RD}}$	t _{CCLR}		120	-	ns
Control H pulse width ($\overline{\text{WR}}$)	$\overline{\text{WR}}$	t _{CCHW}		60	-	ns
Control H pulse width ($\overline{\text{RD}}$)	$\overline{\text{RD}}$	t _{CCHR}		60	-	ns
Data setup time	DB7 - 0	t _{DS8}		40	-	ns
Address hold time		t _{DHS}		15	-	ns
RD access time		t _{ACC8}	C _L = 100pF	-	140	ns
Output disable time		t _{OHS}		10	100	ns

Note1: The input signal rise time and fall time (t_r, t_f) is specified at 15 ns or less. When the system cycle time is extremely fast, (t_r + t_f) ≤ (t_{CYC8} - t_{CCLW} - t_{CCHR}) for (t_r + t_f) ≤ (t_{CYC8} - t_{CCLR} - t_{CCHW}) are specified.

Note2: All timing is specified using 20% and 80% of VDD as the reference.

Note3: t_{CCLW} and t_{CCLR} are specified as the overlap between CS1 being 'L' (CS2 = 'H') and $\overline{\text{WR}}$ and $\overline{\text{RD}}$ being at the 'L' level.

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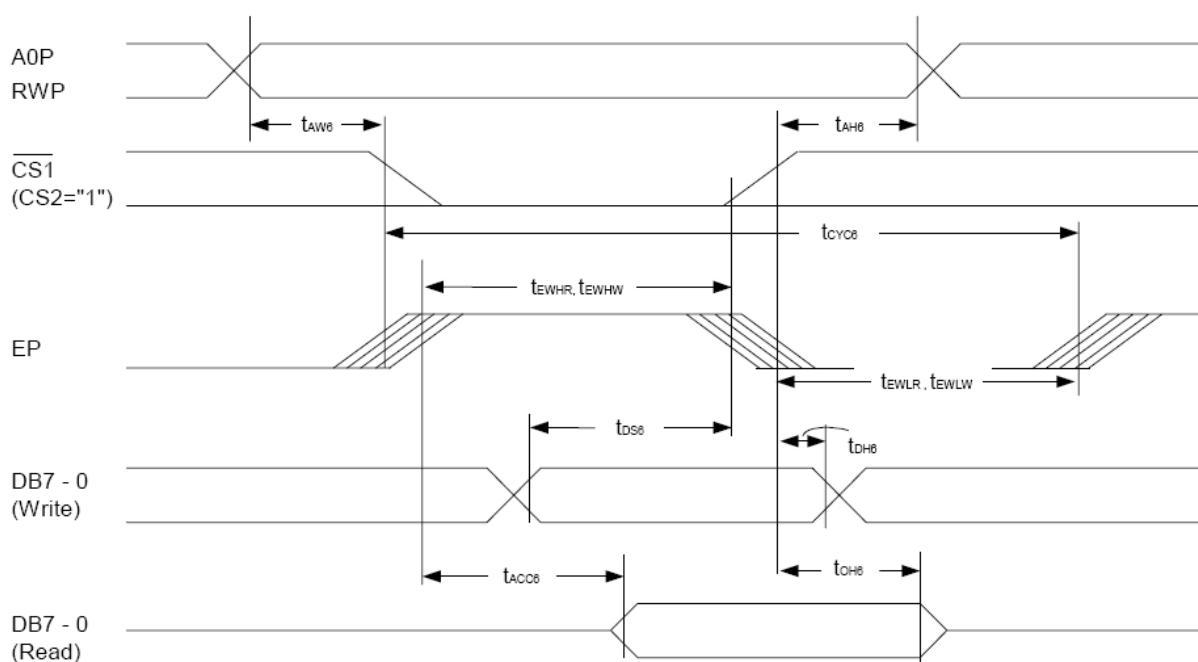
LCM

YES

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System bus read/write characteristics 2 (6800 series MPU)

(VDD = 2.7V to 4.5V, T_A = 25°C)

Item		Signal	Symbol	Condition	Rating		Units
					Min.	Max.	
Address hold time		A0P	t_{AH6}		0	-	ns
Address setup time			t_{AW6}		0	-	ns
System cycle time		A0P	t_{CYC6}		300	-	ns
Data setup time		DB7 - 0	t_{DS6}	$C_L = 100pF$	40	-	ns
Data hold time			t_{DH6}		15	-	ns
Access time			t_{ACC6}	-	140	ns	
Output disable time			t_{OH6}	10	100	ns	
Enable H pulse time	Read	EP	t_{EWHR}		120	-	ns
	Write		t_{EWHW}		60	-	ns
Enable L pulse time	Read	EP	t_{EWLR}		60	-	ns
	Write		t_{EWLW}		60	-	ns

Note1: The input signal rise time and fall time (t_r, t_f) is specified at 15 ns or less. When the system cycle time is extremely fast, (t_r + t_f) ≤ (t_{CYC6} - t_{EWLR} - t_{EWHW}) for (t_r + t_f) ≤ (t_{CYC6} - t_{EWLR} - t_{EWHR}) are specified.

Note2: All timing is specified using 20% and 80% of VDD as the reference.

Note3: t_{EWLW} and t_{EWLR} are specified as the overlap between CS1 being 'L' (CS2 = 'H') and EP.

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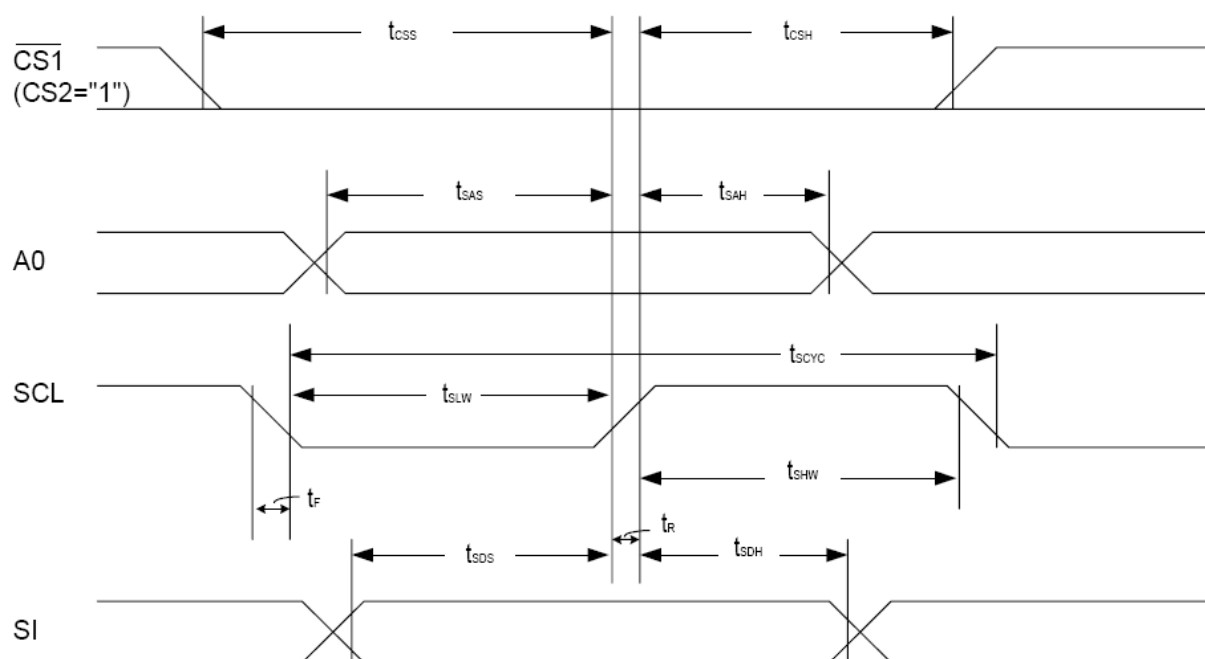
LCM

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The serial interface



(VDD = 2.7V to 4.5V, T_A = 25°C)

Item	Signal	Symbol	Condition	Rating		Units
				Min.	Max.	
Serial Clock Period		tSCYC	-	250	-	ns
SCL 'H' pulse width	SCL	tSHW	-	100	-	ns
SCL 'L' pulse width	SCL	tslw	-	100	-	ns
Address setup time	A0P	tsas	-	150	-	ns
Address hold time		tsah	-	150	-	ns
Data setup time	SI	tsds	-	100	-	ns
Data hold time		tsdh	-	100	-	ns
CS-SCL time	CS	tcSS	-	150	-	ns
		tcSH	-	150	-	ns

Note1: The input signal rise and fall time (t_r , t_f) are specified at 15 ns or less.

Note2: All timing is specified using 20% and 80% of VDD as the standard.

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II.The Characteristics and The Reliability Test

1.Electro-Optic Characteristics:

Condition:TEMP=(23±3)°C

NO	Item		Symbol	Min.	Typ.	Max.	Unit	Condition
1	Supply Voltage(Logic)		Vdd-Vss		3.3		V	
3	LCD Operating Voltage		Vdd-V ₀		10.1		V	-20°C
				9.5	9.7	9.9	V	25°C
					9.3		V	70°C
4	Response Time		Ton		80		ms	
			Toff		320		ms	
5	Contrast Ratio		CR	2				
6	Viewing Angle	12H	θ 1		44		Deg.	(CR≥2.0)
		6H	θ 2		69			
		3H	θ 3		55			
		9H	θ 4		55			

2. Characteristics of backlight (LED unit)

Color:Blue

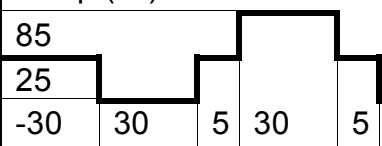
	Symbol	MIN	TYP.	MAX	UNIT	CONDITIONS
Forward Voltage	V _f	2.85	3.2	3.5	V	I _f =90mA
Forward Current	I _f		90	120	mA	V _f =3.2V
Spectral Line Half width	Δλ		25		nm	
Luminous Intensity	L _v		60		cd/m ²	I _f =90mA
Emission Wavelength	λ _p	470	472.5	475	nm	
Operating Temperature	T _{opr}	-20 °C TO + 70 °C				
	T _{sty}	-30 °C TO + 80 °C				

WARNING:

A BACKLIGHT IS A KIND OF CURRENT DEVICE,IT MUST CONNECT WITH A RESISTOR FOR LIMITING CURRENT ,OR IT WILL BE DAMAGED.

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3. Reliability Test

No	Items	Test Condition	Equipment	Test Result
1	High Temp Storage	Temp: $85 \pm 2^{\circ}\text{C}$ Time: 96h Restore: 24h	Tenny	Passed
2	Low Temp Storage	Temp: $-30 \pm 3^{\circ}\text{C}$ Time: 96h Restore: 24h	Tenny	Passed
3	High Temp Operating	Temp: $70 \pm 2^{\circ}\text{C}$ Vop: 3.3V Time: 24h Restore: 24h	Tenny	Passed
4	Low Temp Operating	Temp: $-20 \pm 3^{\circ}\text{C}$ Vop: 3.3V Time: 24h Restore: 24h	Tenny	Passed
5	High Temp High Hum Storage	Temp: $40 \pm 2^{\circ}\text{C}$ Hum: 95%Rh Time: 96h Restore: 24h	Tenny	Passed
6	Thermal Shock	Temp: ($^{\circ}\text{C}$)  5 Cycles Restore: 24h	Tenny	Passed

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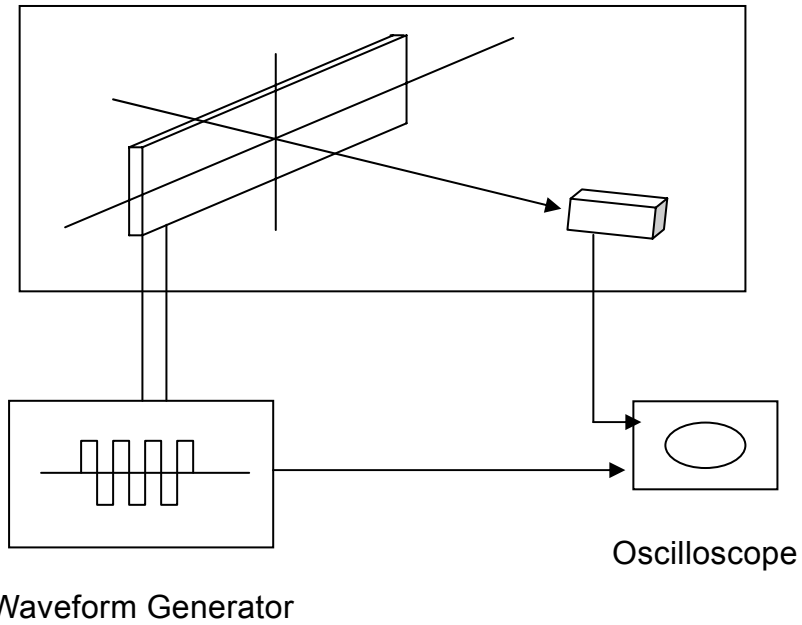
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III.The LCD Measuring Method and Equipment

1. Threshold Voltage and Response Time Measuring

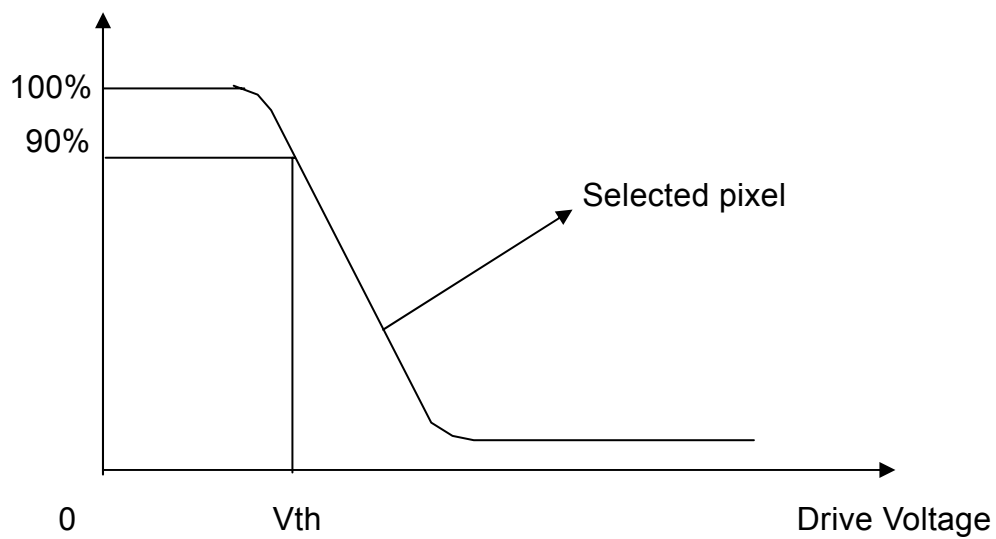
(1) Equipment



(2) Definition

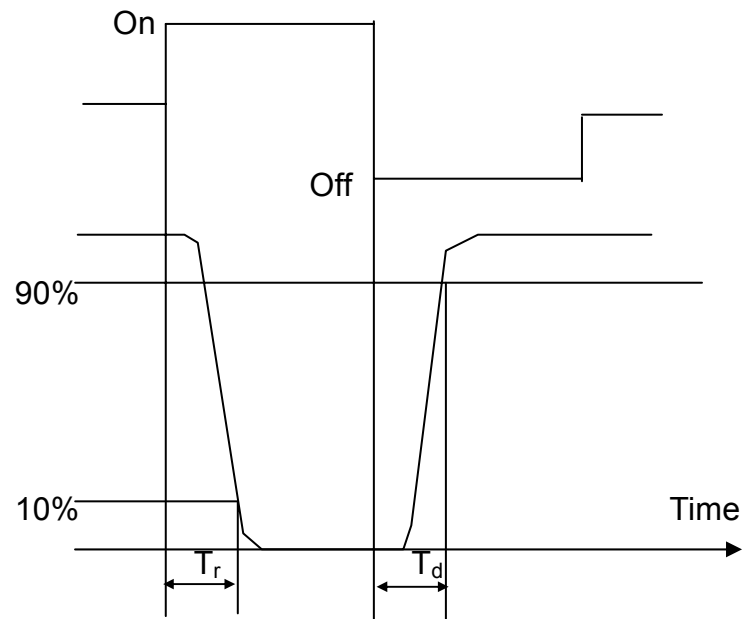
A. Threshold Voltage (V_{th})

Brightness



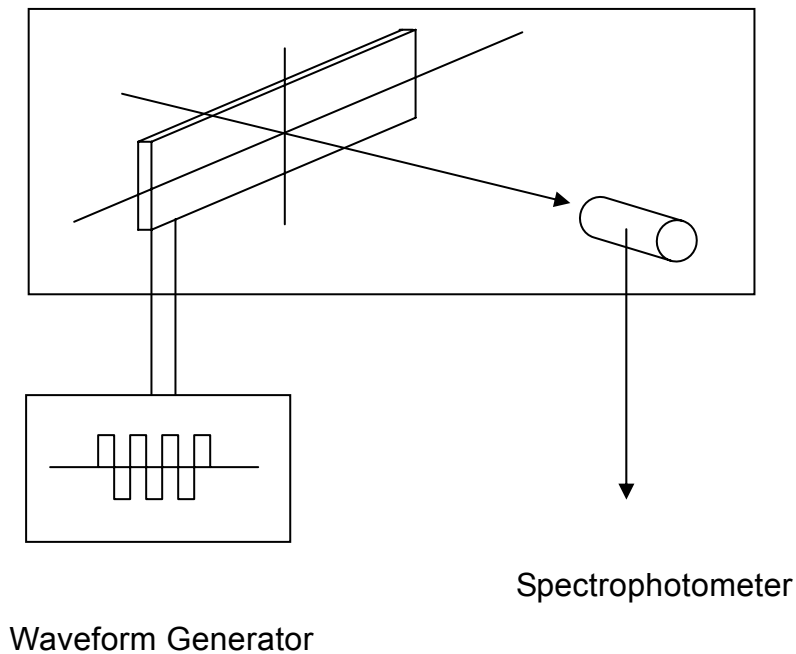
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B. Response Time



2. Contrast Measuring

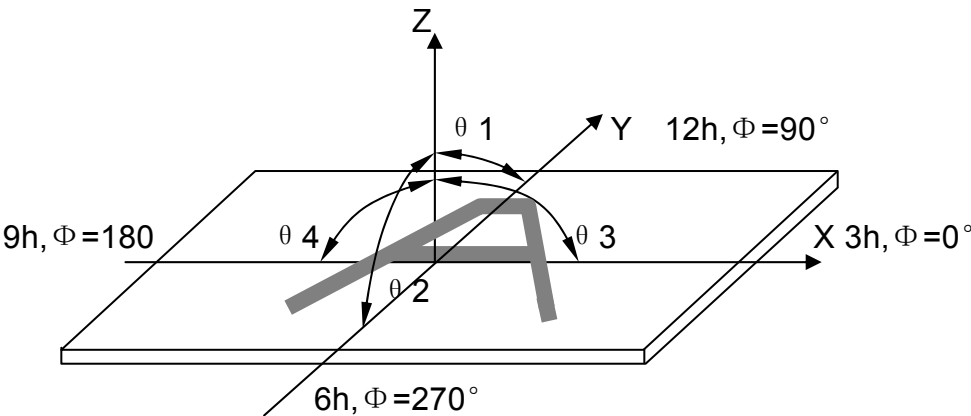
(1) Equipment



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(2)Definition:

A.Viewing Angle:



B. Contrast Ratio (Positive)

$$CR = \frac{\text{Brightness of non-selected pixel}}{\text{Brightness of selected pixel}}$$

3. Reliability Test:

Equipment : TENNY

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IV. Standard Specifications for Product Quality

1.MTBF

More than 50,000 hours.

2. Method of Test::

(1)The Test Must Be Under 40W Fluorescent Lamp, And The Distance Of View Must Be At 30cm.

(2)The eye's Test Direction Is Based On the vertical direction 15° - 45° .

3. Definition Of Defects

(1) Major Defects

A:Non-Display

B:Segment Missing

C:Over Current

D:Segment Short

E: Wrong Polarizer Direction

(2)Minor Defects: The Others.

4.Major Defects Should Be In AQL 0.25,and The Minor In AQL 1.00

The sampling inspection plan is in accordance with the Level II and normal inspection.

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5. Inspection Item and Standards

Item	The Standard Of Quality Inspection	Checking Method	Quantity Ratio
Frame	Smooth and even surface,no crack,no scratch,no rusty,and not be wrenched out of shape.the range between convex and concave is: $d \leq 0.35\text{mm}$,and the frame must be connected with the ground pad.	Checking With Eyes And Using Vernier Caliper, Multimeter	100%
The Relative Position of LCD and Frame	The end seal of the LCD must be at the same side with the frame's opening.	Checking With Eyes	100%
The Relative Position of PCB/Panel /Frame	The frame installing direction must be correct.the twisted angle of the leg is from 45° to 60° ,the leg is vertical to PCB panel and it must be in the middle position of the installing holes.	Checking With Eyes	100%
LED	1.The LED must be blue 2.The LED must be uniform.	Checking With Eyes	100%
Function Test	1. The major defects must be reject. 2. Background changes evenly and no disorderly displaying phenomenon. 3. Display no shortage.	Check It When Displaying	100%

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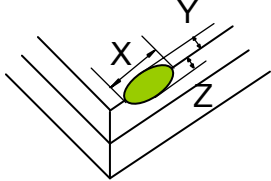
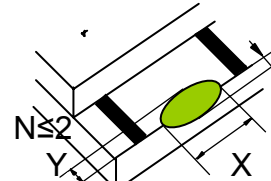
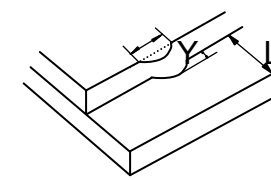
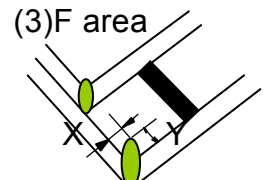
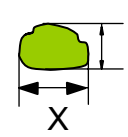
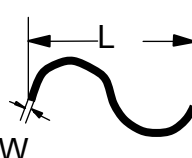
YES

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LCD:

Standard of appearance test: (unit: mm)

No	Items	Criterion	Checking manner
1	Substrate crack X: defect Length Y: defect Width Z: defect Depth T: glass Thickness N: defect QTY L: Connector Width	(1) A area  $X \leq 3.0$ Y: Don't allowed hurt sealing $Z \geq T/2$ $N \leq 3$ $X \leq 5.0$ Y: Don't allowed hurt sealing $Z \leq T/2$ $N \leq 3$ $X \leq 1.0$ $Y \leq 0.5$ $Z \leq T/3$ No check (2) G area  $Z \leq 3.0$ $Y \leq 0.5$ $Z \leq T/2$ $N \leq 2$  $X \leq 1/2$ total length $Y \leq 1/4L$ $N \leq 1$ Over the drawing tolerance is not allowed (3) F area  $X \leq 2.0$ $Y \leq 3$ $Z \leq T$ $N \leq 3$ Don't allowed hurt sealing	checking with eyes
2	Black spot white spot $D = (X+Y)/2$ Line	(1)  $0.2 < D \leq 0.25$ $N \leq 1$ $0.1 < D \leq 0.2$ $N \leq 3$ $D \leq 0.1$ No check II area No check (2)  $L \leq 2.0$ $W \leq 0.03$ $N \leq 2$ $L \leq 1.0$ $W \leq 0.05$ $N \leq 1$ II area No check	Checking on the table with light and polarizer and checking with eyes directly.

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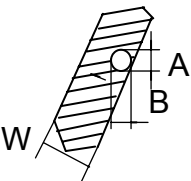
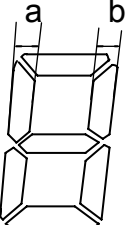
YES

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No	Items	Criterion	Checking manner
3	Polarizer Bubble	$D \leq 0.15$ No check $0.15 < D \leq 0.4$ $N \leq 2$ II area No check	Checking on the table with light and polarizer, and checking with eyes directly
4	Rainbow Color	Allow tiny rainbow Allow 5% color contrast or accord limitative sample	Checking on the table with light and polarizer, And checking with eyes directly
5	Polarizer or pad appearance	No dirty	Checking with eyes

Standard of display test

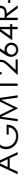
No	Items	Criterion	Checking manner
1	Pin hole $D = (A+B)/2$ W: segment width	 $W \leq 0.4$ $D \leq 0.20$ And $D \leq 1/2W$ $N \leq 1$ $W > 0.4$ $D \leq 0.25$ And $D \leq 1/3W$ $N \leq 2$ $D \leq 0.05$ No check	Checking at the display state
2	Different width of segment	 $ a-b < 0.25$ or $ a-b \leq 1/4W$ No check	Checking at the display state

Note: d ~ Diameter n ~ Quantity Unit: mm

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1. Operating Voltage: 3.3V
2. Display Type: FSTN
3. Drive Method: 1/64Duty, 1/9 Bias
4. Viewing Direction: 6:00
5. Operating Temp: -20°C~70°C
Storage Temp: -30°C~85°C
6. Backlight: LED/BLUE
7. Resolution: 128X64 Dots
8. Customer NO: HDG12864L-6-Z60

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	<i>Unit:mm</i>

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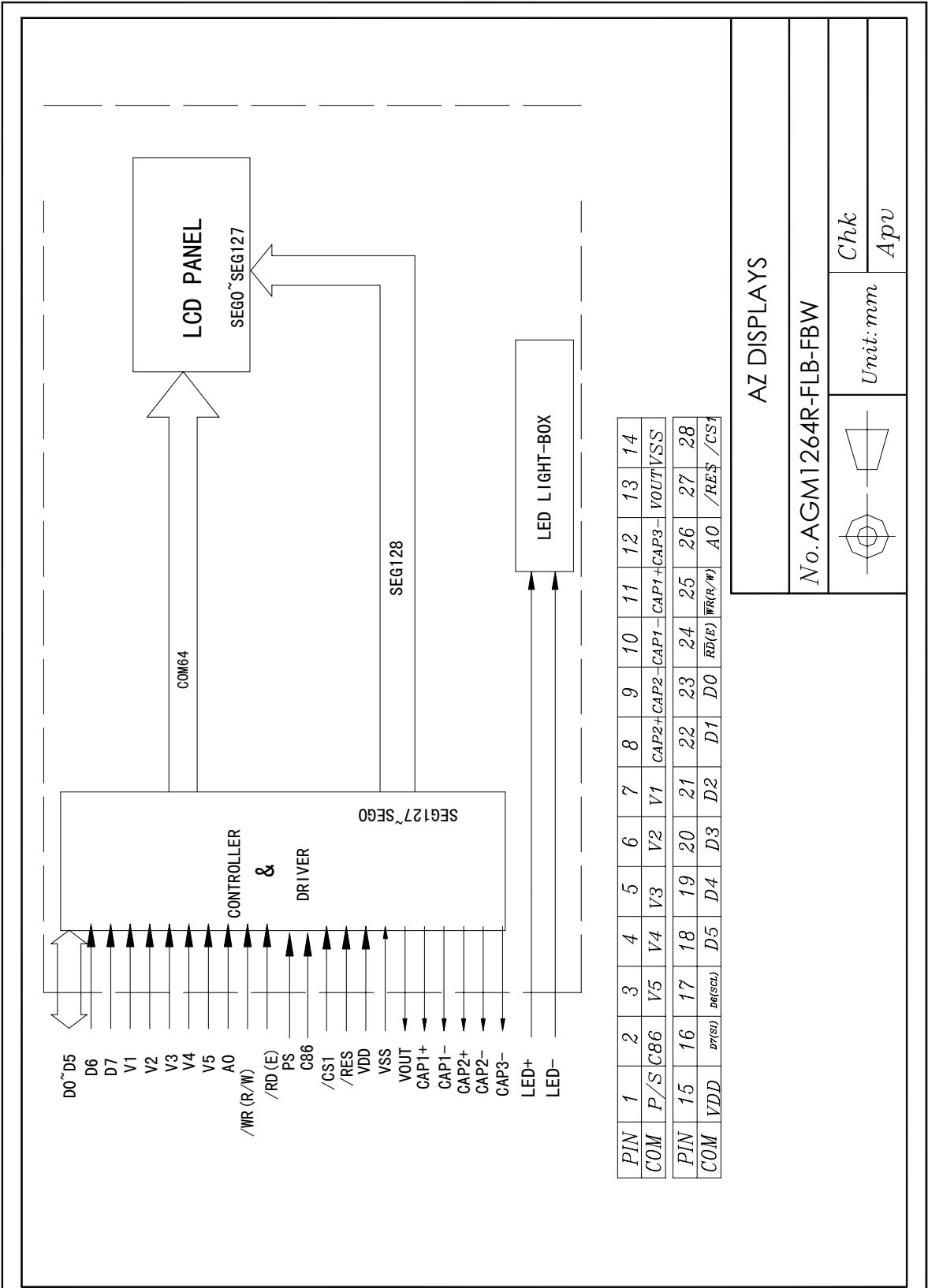
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Unit:mm

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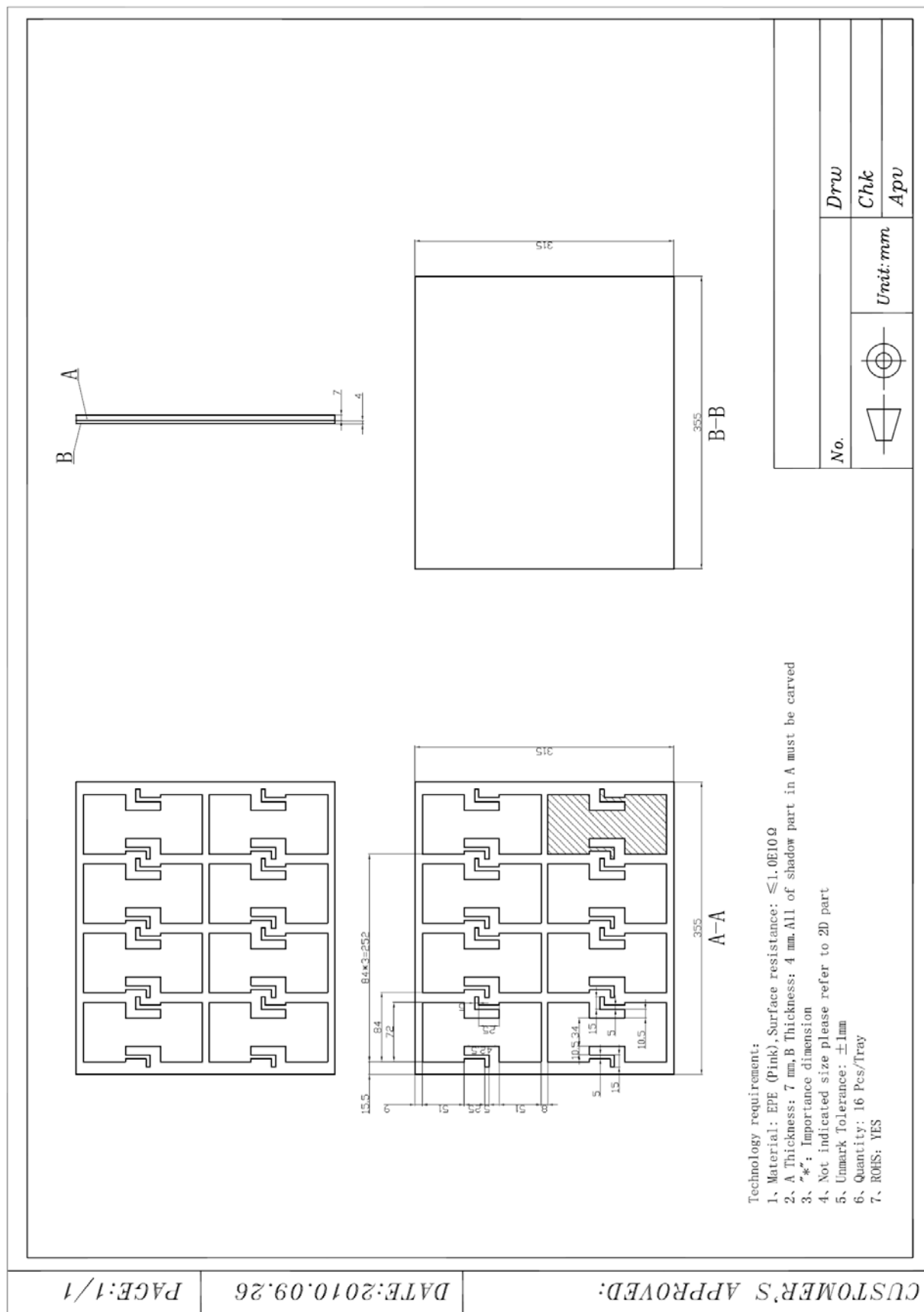
Apv

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CUSTOMER'S APPROVED:

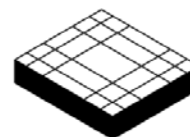
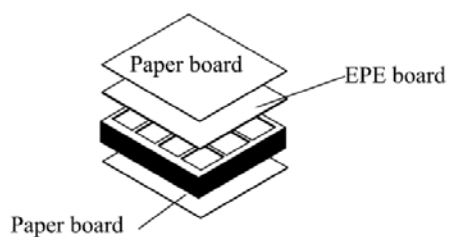
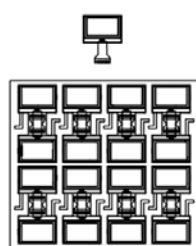
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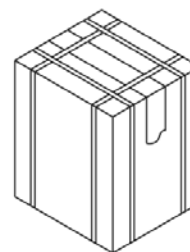
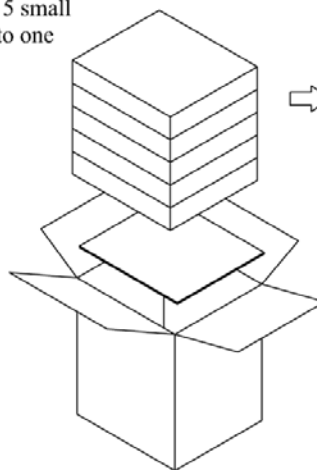
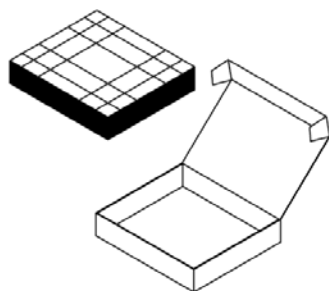
PRODUCT PART NO.:YMS12864-18AEBFDGL

PACKING TYPE: BY EPE TRAY(T12864-18A)

PACKLING ORDER:

1) Putting 16 pcs Modules
on each EPE tray.2) Putting 7 pcs EPE trays together with
EPE board on the top of EPE tray.3) Assembling the boards
and the tray together with
adhesive tape4) Putting in the inner
small carton (TYPE:H82)5) Putting 5 small
cartons into one
outcarton

6) Packing finished



Note: 16 pcs in a tray, 7 trays in a inner carton, 5 inner cartons in a out carton, so 16x7x5=560pcs/Outcarton

Dimension (Small carton): 385*325*87mm

Dimension (Out carton): 394*344*470mm

Drw:

Chk:

Apv:

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VII. Precautions For Use

1. Safety

- (1) Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
- (2) If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3) If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

2. Storage Conditions

- (1) Store the panel or module in a dark place where the temperature is $23\pm5^{\circ}\text{C}$ and the humidity is below $50\pm20\%\text{RH}$.
- (2) Store in anti-static electricity container.
- (3) Store in clean environment, free from dust, active gas, and solvent.
- (4) Do not place the module near organics solvents or corrosive gases.
- (5) Do not crush, shake, or jolt the module.
- (6) Do not exposed to direct sun light of fluorescent lamps.

3. Installing LCD Module

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate or touch panel to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements.

4. Precautions For Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (V_o). Adjust V_o to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.

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(4) When turning the power on, input each signal after the positive/negative voltage becomes stable.

(5) Do not apply water or any liquid on product which composed of T/P.

5. Handling Precautions

(1) Avoid static electricity which can damage the CMOS LSI; please wear the wrist strap when handling.

(2) The polarizing plate of the display is very fragile. so, please handle it very carefully.

(3) Do not give external shock.

(4) Do not apply excessive force on the surface; it may cause display abnormal .

(5) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

(6) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.

(7) Do not operate it above the absolute maximum rating.

(8) Do not remove the panel or frame from the module.

(9) Do not apply water or any liquid on product which composed of T/P.

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