

### • General Description

The AGM15T03LL combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### • Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche test
- 100% DVDS tested

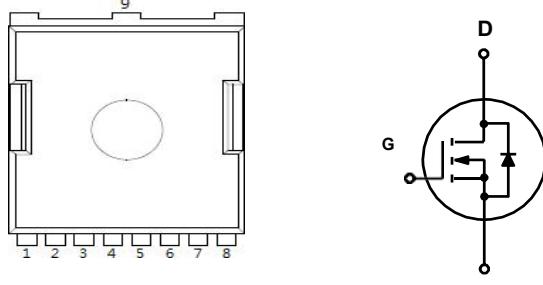
### • Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### Product Summary

BVDSS	RDS(on)	ID
150V	3.1mΩ	220A

### TOLL-8L Pin Configuration



Pin	Description
1	Gate(G)
2,3,4,5,6,7,8	Source(S)
9	Drain(D)

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM15T03LL	AGM15T03LL	TOLL	330mm	25mm	2000

Table 1. Absolute Maximum Ratings (TC=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	150	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	220	A
	Drain Current-Continuous(Tc=100°C)	155	A
IDM (pulse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	880	A
PD	Maximum Power Dissipation(Tc=25°C)	429	W
	Maximum Power Dissipation(Tc=100°C)	214	W
EAS	Avalanche energy <b>(Note 3)</b>	720	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 175	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	60	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	0.35	°C/W

**Table 3. Electrical Characteristics (TC=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	150	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=150V, VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V, VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=250μA	2.0	2.9	4.0	V
gFS	Forward Transconductance	VDS=5V, ID=10A	--	84	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A	--	3.1	3.7	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=75V, VGS=0V, F=1MHZ	--	10640	--	pF
Coss	Output Capacitance		--	729	--	pF
Crss	Reverse Transfer Capacitance		--	182	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V, f=1.0MHz	--	1.9	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V, VDS=75V, ID=20A, RGEN=10Ω	--	34	--	nS
tr	Turn-on Rise Time		--	30	--	nS
td(off)	Turn-Off Delay Time		--	44	--	nS
tf	Turn-Off Fall Time		--	19	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=75V, ID=20A	--	206	--	nC
Qgs	Gate-Source Charge		--	44	--	nC
Qgd	Gate-Drain Charge		--	70	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	220	A
VSD	Forward on Voltage	VGS=0V, IS=10A	--	0.9	1.2	V
trr	Reverse Recovery Time	VR=75V, IF=10A , dI/dt=100A/μs , TJ=25°C	--	101	--	ns
Qrr	Reverse Recovery Charge		--	253	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

Figure 1. Typical Output Characteristics

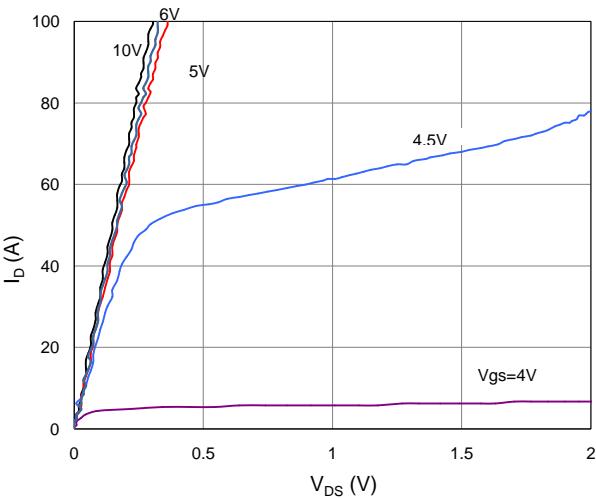


Figure 2. On-Resistance vs. Gate-Source Voltage

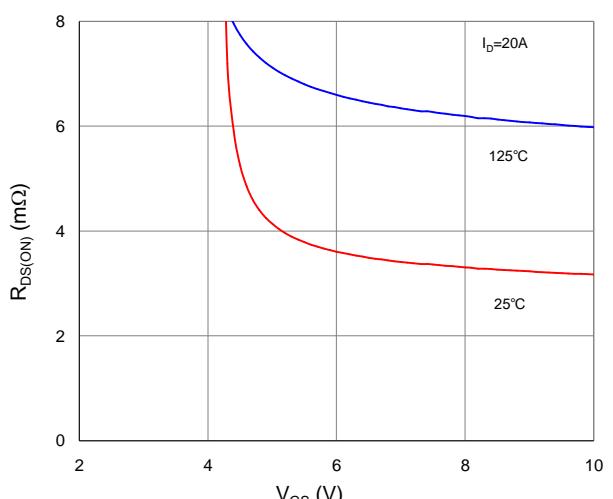


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

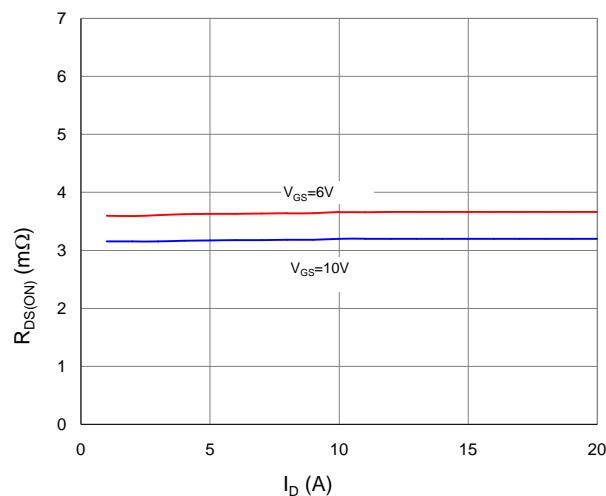


Figure 4. Normalized On-Resistance vs. Junction Temperature

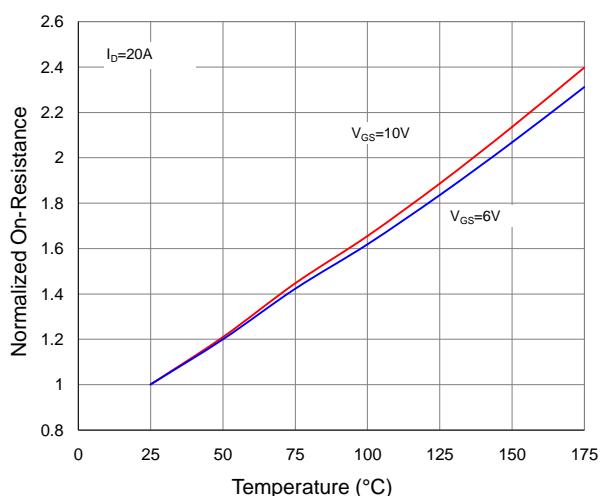


Figure 5. Typical Transfer Characteristics

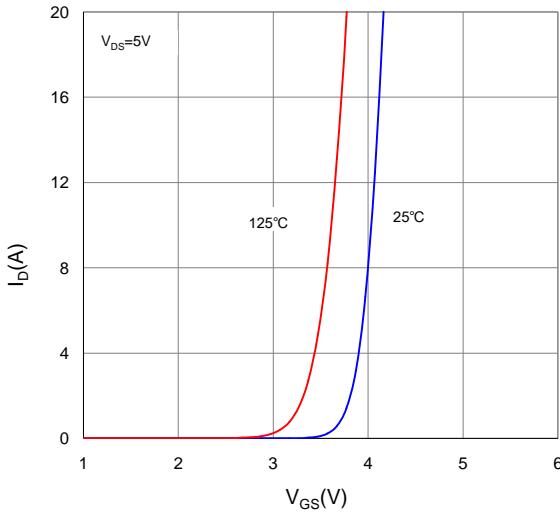


Figure 6. Typical Source-Drain Diode Forward Voltage

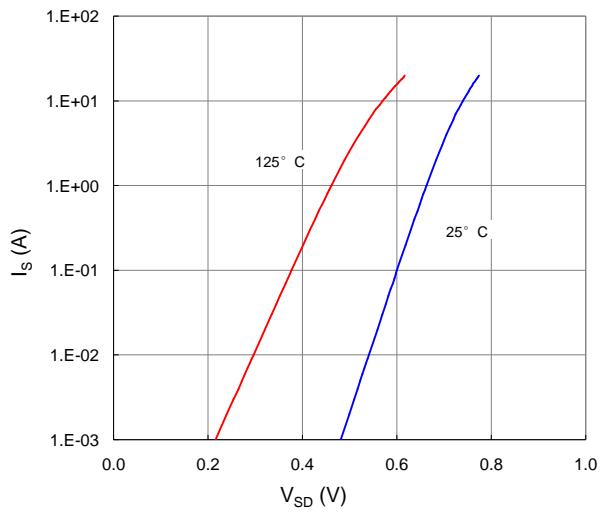


Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

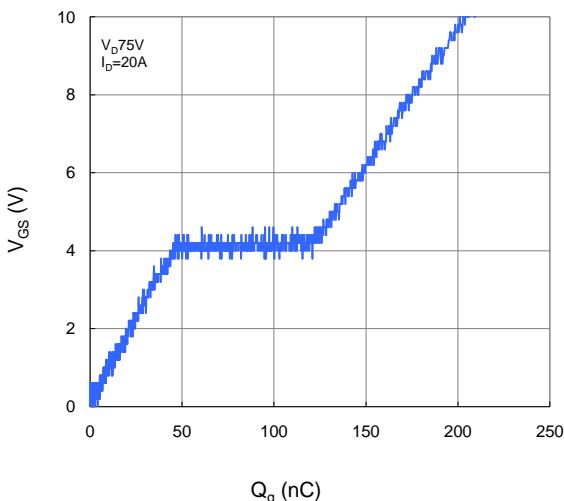


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

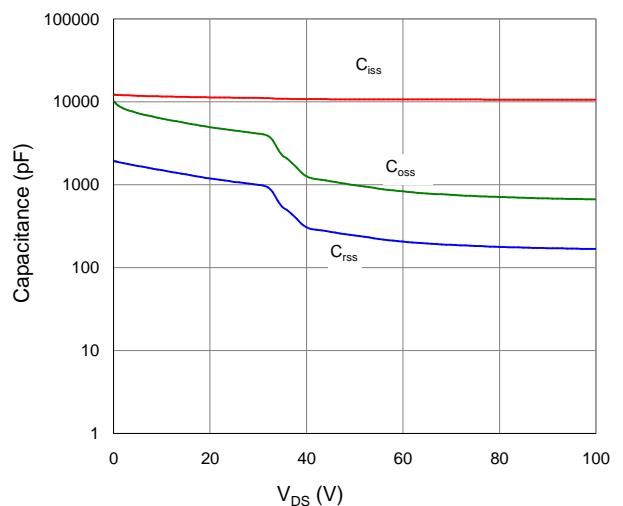


Figure 9. Maximum Safe Operating Area

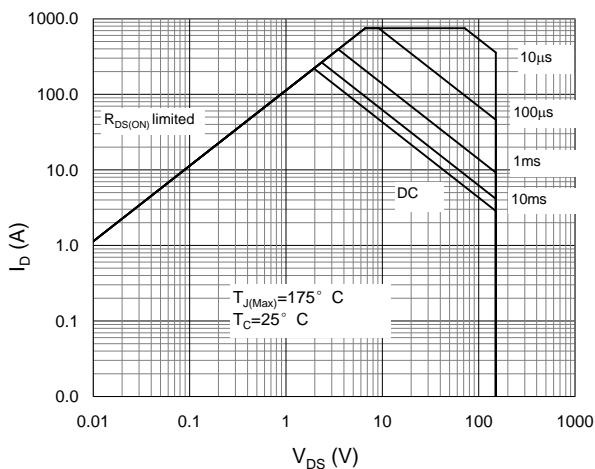


Figure 10. Maximum Drain Current vs. Case Temperature

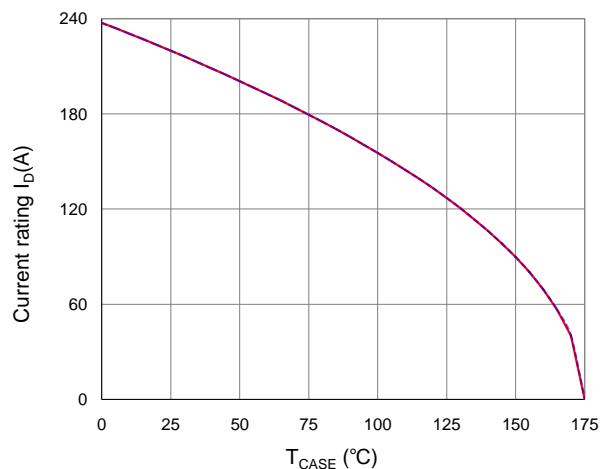
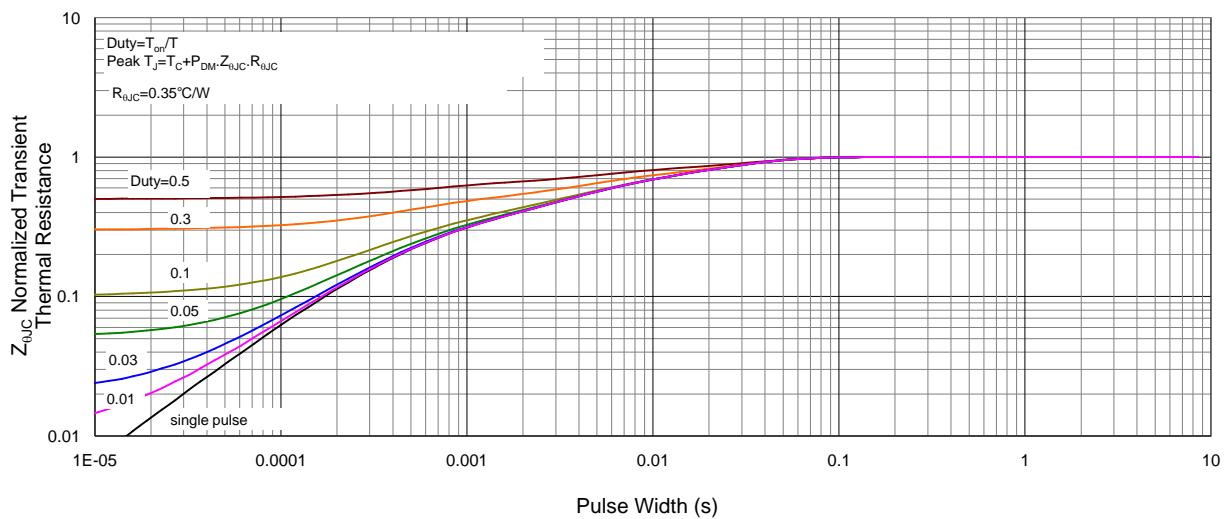
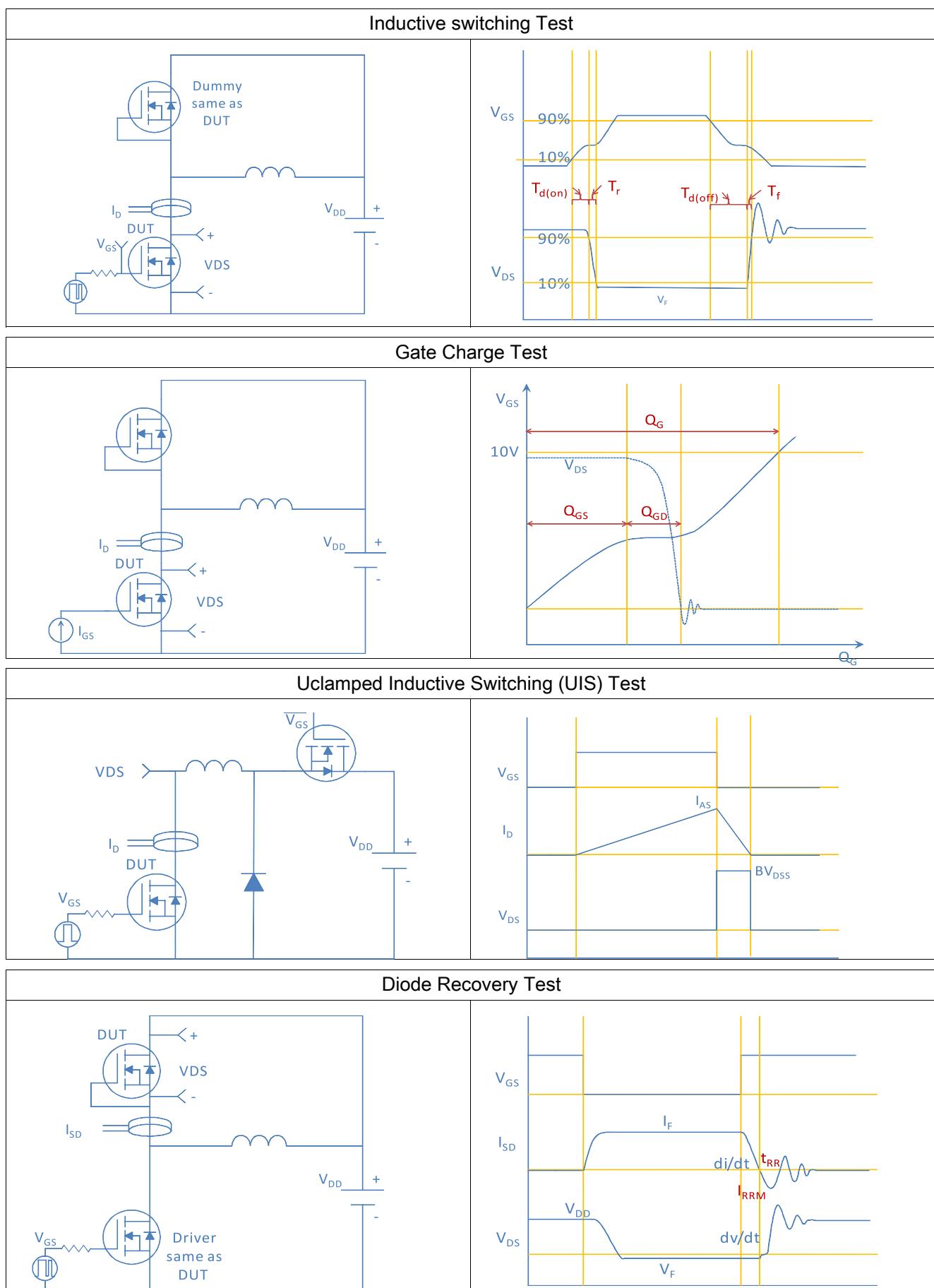
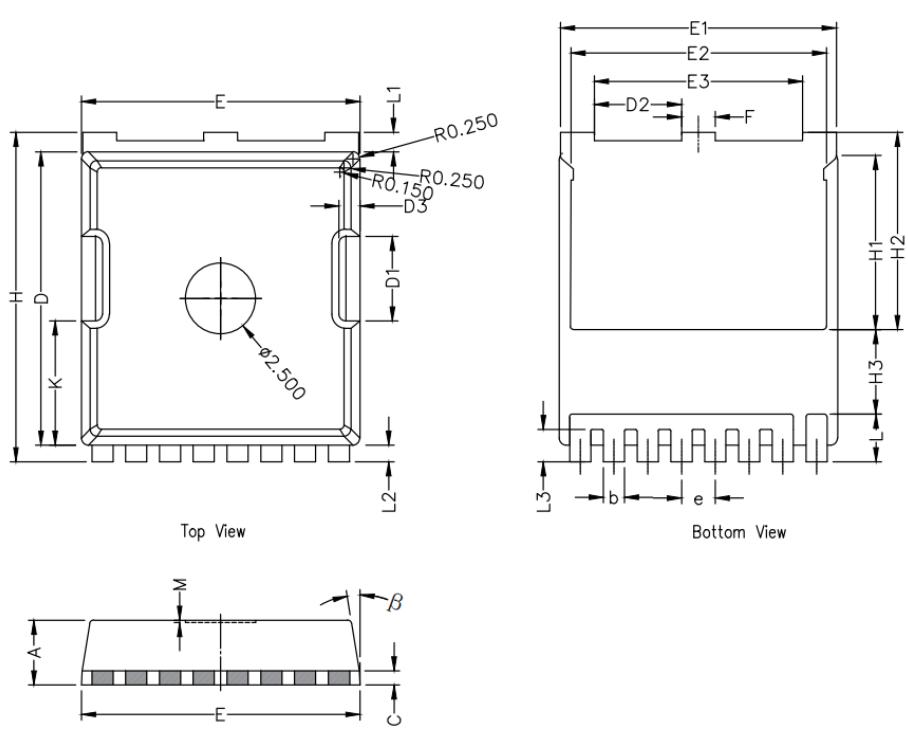
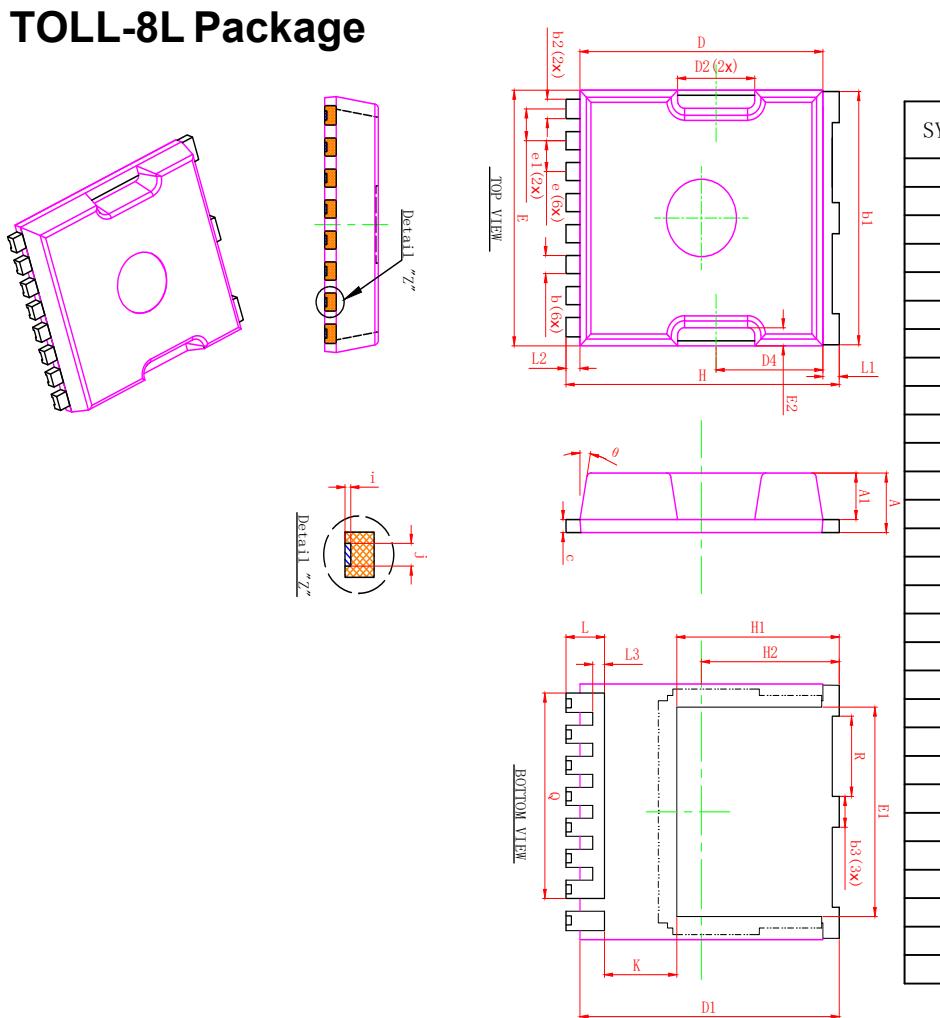


Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case





## TOLL-8L Package



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