General Description

The AGM308MN combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance
- 100% Avalanche tested
- 100% DVDS tested

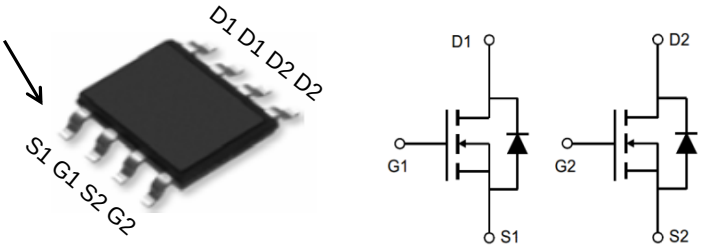
Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
30V	8.8mΩ	15A

SOP-8 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM308MN	AGM308MN	SOP8	330mm	12mm	3000

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	30	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Ta=25℃) **(Note 1)**	15	A
	Drain Current-Continuous(Ta=100℃)	6	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed **(Note 2)**	60	A
PD	Maximum Power Dissipation(Ta=25℃)	2.5	W
	Maximum Power Dissipation(Ta=100℃)	1.0	W
EAS	Avalanche energy **(Note 3)**	43	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	℃

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State)[1]	---	50	℃/W

Table 3. Electrical Characteristics (TA=25℃unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	30	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=30V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.1	1.5	2.5	V
gFS	Forward Transconductance	VDS=5V,ID=10A	--	11	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=20A	--	8.8	10	mΩ
		VGS=4.5V, ID=15A	--	11	14	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VGS=0V, F=1MHZ	--	980	--	pF
Coss	Output Capacitance		--	205	--	pF
Crss	Reverse Transfer Capacitance		--	120	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=10V,VDS=15V, RL=0.75Ω,RGEN=3.3Ω	--	20	--	nS
tr	Turn-on Rise Time		--	15	--	nS
td(off)	Turn-Off Delay Time		--	60	--	nS
tf	Turn-Off Fall Time		--	11	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=25V, ID=8A	--	12	--	nC
Qgs	Gate-Source Charge		--	4.0	--	nC
Qgd	Gate-Drain Charge		--	6.0	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	15	A
VSD	Forward on Voltage	VGS=0V,IS=25A	--	--	1.2	V
trr	Reverse Recovery Time	IF=25A , dI/dt=100A/μs , TJ=25℃	--	--	--	ns
Qrr	Reverse Recovery Charge		--	--	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25℃

Fig.1 Power Dissipation

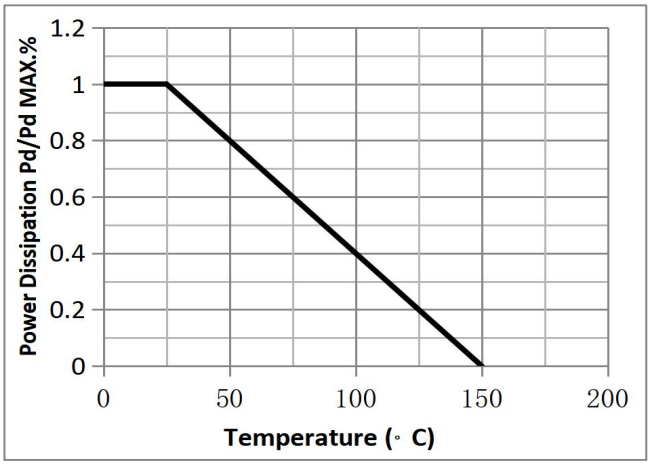


Fig.2 Typical output Characteristics

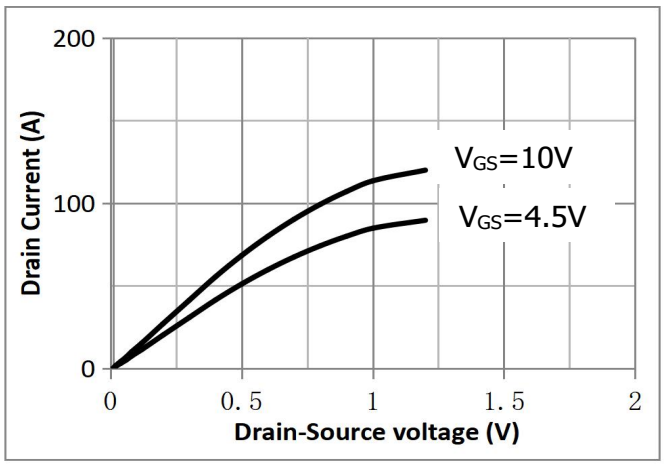


Fig.3 Threshold Voltage V.S Junction Temperature

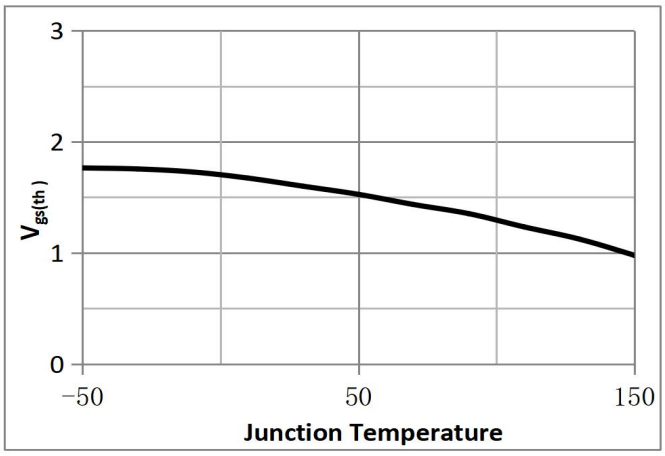


Fig.4 Resistance V.S Drain Current

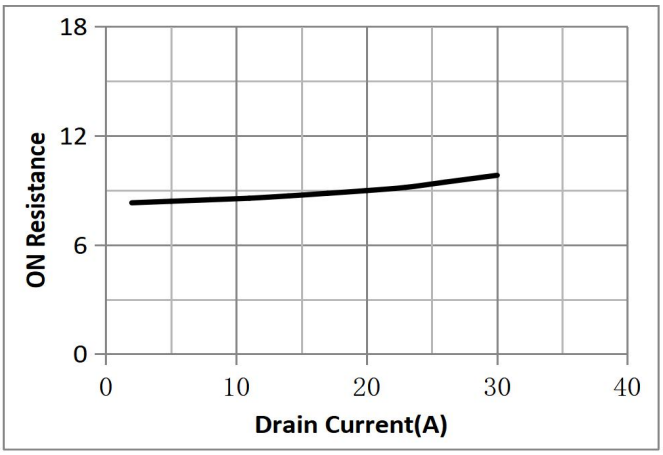


Fig.5 On-Resistance VS Gate Source Voltage

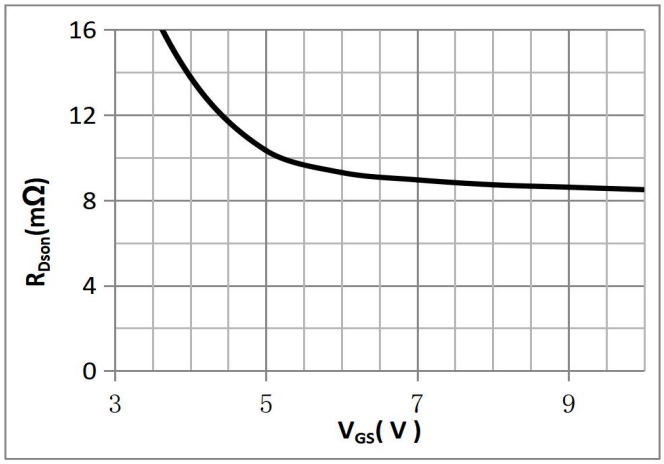


Fig.6 On-Resistance V.S Junction Temperature

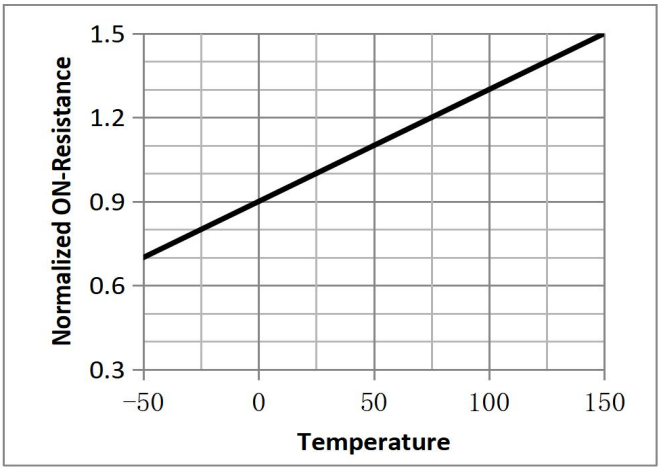


Fig.7 Switching Time Measurement Circuit

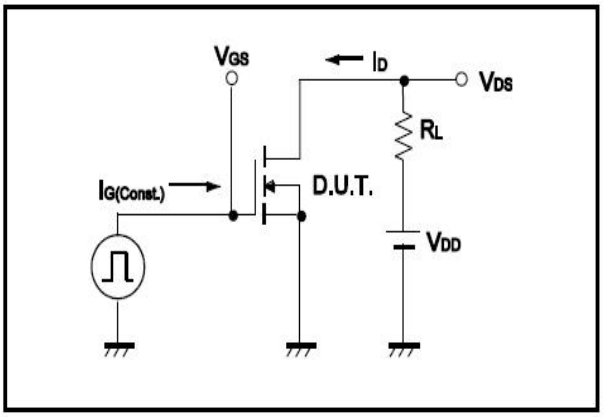


Fig.8 Gate Charge Waveform

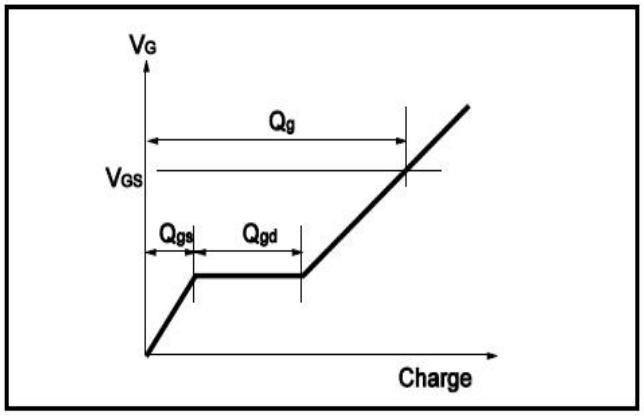


Fig.9 Switching Time Measurement Circuit

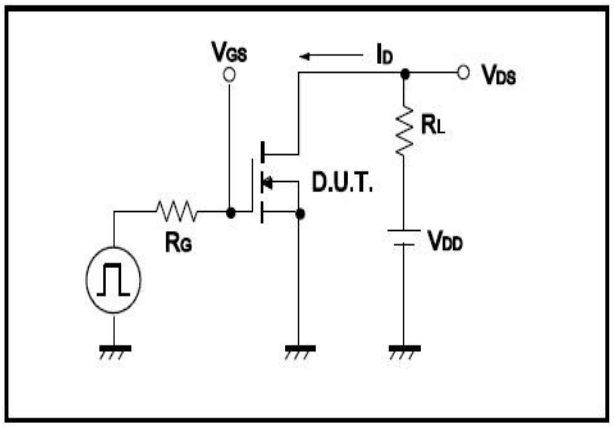


Fig.10 Gate Charge Waveform

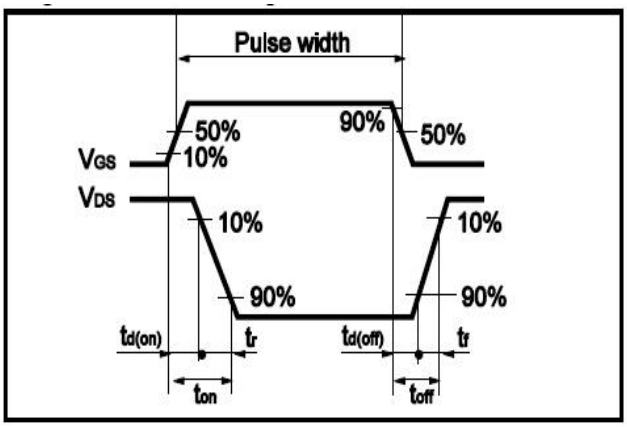


Fig.11 Avalanche Measurement Circuit

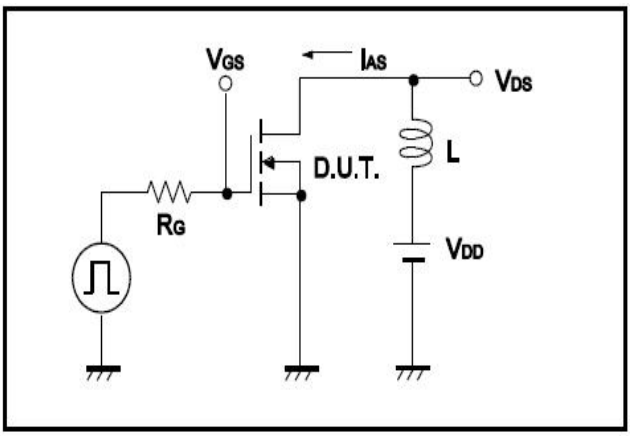
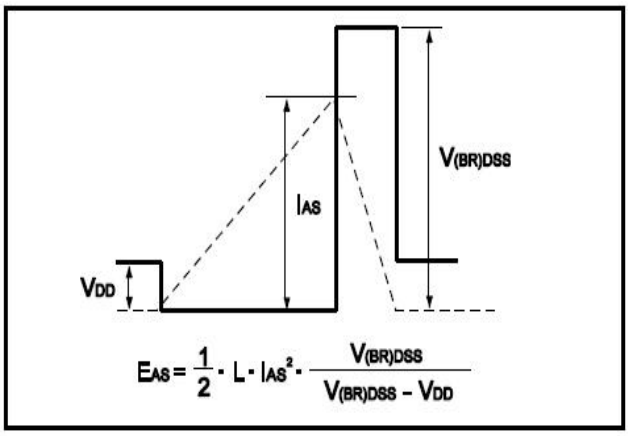
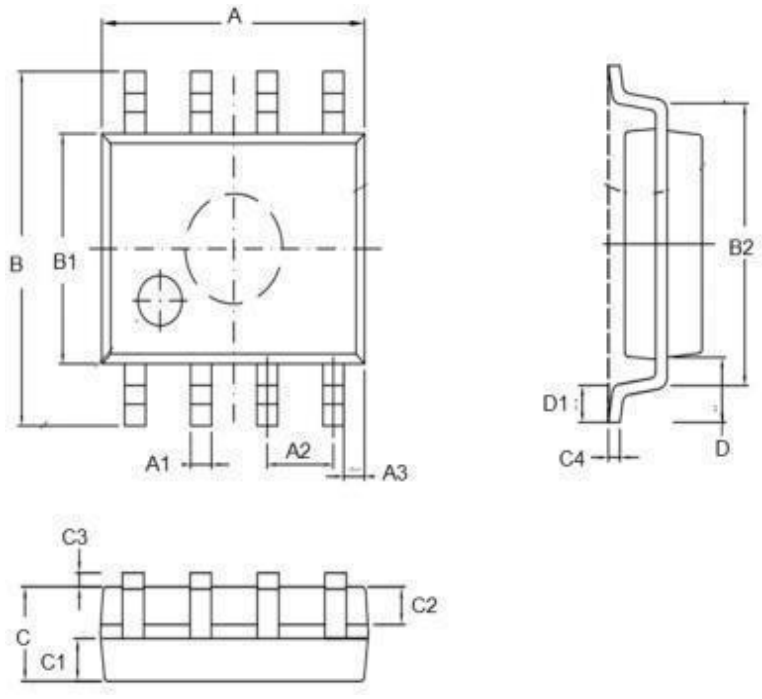


Fig.12 Avalanche Waveform



•Dimensions(SOP8)

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.80		5.00	C	1.30		1.50
A1	0.37		0.47	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.20
B	5.80		6.20	C4	0.19	0.20	0.23
B1	3.80		4.00	D		1.05	
B2		5.00		D1	0.40		0.62



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This document is the third version issued on July10th, 2023. This document replaces all previously provided information.