

### ● General Description

The AGM401A combines advanced trench MOSFET technology with a low resistance package to provide extremely low  $R_{DS(ON)}$ .

This device is ideal for load switch and battery protection applications.

### ● Features

- Advance high cell density Trench technology
- Low  $R_{DS(ON)}$  to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

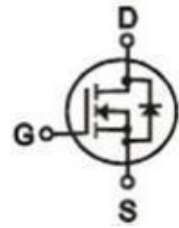
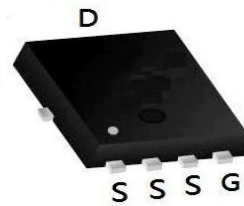
### ● Application

- MB/VGA Vcore
- SMPS 2<sup>nd</sup> Synchronous Rectifier
- POL application
- BLDC Motor driver

### Product Summary

BVDSS	RDSON	ID
40V	0.86mΩ	200A

### PDFN5\*6 Pin Configuration



### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM401A	AGM401A	PDFN5*6	----	----	3000

**Table 1. Absolute Maximum Ratings (TA=25°C)**

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) <b>(Note 1)</b>	200	A
	Drain Current-Continuous(Tc=100°C)	130	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed <b>(Note 2)</b>	800	A
PD	Maximum Power Dissipation(Tc=25°C)	92.6	w
	Maximum Power Dissipation(Tc=100°C)	37	w
EAS	Avalanche energy <b>(Note 3)</b>	450	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

**Table 2. Thermal Characteristic**

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>	---	62	°C/W
RθJC	Thermal Resistance Junction-Case <sup>1</sup>	---	1.35	°C/W

**Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>On/Off States</b>						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=250μA	40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=40V,VGS=0V	--	--	1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=250μA	1.0	--	3.0	V
gFS	Forward Transconductance	VDS=10V,ID=50A	--	120	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=10V, ID=50A	--	0.86	1.1	mΩ
		VGS=4.5V, ID=50A	--	--	--	mΩ
<b>Dynamic Characteristics</b>						
Ciss	Input Capacitance	VDS=20V,VGS=0V, F=1MHZ	--	7500	--	pF
Coss	Output Capacitance		--	230	--	pF
Crss	Reverse Transfer Capacitance		--	3.2	--	pF
Rg	Gate resistance	VGS=0V, VDS=0V,f=1.0MHz	--	--	--	Ω
<b>Switching Times</b>						
td(on)	Turn-on Delay Time	VGS=10V,VDS=20V, ID=50A,RGEN=3Ω	--	20	--	nS
tr	Turn-on Rise Time		--	32	--	nS
td(off)	Turn-Off Delay Time		--	98	--	nS
tf	Turn-Off Fall Time		--	32	--	nS
Qg	Total Gate Charge	VGS=10V, VDS=20V, ID=50A	--	115	--	nC
Qgs	Gate-Source Charge		--	24	--	nC
Qgd	Gate-Drain Charge		--	19	--	nC
<b>Source-Drain Diode Characteristics</b>						
ISD	Source-Drain Current(Body Diode)		--	--	200	A
VSD	Forward on Voltage	VGS=0V,IS=50A	--	--	1.0	V
trr	Reverse Recovery Time	IF=50A , dI/dt=100A/μs , TJ=25°C	--	64	--	ns
Qrr	Reverse Recovery Charge		--	98	--	nc

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

## Typical Performance Characteristics

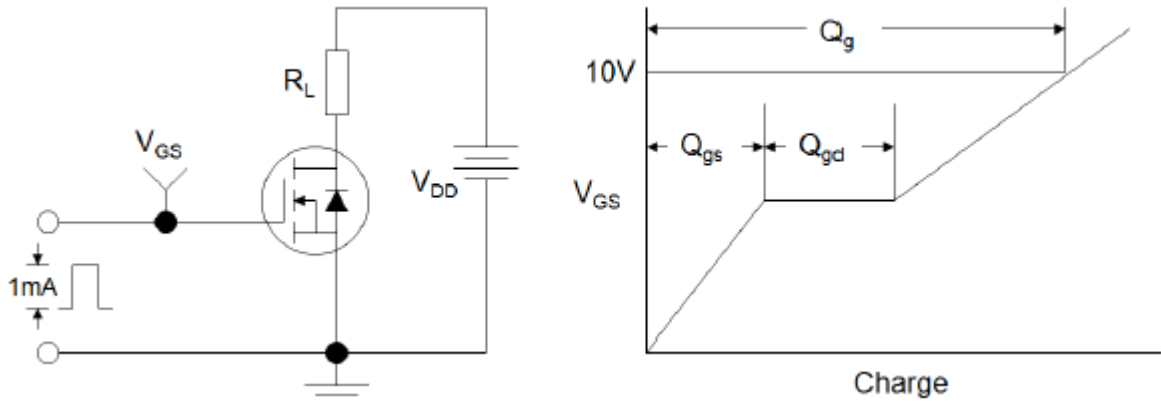


Figure1: Gate Charge Test Circuit & Waveform

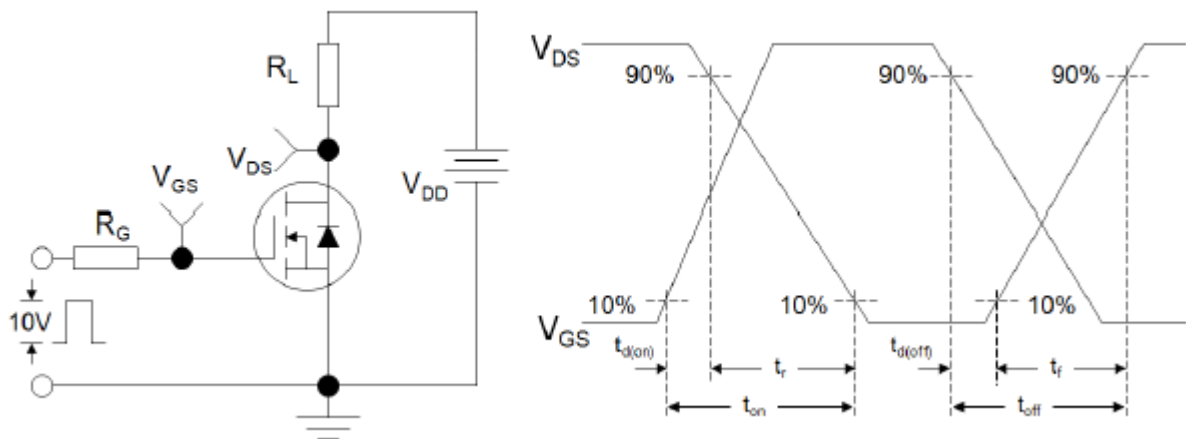


Figure 2: Resistive Switching Test Circuit & Waveforms

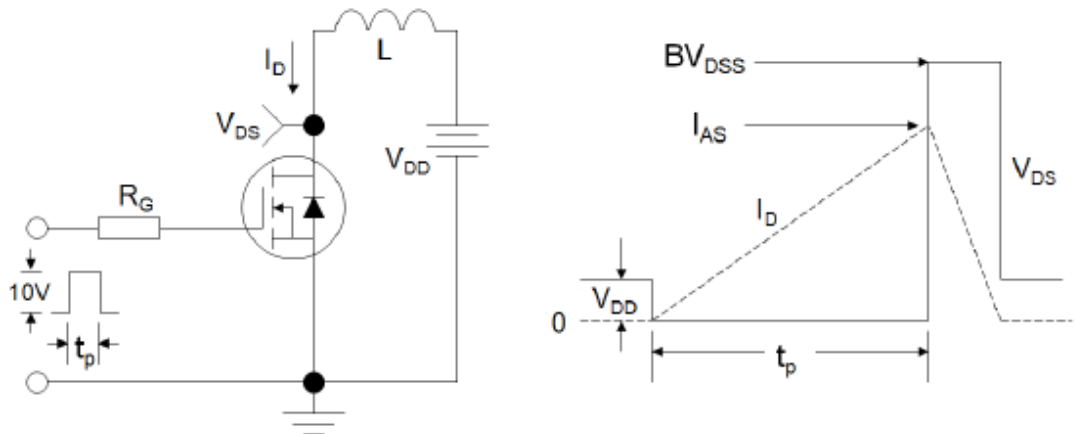


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

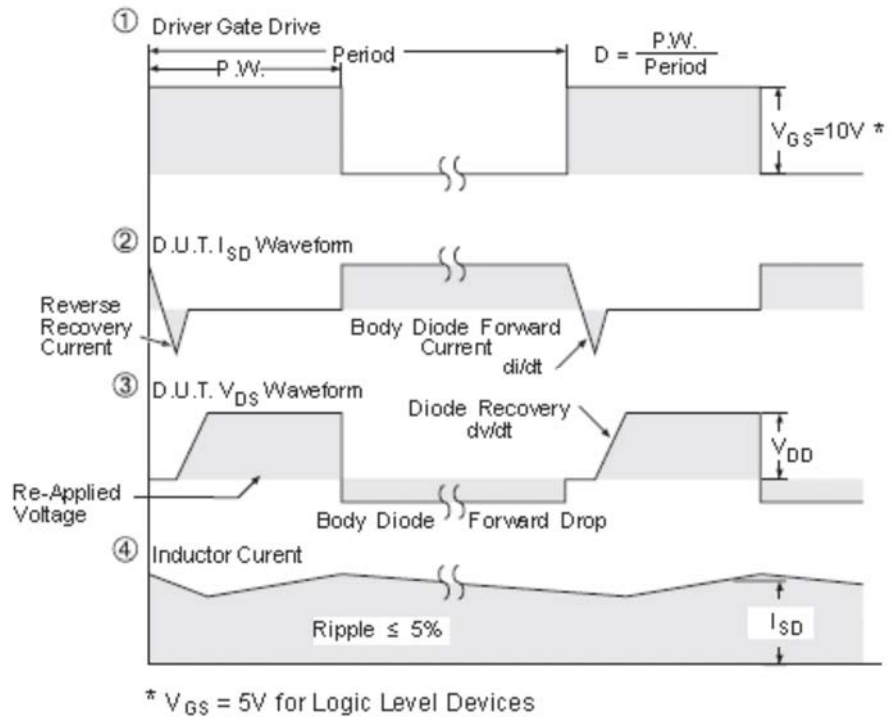
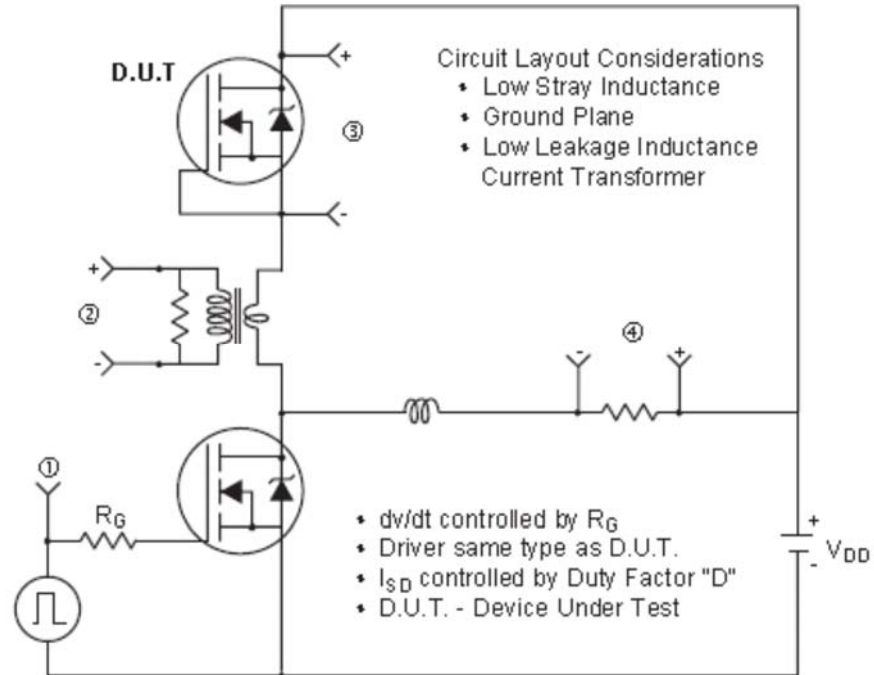
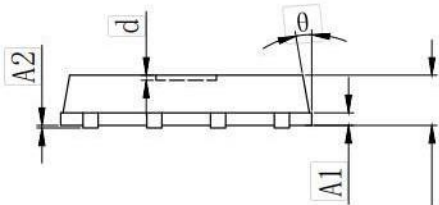
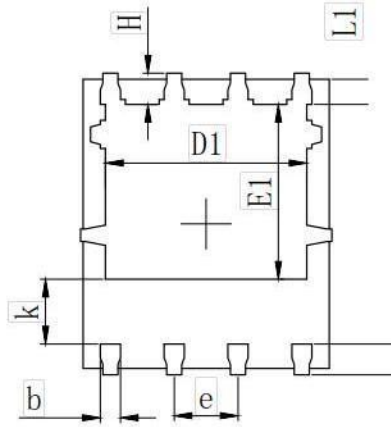
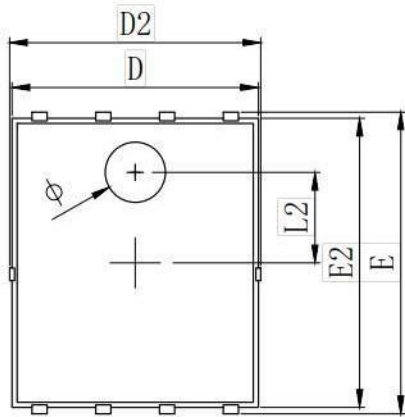


Figure 4: Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms (For N-channel)

**•Dimensions (DFN5×6)**


SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	0.900	1.000	1.100
A1	0.254 REF.		
A2	0~0.05		
D	4.824	4.900	4.976
D1	3.910	4.010	4.110
D2	4.924	5.000	5.076
E	5.924	6.000	6.076
E1	3.375	3.475	3.575
E2	5.674	5.750	5.826
b	0.350	0.400	0.450
e	1.270 TYP.		
L	0.534	0.610	0.686
L1	0.424	0.500	0.576
L2	1.800 REF.		
k	1.190	1.290	1.390
H	0.549	0.625	0.701
θ	8°	10°	12°
Ø	1.100	1.200	1.300
d			0.100


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