

● General Description

The AGM40P35D combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

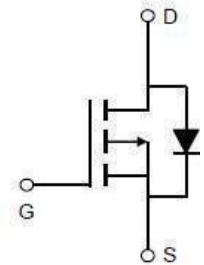
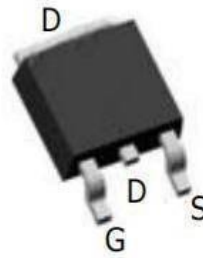
● Application

- MB/VGA Vcore
- SMPS 2nd Synchronous Rectifier
- POL application
- BLDC Motor driver

Product Summary

BVDSS	RDSON	ID
-40V	11mΩ	-60A

TO-252 Pin Configuration



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM40P35D	AGM40P35D	TO-252	----	----	2500

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
VDS	Drain-Source Voltage (VGS=0V)	-40	V
VGS	Gate-Source Voltage (VDS=0V)	±20	V
ID	Drain Current-Continuous(Tc=25°C) (Note 1)	-60	A
	Drain Current-Continuous(Tc=100°C)	--	A
IDM (pluse)	Drain Current-Continuous@ Current-Pulsed (Note 2)	-160	A
PD	Maximum Power Dissipation(Tc=25°C)	30	w
	Maximum Power Dissipation(Tc=100°C)	12	w
EAS	Avalanche energy (Note 3)	--	mJ
TJ,TSTG	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
RθJA	Thermal Resistance Junction-ambient (Steady State) ¹	---	--	°C/W
RθJC	Thermal Resistance Junction-Case ¹	---	4.17	°C/W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BVDSS	Drain-Source Breakdown Voltage	VGS=0V ID=-250μA	-40	--	--	V
IDSS	Zero Gate Voltage Drain Current	VDS=-40V,VGS=0V	--	--	-1	μA
IGSS	Gate-Body Leakage Current	VGS=±20V,VDS=0V	--	--	±100	nA
VGS(th)	Gate Threshold Voltage	VDS=VGS,ID=-250μA	-1.5	-1.7	-2.5	V
gFS	Forward Transconductance	VDS=5V,ID=-12A	--	26	--	S
RDS(on)	Drain-Source On-State Resistance	VGS=-10V, ID=-12A	--	11	15	mΩ
		VGS=-4.5V, ID=-12A	--	17.5	24	mΩ
Dynamic Characteristics						
Ciss	Input Capacitance	VDS=-20V,VGS=0V, F=1MHZ	--	2660	--	pF
Coss	Output Capacitance		--	360	--	pF
Crss	Reverse Transfer Capacitance		--	248	--	pF
Rg	Gate resistance	f=1.0MHz	--	--	--	Ω
Switching Times						
td(on)	Turn-on Delay Time	VGS=-10V,VDS=-20V, ID=-12A,RGEN=3Ω	--	10	--	nS
tr	Turn-on Rise Time		--	18	--	nS
td(off)	Turn-Off Delay Time		--	38	--	nS
tf	Turn-Off Fall Time		--	24	--	nS
Qg	Total Gate Charge	VGS=-10V, VDS=-20V, ID=-12A	--	72	--	nC
Qgs	Gate-Source Charge		--	14	--	nC
Qgd	Gate-Drain Charge		--	15	--	nC
Source-Drain Diode Characteristics						
ISD	Source-Drain Current(Body Diode)		--	--	-60	A
VSD	Forward on Voltage	VGS=0V,IS=-12A	--	--	-1.2	V
trr	Reverse Recovery Time	Isd=-12A ,	--	40	--	ns
Qrr	Reverse Recovery Charge	dI/dt=100A/μs , TJ=25℃	--	42	--	nc

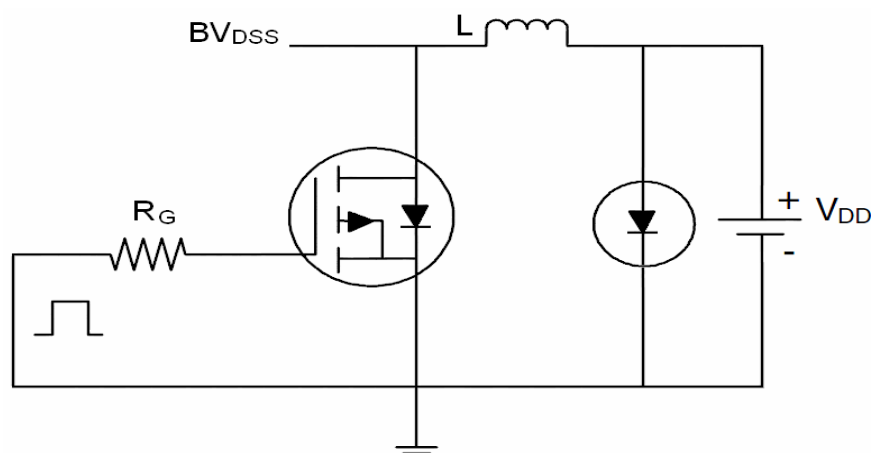
Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

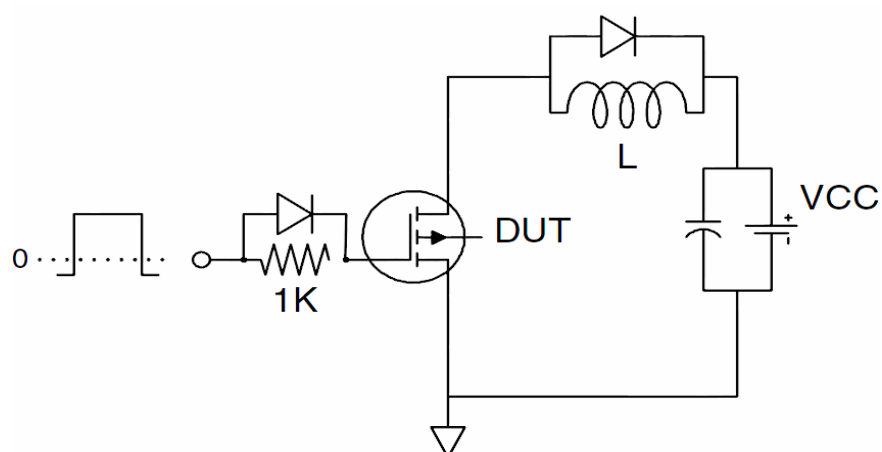
Notes 3.EAS condition: TJ=25°C

Test Circuit

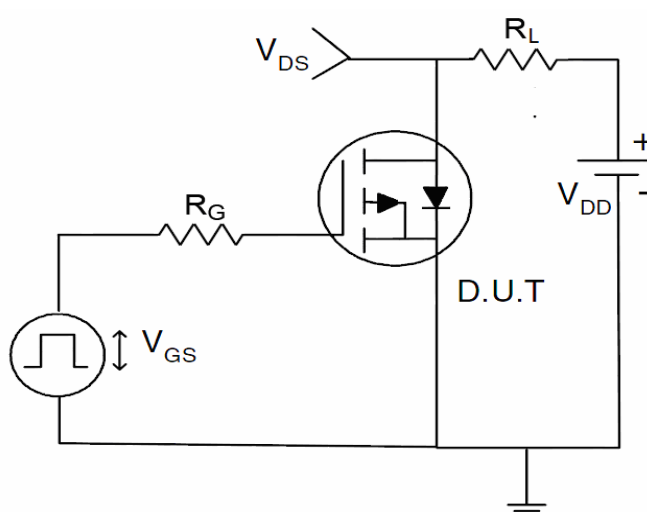
1) E_{AS} Test Circuit



2) Gate Charge Test Circuit



3) Switch Time Test Circuit



Typical Electrical and Thermal Characteristics (Curves)

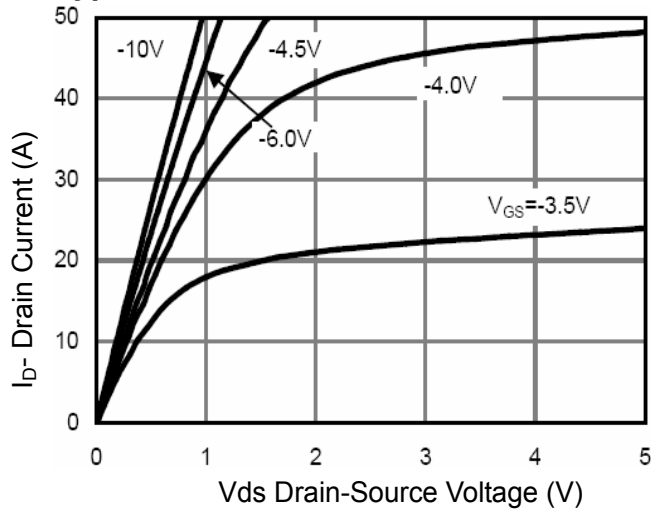


Figure 1 Output Characteristics

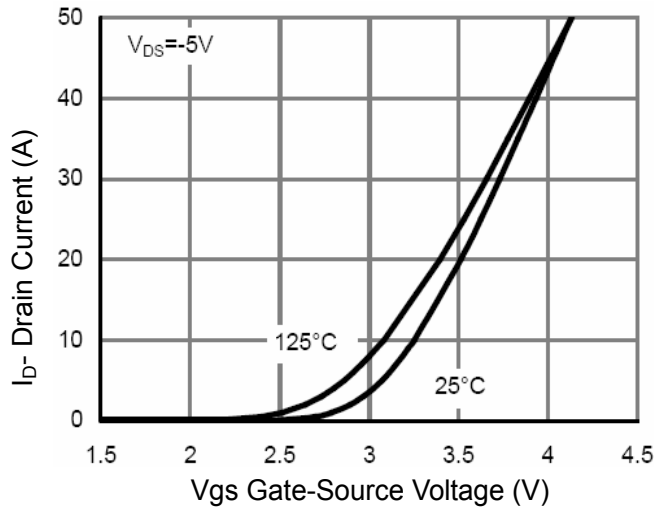


Figure 2 Transfer Characteristics

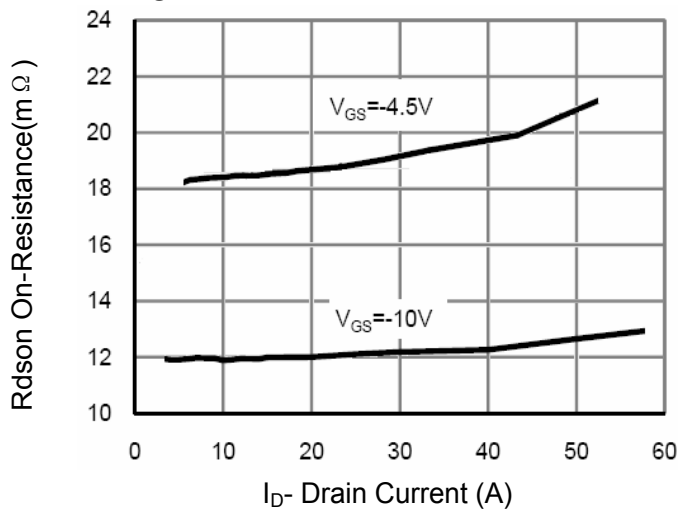


Figure 3 Rdson- Drain Current

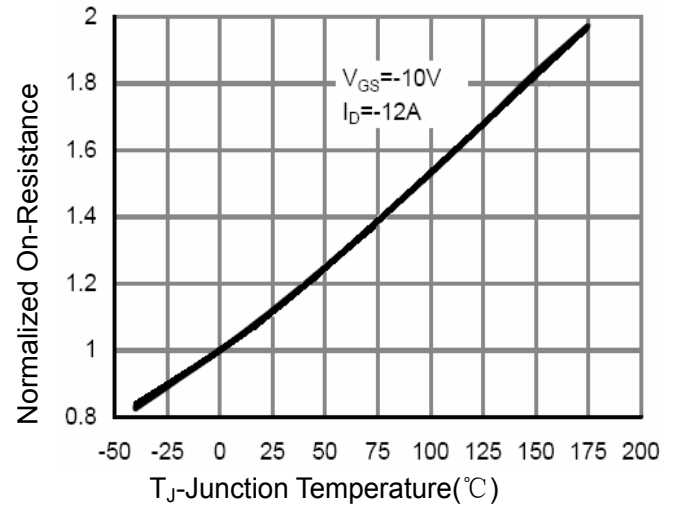


Figure 4 Rdson-Junction Temperature

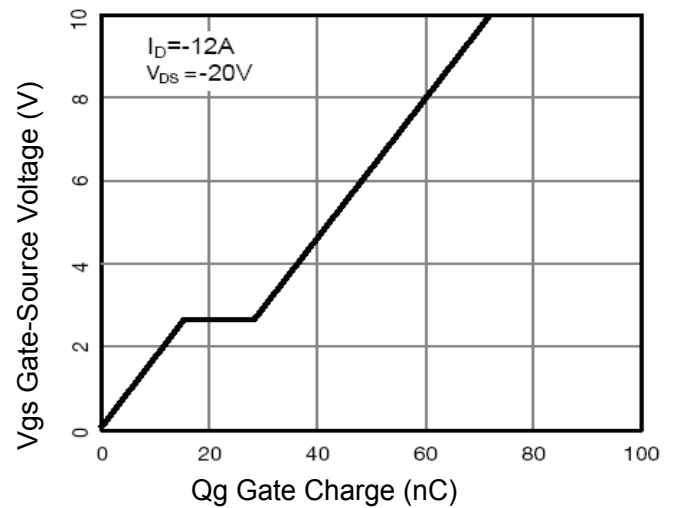


Figure 5 Gate Charge

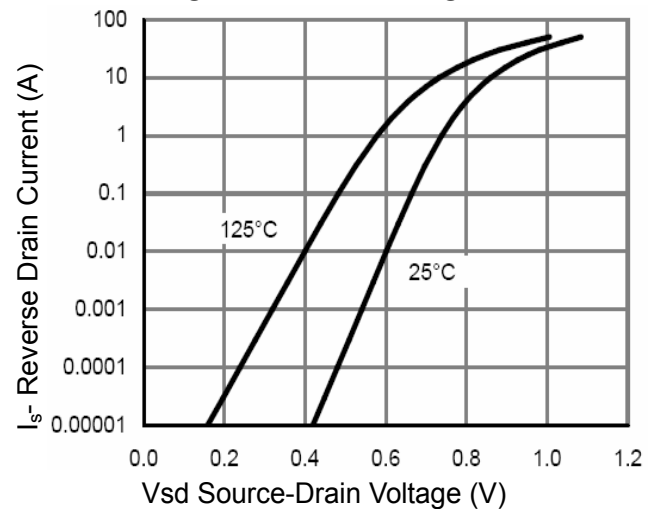


Figure 6 Source- Drain Diode Forward

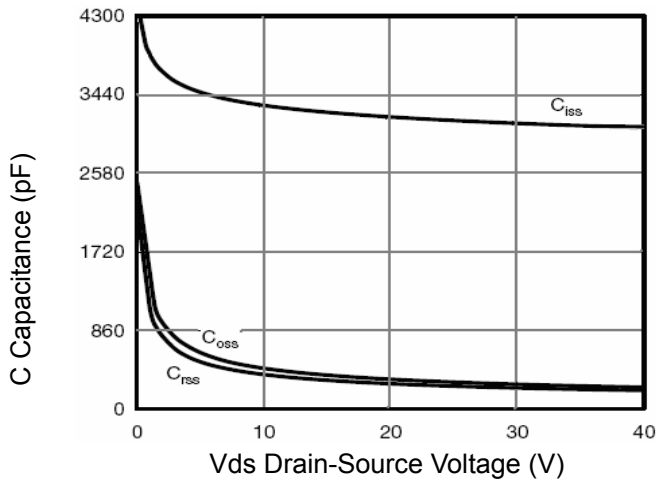


Figure 7 Capacitance vs Vds

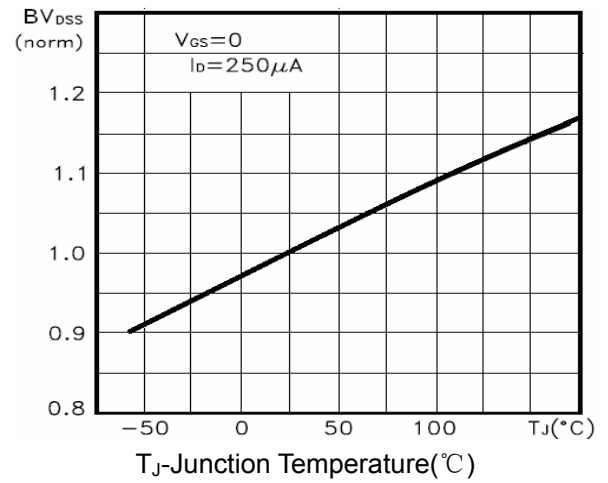


Figure 9 BV_{DSS} vs Junction Temperature

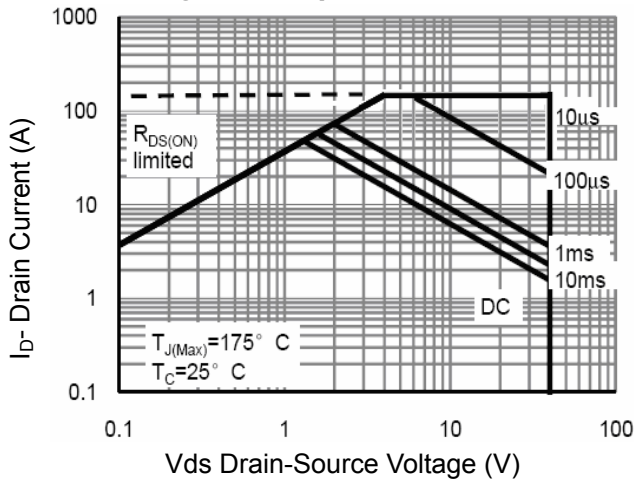


Figure 8 Safe Operation Area

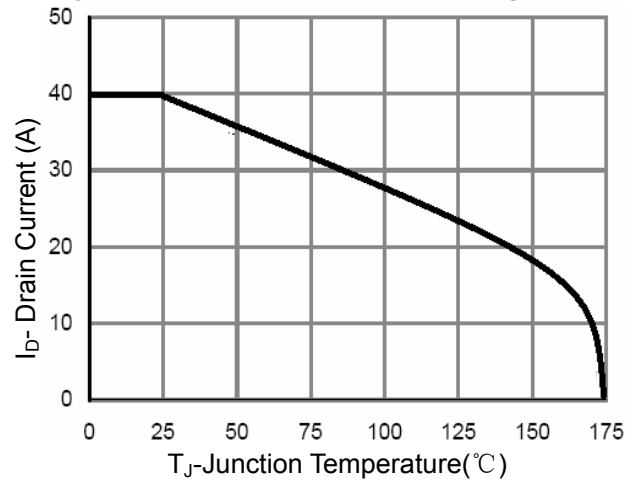


Figure 10 I_D Current Derating vs Junction Temperature

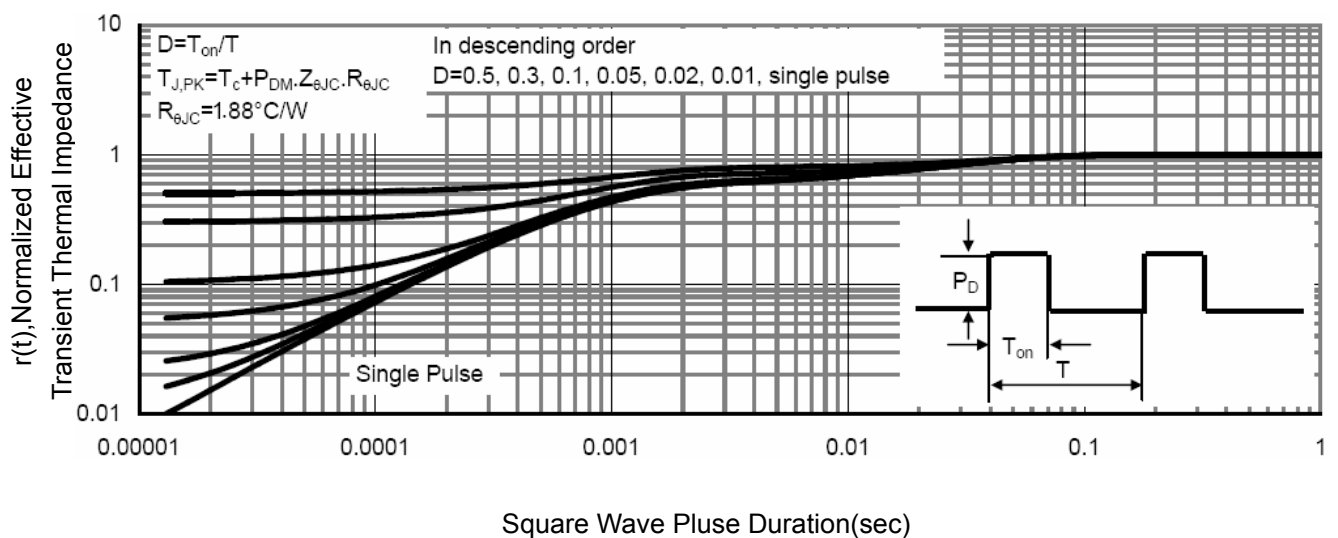
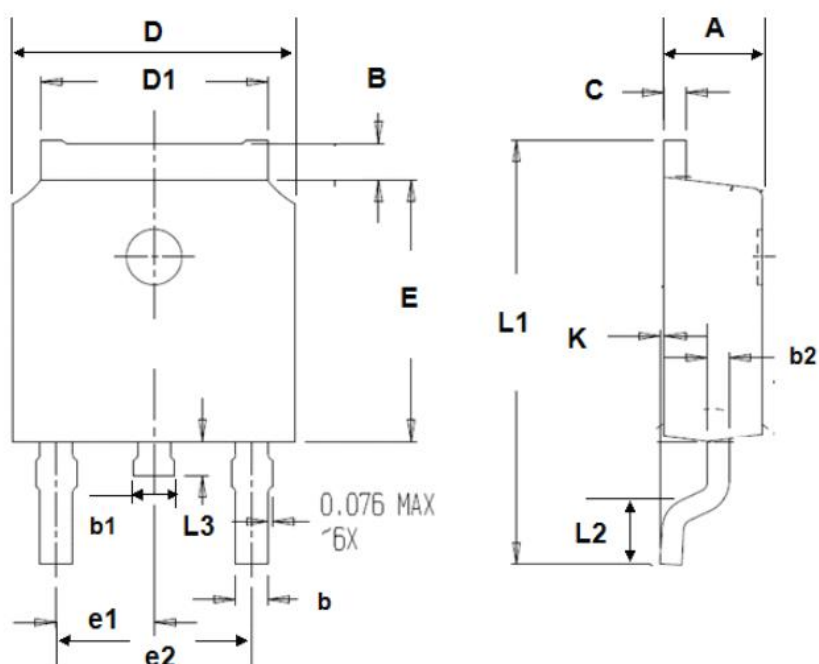


Figure 11 Normalized Maximum Transient Thermal Impedance

Dimensions

SYMBOL	min	max	SYMBOL	min	max
A	2.10	2.50	B	0.85	1.25
b	0.50	0.80	b1	0.50	0.90
b2	0.45	0.70	C	0.45	0.70
D	6.30	6.75	D1	5.10	5.50
E	5.30	6.30	e1	2.25	2.35
L1	9.20	10.60	e2	4.45	4.75
L2	0.90	1.75	L3	0.60	1.10
K	0.00	0.23			



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