

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

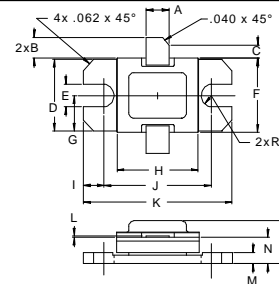
The **ASI ALR200** is Designed for

FEATURES:

- Input Matching Network
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	16 A
V_{CC}	40 V
P_{DISS}	575 W @ T _C = 25 °C
T_J	-65 °C to +250 °C
T_{STG}	-65 °C to +200 °C
θ_{JC}	0.26 °C/W

PACKAGE STYLE .400 2L FLG (A)


DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.135 / 3.43	.145 / 3.68
B	.100 / 2.54	.120 / 3.05
C	.050 / 1.27	
D	.376 / 9.55	.396 / 10.06
E	.110 / 2.79	.130 / 3.30
F	.395 / 10.03	.407 / 10.34
G	.193 / 4.90	
H	.490 / 12.45	.510 / 12.95
I	.100 / 2.54	
J	.690 / 17.53	.710 / 18.03
K	.890 / 22.61	.910 / 23.11
L	.003 / 0.08	.006 / 0.18
M	.052 / 1.32	.072 / 1.83
N	.118 / 3.00	.131 / 3.33
P		.230 / 5.84

ORDER CODE: ASI10515
CHARACTERISTICS T_C = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	I _C = 50 mA	70			V
BV_{CES}	I _C = 50 mA	70			V
BV_{EBO}	I _E = 30 mA	3.0			V
I_{CES}	V _{CE} = 40 V			30	mA
h_{FE}	V _{CE} = 5.0 V I _C = 500 mA	10		---	---
P_G	V _{CC} = 40 V P _{OUT} = 200 W f = 1.2 to 1.4	7.0			dB
η_C	GHz	45			%