

# **Specification for Approval**

Customer:	
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Model Name:

Sı	Supplier Approval		Customer approval
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		



## **Revision Record**

REV NO.	<b>REV DATE</b>	CONTENTS	Note
А	2019-05-17	NEW ISSUE	



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#### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

## 2. General Information

#### LCM

ITEM	STANDARD VALUES	UNITS
LCD type	3.5"TFT	
Dot arrangement	320(RGB)×240	dots
Color filter array	RGB vertical stripe	
Display mode	Transmission / Normally BLACK	
Eyes Viewing Direction	ALL View	
Driver IC	ST7272	
Module size	76.70(W)×63.70(H)×4.42(T)	mm
Active area	70.08(W)×52.56(H)	mm
Dot pitch	0.219(W)×0.219(H)	mm
Interface	24-bit RGB interface	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	10 White LED	
Weight	TBD	g

#### RTP

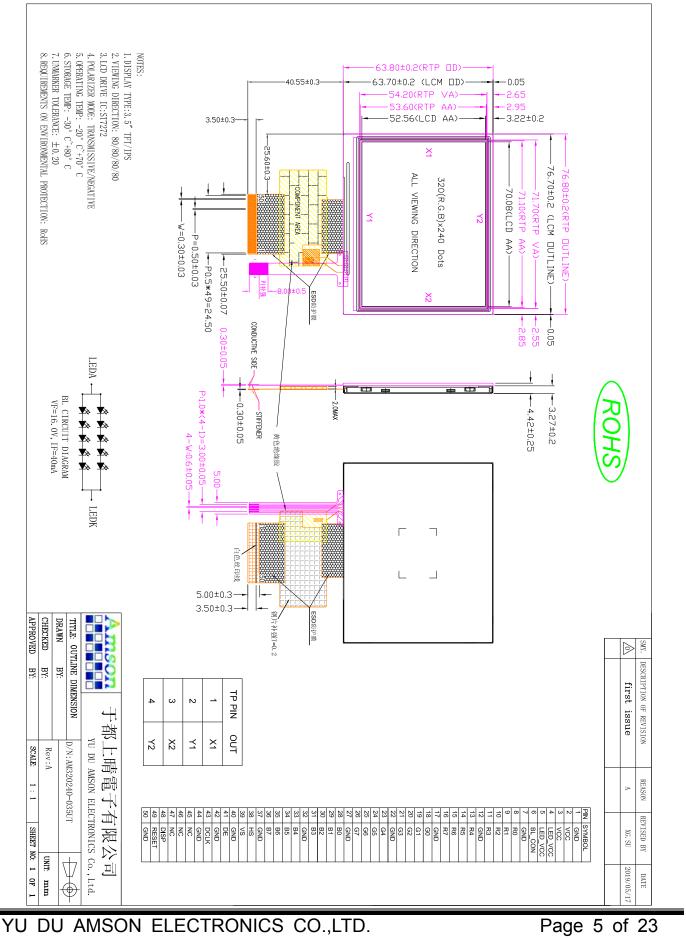
ITEM	STANDARD VALUES	UNITS
RTP type	Film + Glass + FPC	
Surface hardness	ЗН	
Transmittance	≥78%	
RTP size	76.80 (W)×63.80 (H)×1.15(T)	mm
Active area	71.10 (W)×53.60 (H)	mm
Response Time	≤10ms	ms
Response Time	Response Time ≤1.5%	
Insulation resistance	>20MΩ	ΜΩ
Operation force	≤100g	g
X:200Ω ~ 900Ω           Resistance         Y:150Ω ~ 600Ω		Ω

YU DU AMSON ELECTRONICS CO., LTD.



2019-05-17

## 3. External Dimensions





## 4. Interface Description

PINPIN NAMEDESCRIPTION1GNDPower ground.2. 3VCCSupply Voltage4. 5LED_VCCBL VIN Voltage6BL_CONShutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi"=100%, "Low" = 0%.7GNDPower ground.8~11R0-R3Red Data Input12GNDPower ground.13~16R4~R7Red Data Input.17GNDPower ground.18~21G0-G3Green Data Input22GNDPower ground.23~26G4-G7Green Data Input.27GNDPower ground.28~31B0-B3Blue Data Input.32GNDPower ground.33~36B4-B7Blue Data Input.33GNDPower ground.34HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=L_TINING_CONTORLER_SOURCE DRIVER WILL TURN OFF.ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)50GNDPower ground. <th>TFT</th> <th></th> <th></th>	TFT		
2. 3       VCC       Supply Voltage         4, 5       LED_VCC       BL VIN Voltage         6       BL_CON       Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi"=100%, "Low" = 0%.         7       GND       Power ground.         8~11       R0~R3       Red Data Input         12       GND       Power ground.         13~16       R4~R7       Red Data Input.         17       GND       Power ground.         18~21       G0~G3       Green Data Input.         22       GND       Power ground.         23~26       G4~G7       Green Data Input.         27       GND       Power ground.         28~31       B0~B3       Blue Data Input.         32       GND       Power ground.         32-36       B4~B7       Blue Data Input.         37       GND       Power ground.         38       HS       Horizontal sync input. Negative polarity.         39       VS       Vertical sync input. Negative polarity.         40       GND       Power ground.         41       DE       Data Enable signal.         42       GND       Power ground.         43       DCLK <th>PIN</th> <th>PIN NAME</th> <th>DESCRIPTION</th>	PIN	PIN NAME	DESCRIPTION
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6         BL_CON         Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.           7         GND         Power ground.           8~11         R0~R3         Red Data Input           12         GND         Power ground.           13~16         R4~R7         Red Data Input.           17         GND         Power ground.           18~21         G0~G3         Green Data Input.           22         GND         Power ground.           23~26         G4~G7         Green Data Input.           27         GND         Power ground.           28~31         B0~B3         Blue Data Input.           32         GND         Power ground.           33~36         B4~B7         Blue Data Input.           37         GND         Power ground.           38         HS         Horizontal sync input. Negative polarity.           39         VS         Vertical sync input. Negative polarity.           40         GND         Power ground.           41         DE         Data Enable signal.           42         GND         Power ground.           43         DCLK         Clock input.           44	2, 3	VCC	Supply Voltage
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40       GND       Power ground.         41       DE       Data Enable signal.         42       GND       Power ground.         43       DCLK       Clock input.         44       GND       Power ground.         45~47       NC.       Not connect.         48       DISP       STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z         49       RESET       Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	38	HS	Horizontal sync input. Negative polarity.
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42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	40	GND	Power ground.
43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	41	DE	Data Enable signal.
44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	42	GND	Power ground.
45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	43	DCLK	Clock input.
48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	44	GND	Power ground.
48       DISP       DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z         49       RESET       Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	45~47	NC.	Not connect.
49       RESET       Suggest to connecting with an RC reset circuit for stability.         Normally pull high.(R=10KΩ, C=1µF)	48	DISP	DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN
50 GND Power ground.	49	RESET	Suggest to connecting with an RC reset circuit for stability.
	50	GND	Power ground.



RIP			
PIN NO.	PIN NAME		
1	X1	RTP Left	
2	Y1	RTP Down	
3	X2	RTP Right	
4	Y2	RTP Up	

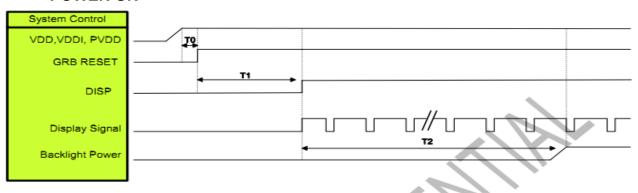
## 5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Analog Supply Voltage	VCC	-0.3	4.0	V
Input Voltage	Vin	GND-0.3	5.0	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	-	90	%RH

## 6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Analog Supply Voltage	VCC	2.5	3.3	3.6	V	-
Input High Voltage	V <sub>IH</sub>	0.7VCC	-	-	V	-
Input Low Voltage	V <sub>IL</sub>	0	-	0.3VCC	V	-
Output High Voltage	V <sub>OH</sub>	0.9	-	-	V	-
Output Low Voltage	V <sub>OL</sub>	0	-	0.1	V	-
Logic Input Current	IIL/IIH	-1	-	1	uA	-

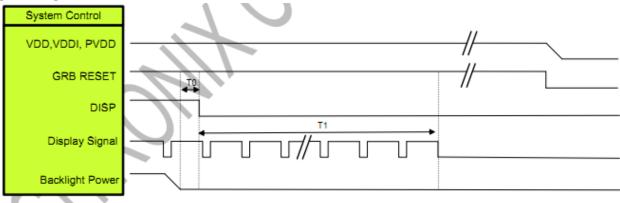
#### 7. Timing Characteristics 7.1 POWER ON/OFF SEQUENCE POWER ON



Symbol	Description	Min. Time	Unit
то	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

#### **POWER OFF**



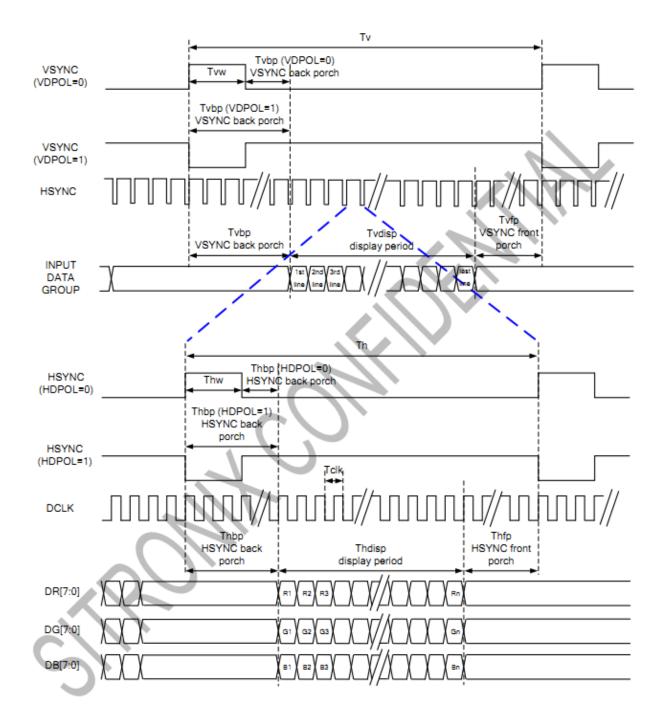
Symbol	Description	Min. Time	Unit
TO	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]



## 7.2 RGB Interface

## SYNC Mode

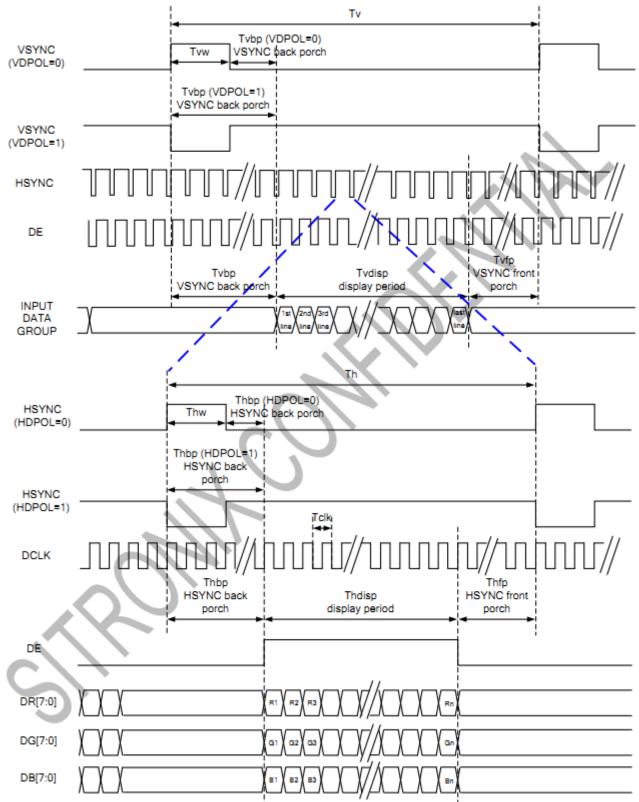




Version: A

2019-05-17

SYNC-DE Mode

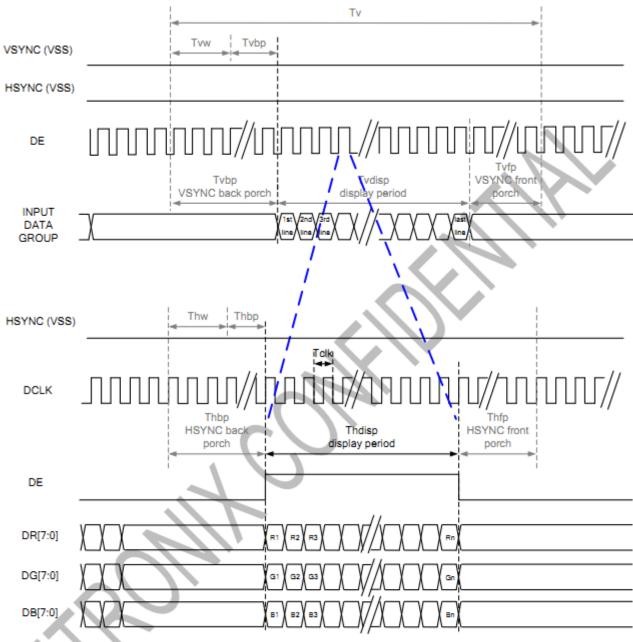




Version: A

2019-05-17

**DE Mode** 

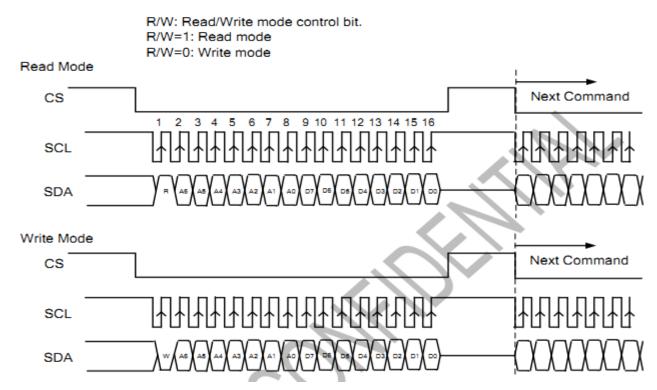


RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input



2019-05-17

## 7.3 3-wire SPI Timing Characteristics



### 7.4 Parallel 24-bit RGB Input Timing Table

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

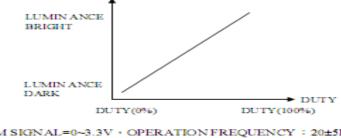
Parallel 24-bit RGB Input Timing Table								
	Item	Symbol	Min.	Тур.	Max.	Unit	Note	
DCLK	Frequency	Fclk	5	6	8	MHz		
DC	LK Period	Tclk	125	167	200	ns		
	Period Time	Th	325	371	438	DCLK		
	Display Period	Thdisp		320		DCLK		
HSYNC	Back Porch	Thbp	3	43	43	DCLK	SYNC mode back porch control by H_BLANKING[7:0] setting Thbp= H_BLANKING[7:0]	
	Front Porch	Thfp	2	8	75	DCLK		
	Pulse Width	Thw	2	4	43	DCLK		
	Period Time	Tv	244	260	289	HSYNC		
	Display Period	Tvdisp		240		HSYNC		
VSYNC	Back Porch	Tvbp	2	12	12	HSYNC	SYNC mode back porch control by V_BLANKING[7:0] setting Tvbp= V_BLANKING[7:0]	
	Front Porch	Tvfp	2	8	37	HSYNC		
	Pulse Width	Tvw	2	4	12	HSYNC		

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

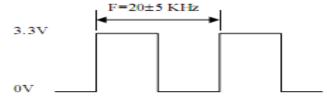


## 8. Backlight Characteristic

Item		Symbol	MIN	ΤΥΡ	MAX	UNIT	NOTE
Backlight Power		LED_VCC	5	12	18	V	Ta = 25°C
Backlight Power		ILED_VCC	-	(0.5)	(0.7)	A	LED_VCC=5V
EN Signal Volta	VIH		1.2			V	
ge	VIL	BL_CON	GND		0.4	V	
Lifetime			20000	-	-	Hr	
Color		White					
Average Brightness		-	250	300	-	Cd/cm2	
Luminance unifo	-		80	-	%		
		•	•		•	•	•



#### NOTE (2): PWM SIGNAL=0~3.3V · OPERATION FREQUENCY : 20±5KHz



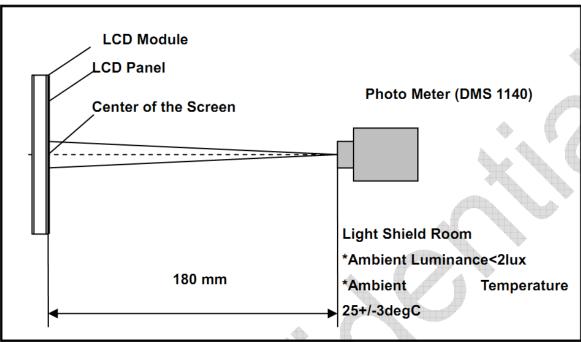


## 9. Optical Characteristics

(Note1 , Note2) (Using Normal Polarizer + Backlight, reference only)

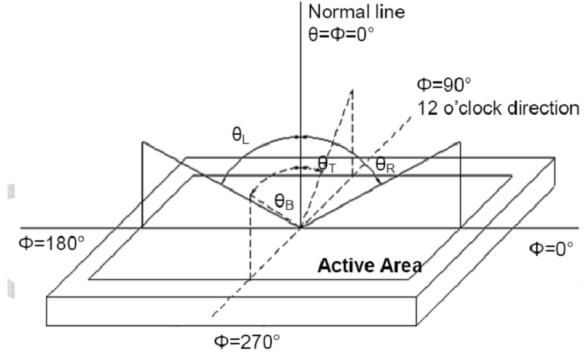
(Note 1 / Notez) (Using Norman Unanzer + Dackigni, reference Uniy)								
ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Contrast Ratio		CR	$\theta = \phi = 0^{\circ}$	300	400			Note1,Note3
Response	Time	Tr+ Tf	$\theta = \phi = 0^{\circ}$		50	70	ms	Note1,Note4
	Upper	θ			80			
Viewing	Down	U			80			Note 5
Angle	Right	<i>(</i> )	CR≧10		80			NOLE 5
	Left	φ			80			
	\//bita	Х	$\theta = \phi = 0^{\circ}$		TBD			
	White	У			TBD			
	Dod	Х	0 – (a – 0°		TBD			
Color Filter	Red	У	$\theta = \phi = 0^{\circ}$	Тур.	TBD	Тур.		Noto 6
Chromaticity	Croop	Х	0 – (a – 0°	-0.05	TBD	+0.05		Note 6
	Green	У	$\theta = \phi = 0^{\circ}$		TBD			
	Blue	Dhua X	$0 - \alpha = 0^{\circ}$		TBD			
	Diue	У	$\theta = \phi = 0^{\circ}$		TBD			

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.





#### Note (2) Definition of Viewing Angle

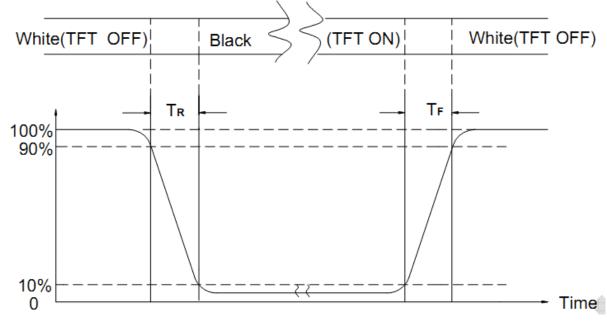


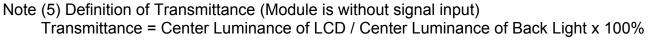
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time





Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



## 10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
	High Temperature Storage	80°C±2°C×200Hours	
	Low Temperature Storage	-30°C±2°C×200Hours	
	High Temperature Operating	70°C±2°C×120Hours	Inspection after 2~4hours
	Low Temperature Operating	-20°C±2°C×120Hours	storage at room temperature, the samples should be free from
	Temperature Cycle(Storage)	-20°C $\longleftrightarrow$ 25°C $\longleftrightarrow$ 70°C (30min) (5min) (30min) 1cycle Total 10cycle	defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments.
	Damp Proof Test (Storage)	50°C±5°C×90%RH×120Hours	5, Glass crack. 6, Current IDD is twice higher than initial value.
	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5M X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	<ul><li>7, The surface shall be</li><li>free from damage.</li><li>8, The electric</li><li>characteristic</li><li>requirements shall be</li></ul>
	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	satisfied.
	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

**REMARK**:

1, The Test samples should be applied to only one test item.

2, Sample side for each test item is 5~10pcs.

3,For Damp Proof Test, Pure water(Resistance >  $10M\Omega$ )should be used.

4, In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.

5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.

6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



## 11. Inspection Standard

#### 11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

#### **11.1.1. INSPECTIONTOOLS AND INSTRUMENTS**

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

#### 11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

#### **11.1.3. INCOMING INSPECTION**

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

#### (B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E ) ,LEVEL:

, ·
AQL(%)
0.4 %
0.65 %
1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

#### (C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

#### **11.1.4. WARRANTY POLICY**

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

#### **11.2. CHECKING CONDITION**

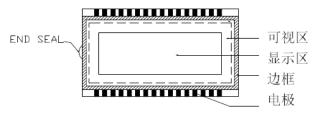
- **11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- 11.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE
- **11.2.3.**Ambient Illumination:

0~30 Lux for functional inspection

500 ~ 1200 Lux for external appearance inspection.

11.2.4. TEST AREA:

**11.2.5.** Inspection should be carried out with rope electrostatic ring and static finger cover (both hands except small fingers must be worn)





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**11.2.6.** The inspector may make a visual inspection or a comparative examination with a film ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.

**11.2.7.** Functional testing uses electrical testing fixtures or test fixtures required by customers.

**11.2.8.** the ion fan should be used when testing.

## 11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

11.3.2 Poor definitionPixel:A combination of three sub-pixels(Red + Green + Blue).

## Dot:

Any of the sub-pixels (Red or Green or Blue).

#### Bright and dark dots:

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

#### Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images. **Neighborhood**:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).



## 11.3. INSPECTION PLAN :

CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREA REJECTED	Minor
	6. BLEMISH V BLACK SPOT V WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
APPEARANCE	7. BLEMISH • BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS ( CONTRAST: VOP : CHROMATICITY ETC )	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA )	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT, LINE, CHARACTER REJECTED	Critical
	12.SHORT CIRCUIT- WRONG PATTERN DISPLAY	NO DISPLAY VRONG PATTERN DISPLAY CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL	Minor



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NO.	CLASS	ITEM	JUDGEMENT				
			(A) ROUND TYPE: unit : mm.	$\neg \neg$			
			DIAMETER (mm.) ACCEPTABLE Q'TY				
			Φ ≤ 0.15 Distance≥1mm				
			$0.15 < \Phi \leq 0.4$ 3 (Distance>15mm)				
		BLACK AND WHITE SPOT FOREIGN MATERIEL	0.4 < D 0				
11 4 1	MINOR		NOTE: $\Phi = (\text{LENGTH} + \text{WIDTH})/2$				
	MINOR	BLEMISH	(B) LINEAR TYPE: unit : m	m.			
		SCRATCH	LENGTH WIDTH ACCEPTABLE Q'TY	(			
			W ≦0.03 Distance≥1mr	m			
			$L \le 4.0$ 0.03 < W $\le 0.05$ 3 (Distance>15r				
			0.05 < W FOLLOW ROUND T	YPE			
			unit : mm.				
			DIAMETER ACCEPTABLE Q'TY ⊕ ≤ 0.2 Distance≥1mm				
11 4 2	MINOR	BUBBLE IN POLARIZER DENT ON POLARIZER	Φ ≤ 0.2 Distance≥1mm 0.2 < Φ ≤ 0.3 3 (Distance>15mm)				
11.4.2	MINOR		$0.2 < \Phi \leq 0.3$ S(Distance>rbmm) $0.3 < \Phi = 0$				
			0.3 \ \ \				
				1			
			Items ACC. Q'TY				
		Dot Defect	Bright dot N≦2 (Distance≥15mm)				
			Dark dot N≤3 (Distance≥15mm)				
			Pixel Define : Pixel				
			R G B				
11.4.3	MINOR		🗲 Dot 🗲 Dot 🗲 Dot 🗲				
			Note 1: The definition of dot: The size of a defective dot over	,			
			1/2 of whole dot is regarded as one defective dot.				
			Definittion:<1/2dot and visible by 6% ND filter N ≦	I			
			Note 2: Bright dot: Dots appear bright and unchanged in size	I			
			in which LCD panel is displaying under black pattern	I			
			Note 3: Dark dot: Dots appear dark and unchanged in size in	I			
			which LCD panel is displaying under pure red, green	J			
			,blue pattern.				
		Mura	Not visible thriugh 5% ND filter in 50% gray or judge				
11.4.4	MINOR		by limit sample if necessary				



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NO.	CLASS	ITEM	JUDGEMEN	T
11.4.4	MINOR	LCD GLASS CHIPPING	S S	Y > S Reject
11.4.5	MINOR	LCD GLASS CHIPPING	S X S	X or Y > S Reject
11.4.6	MAJOR	LCD GLASS GLASS CRACK	Y Y	Y > (1/2) T Reject
11.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	$\Lambda_{\mp \vdash a \rightarrow}^{\perp} \xrightarrow{L} _{\pm} B$	<ol> <li>a&gt; L/3, A&gt;1.5mm. Reject</li> <li>B: ACCORDING TO DIMENSION</li> </ol>
11.4.8	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL AREA )	T	$\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject
11.4.9	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL SURFACE )	TZX	Y > (1/3) T Reject
11.4.10	MINOR	LCD GLASS CHIPPING	X -> -y Z	Y > T Reject



## **12. Handling Precautions**

## 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to POWER or GND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

## 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.



#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

## **13. Precaution for Use**

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 14. Packing Method

TBD