Analog Power AM100N04-02D

N-Channel 40-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- Low thermal impedance
- · Fast switching speed

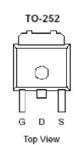
Typical Applications:

- White LED boost converters
- · Automotive Systems
- Industrial DC/DC Conversion Circuits

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)	
40	2.3 @ V _{GS} = 10V	100	
	$4.5 @ V_{GS} = 4.5V$	65	







ABSOLUTE MAXIMUM RATINGS (T _A = 25 ℃ UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			40	V		
Gate-Source Voltage		V_{GS}	±20			
Continuous Drain Current a	T _A =25 ℃	I _D	100	۸		
Pulsed Drain Current ^b		I _{DM}	300 A			
Continuous Source Current (Diode Conduction) a			56	Α		
Power Dissipation ^a	T _A =25 ℃	P_D	50	W		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	$_{\mathbb{C}}$		

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	40	°C/W		
Maximum Junction-to-Case	$R_{ heta JC}$	3	C/VV		

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	lana	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	I _{DSS}	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			25	uA	
On-State Drain Current	$I_{D(on)}$	$V_{DS} = 5 V, V_{GS} = 10 V$	150			Α	
Drain-Source On-Resistance	r	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$			2.3	mΩ	
	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 16 \text{ A}$			4.5		
Forward Transconductance	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		30		S	
Diode Forward Voltage	V_{SD}	$I_{S} = 28 \text{ A}, V_{GS} = 0 \text{ V}$		0.83		V	
		Dynamic					
Total Gate Charge	Q_g	$V_{DS} = 20 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 20 \text{ A}$		60		nC	
Gate-Source Charge	Q_gs			18			
Gate-Drain Charge	Q_{gd}			19			
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 20 \text{ V}, R_L = 1 \Omega,$ $I_D = 20 \text{ A},$ $V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		21		ns	
Rise Time	t _r			22			
Turn-Off Delay Time	$t_{d(off)}$			160			
Fall Time	t_f			50			
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		5000		pF	
Output Capacitance	C _{oss}			600			
Reverse Transfer Capacitance	C_{rss}			400			

Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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