## **Analog Power**

#### AM1360NE

# N-Channel 60V (D-S) MOSFET

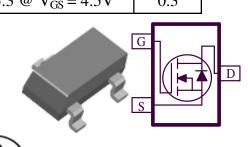
These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SC70-3 saves board space
- Fast switching speed
- High performance trench technology

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$\mathbf{r}_{\mathrm{DS(on)}}\left( \Omega  ight)$	I <sub>D</sub> (A)		
60	$3 @ V_{GS} = 10 V$	0.3		
	$3.3 @ V_{GS} = 4.5V$	0.3		

ESD Protected 2000V





ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C UNLESS OTHERWISE NOTED)							
Parameter		Symbol	Maximum	Units			
Drain-Source Voltage			60	V			
Gate-Source Voltage		V <sub>GS</sub>	±20	v			
Continuous Drain Current <sup>a</sup>	T <sub>A</sub> =25°C	I.,	1.7				
	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	тD	1.4	А			
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	±20				
Continuous Source Current (Diode Conduction) <sup>a</sup>		Is	1.6	А			
	T <sub>A</sub> =25°C	D_	0.34	W			
Power Dissipation <sup>a</sup>	$\begin{array}{c} T_{A}=25^{\circ}C \\ T_{A}=70^{\circ}C \end{array}$		0.22	٧V			
Operating Junction and Storage Temperature Range		TJ, Tstg	-55 to 150	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
	t <= 5 sec	D	100	<sup>0</sup> C AV		
Maximum Junction-to-Ambient <sup>a</sup>	Steady-State	$R_{THJA}$	166	C/w		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

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SPECIFICATIONS ( $T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)							
Donometor	Chl		Limits			TI	
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static	-				-	-	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±10	μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 48 V, V_{GS} = 0 V$	$V_{GS} = 0 V$		1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	μA	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	0.3			Α	
Drain-Source On-Resistance <sup>A</sup>	<b>f</b> par )	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.1 \text{ A}$			3	Ω	
Dram-Source On-Resistance	<sup>1</sup> DS(on)	$r_{DS(on)}$ $V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.1 \text{ A}$			3.3	32	
Forward Tranconductance <sup>A</sup>	g <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.1 \text{ A}$		11.3		S	
Diode Forward Voltage	V <sub>SD</sub>	$I_{S} = 0.1 \text{ A}, V_{GS} = 0 \text{ V}$		0.75		V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			4			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 10 V, V_{GS} = 5 V, I_D = 0.1 A$		1		nC	
Gate-Drain Charge	Q <sub>gd</sub>			1			
Turn-On Delay Time	t <sub>d(on)</sub>			3			
Rise Time	t <sub>r</sub>	$V_{DD} = 10 \text{ V},  R_L = 15 \Omega,  I_D = 0.1 \text{ A},$		4			
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GEN} = 4.5 V$		20		ns	
Fall-Time	t <sub>f</sub>			6		1	

Notes

a. Pulse test:  $PW \le 300$  us duty cycle  $\le 2\%$ .

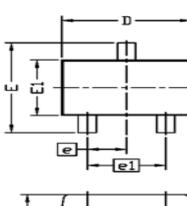
b. Guaranteed by design, not subject to production testing.

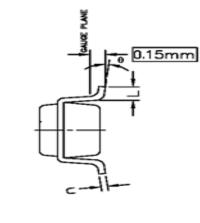
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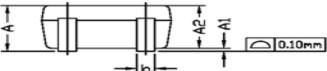
Publication Order Number: DS-AM1360NE\_A

# Package Information

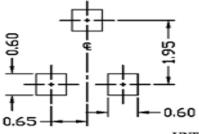
SC70 PACKAGE OUTLINE







RECOMMENDED LAND PATTERN



TYMBOLS DIMENSIONS IN MILLIMETERS		DIMENSIONS IN INCIDE					
FIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
Α			1.10			0.043	
A1	0.00		0.10	0.00		0.004	
A2	0.7	0.9	1.00	0.028	0.035	0.039	
b	0.15		0.30	0.006		0.012	
¢	0.08		0.22	0.003		0.009	
D	1.85	2,10	2.15	0.073	0.083	0.085	
E	1.80	2.30	2,40	0.071	0.091	0.094	
e		0.65 BSC			0.026 BSC		
el	1.30 BSC				0.051 BSC		
E1	1.1	1.30	1.4	0.043	0.051	0.055	
L	0.26	0.36	0.46	0.010	0.014	0.018	
θ	0°	4°	8°	0°	4°	8°	

UNIT: mm

NOTE

1. ALL DIMENSIONS ARE IN MILLMETERS.

2. DIMENSIONS ARE INCLUSIVE OF PLATING.

3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.

MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 3 MILS EACH.

4. DIE IS FACING UP FOR MOLD AND FACING DOWN FOR TRIM/FORM.

ie:REVERSE TRIM/FORM.

5. DIMENSION L IS MEASURED IN GAUGE PLANE,

6. CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.