

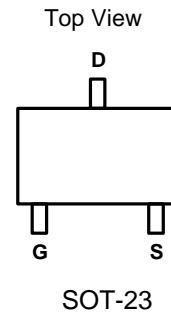

**axelite** 亞瑟萊特科技股份有限公司 AXElite Technology Co.,Ltd

## N-Channel Enhancement Mode MOSFET

### Features

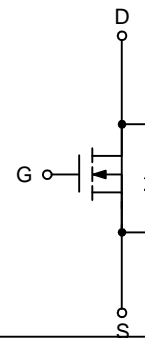
- 30V/4A  
 $R_{DS(ON)} = 55m\Omega(Typ.) @ V_{GS} = 10V$   
 $R_{DS(ON)} = 70m\Omega(Typ.) @ V_{GS} = 4.5V$   
 $R_{DS(ON)} = 110m\Omega(Typ.) @ V_{GS} = 2.8V$
- Super High Dense Cell Design
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)


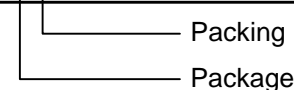
### Pin Description



### Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems



N-Channel MOSFET	
<b>Ordering and Marking Information</b>	
AM3402	
	Package R : SOT23-3L Packing Blank : Tube A : Taping
	

lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. AXElite defines "Green" material and total of Br and Cl does not exceed 1500ppm by weight).

AM3402 : B2XXX XXX - Date Code to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous

Note: AXElite

tion finish; which are fully compliant with RoHS. AXElite

of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. AXElite defines "Green"

material and total of Br and Cl does not exceed 1500ppm by weight).

**Absolute Maximum Ratings** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Rating	Unit
$V_{DSS}$	Drain-Source Voltage	30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 12$	V
$I_D$	Continuous Drain Current	$T_A=25^\circ\text{C}$	A
	Current <sup>A</sup>	$T_A=70^\circ\text{C}$	
$I_{DM}$	Pulsed Drain Current <sup>B</sup>	15	
$P_D$	Power Dissipation <sup>A</sup>	$T_A=25^\circ\text{C}$	W
		$T_A=70^\circ\text{C}$	
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics					
Parameter	Symbol	Typ	Max	Unit	
Maximum Junction-to-Ambient <sup>A</sup>	$R_{\theta JA}$	$t \leq 10\text{s}$	70	90	$^\circ\text{C/W}$
Maximum Junction-to-Ambient <sup>A</sup>		Steady-State	100	125	$^\circ\text{C/W}$
Maximum Junction-to-Lead <sup>C</sup>	$R_{\theta JL}$	Steady-State	63	80	$^\circ\text{C/W}$

**Electrical Characteristics** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit	
<b>STATIC PARAMETERS</b>							
$BV_{DSS}$	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	30			V	
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24\text{V}, V_{GS}=0\text{V}$	$T_J=55^\circ\text{C}$		1	$\mu\text{A}$	
					5		
$I_{GSS}$	Gate-Body leakage current	$V_{DS}=0\text{V}, V_{GS}=\pm 12\text{V}$			100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.6	1	1.4	V	
$I_{D(ON)}$	On state drain current	$V_{GS}=4.5\text{V}, V_{DS}=5\text{V}$	10			A	
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=4\text{A}$	$T_J=125^\circ\text{C}$		45	55	m $\Omega$
					66	80	
		$V_{GS}=4.5\text{V}, I_D=3\text{A}$		55	70	m $\Omega$	
		$V_{GS}=2.5\text{V}, I_D=2\text{A}$		83	110	m $\Omega$	
$g_{FS}$	Forward Transconductance	$V_{DS}=5\text{V}, I_D=4\text{A}$		8		S	
$V_{SD}$	Diode Forward Voltage	$I_S=1\text{A}, V_{GS}=0\text{V}$		0.8	1	V	
$I_S$	Maximum Body-Diode Continuous Current				2.5	A	

## Electrical Characteristics (Cont.)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>DYNAMIC PARAMETERS</b>						
$C_{iss}$	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$		390		pF
$C_{oss}$	Output Capacitance			54.5		pF
$C_{rss}$	Reverse Transfer Capacitance			41		pF
$R_g$	Gate resistanc	$V_{GS}=0V, V_{DS}=0V, f=1MHz$		3		$\Omega$
<b>SWITCHING PARAMETERS</b>						
$Q_g$	Total Gate Charge	$V_{GS}=4.5V, V_{DS}=15V, I_D=4A$		4.34		nC
$Q_{gs}$	Gate Source Charge			0.6		nC
$Q_{gd}$	Gate Drain Charge			1.38		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=10V, V_{DS}=15V, R_L=3.75\Omega, R_{GEN}=6\Omega$		3.3		ns
$t_r$	Turn-On Rise Time			1		ns
$t_{D(off)}$	Turn-Off DelayTime			21.7		ns
$t_f$	Turn-Off Fall Time			2.1		ns
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F=4A, di/dt=100A/\mu s$		12		ns
$Q_{rr}$	Body Diode Reverse Recovery Charge	$I_F=4A, di/dt=100A/\mu s$		6.3		nC

A: The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The value in any given application depends on the user's specific board design. The current rating is based on the  $\leq 10s$  thermal resistance rating.

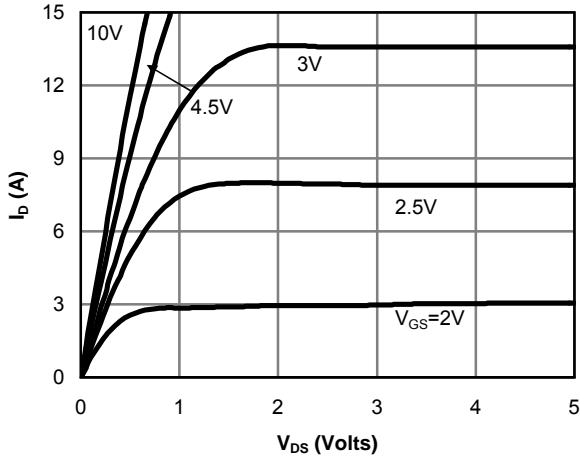
B: Repetitive rating, pulse width limited by junction temperature.

C. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to lead  $R_{\theta JL}$  and lead to ambient.

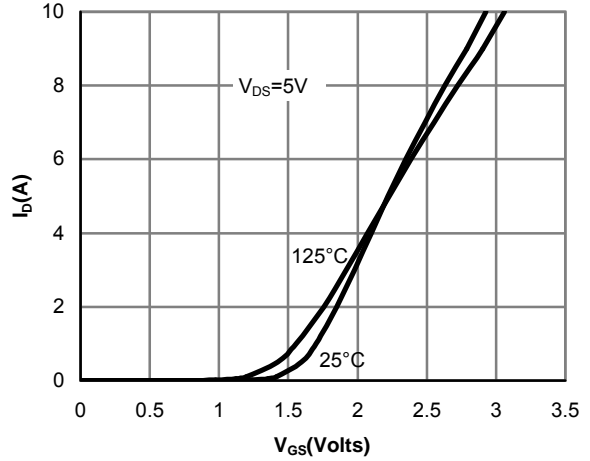
D. The static characteristics in Figures 1 to 6,12,14 are obtained using  $<300\mu s$  pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ C$ . The SOA curve provides a single pulse rating.

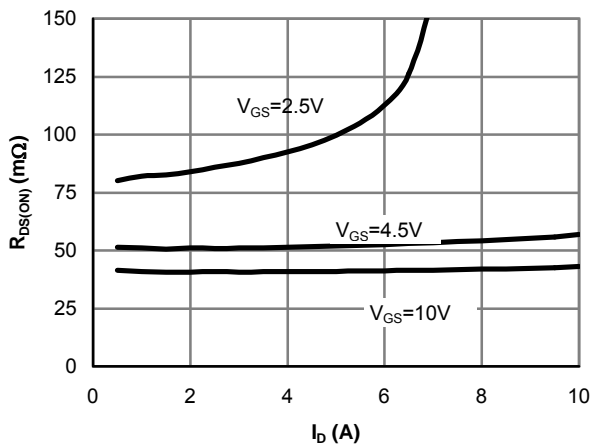
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



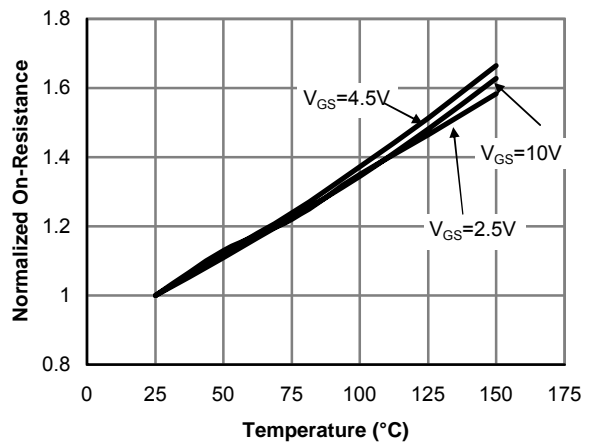
**Fig 1: On-Region Characteristics**



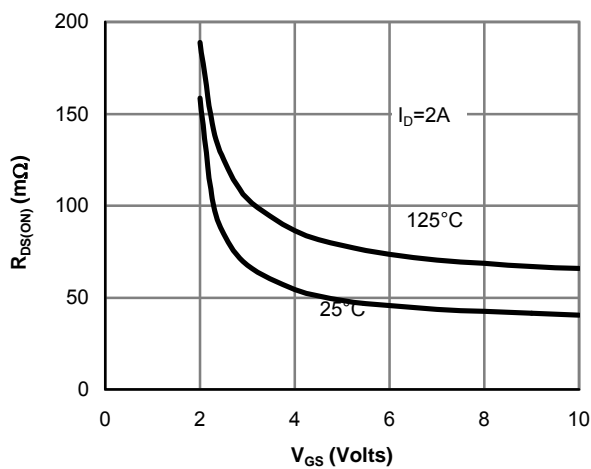
**Figure 2: Transfer Characteristics**



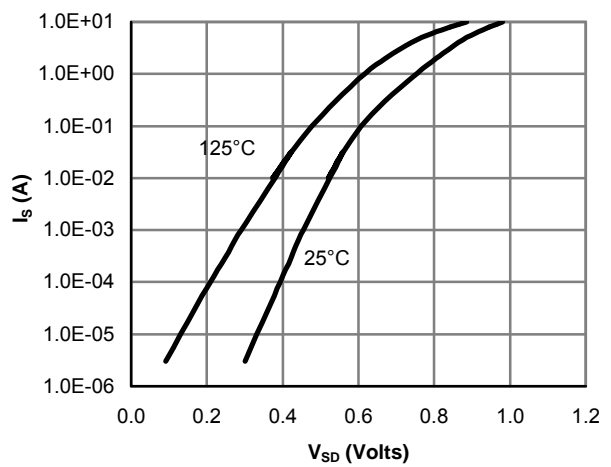
**Figure 3: On-Resistance vs. Drain Current and Gate Voltage**



**Figure 4: On-Resistance vs. Junction Temperature**



**Figure 5: On-Resistance vs. Gate-Source Voltage**



**Figure 6: Body-Diode Characteristics**

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

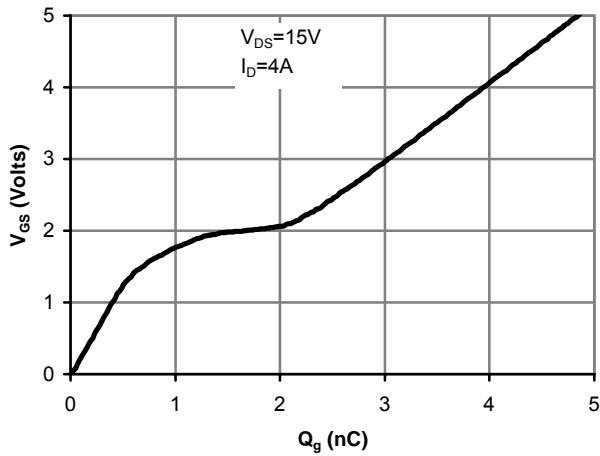


Figure 7: Gate-Charge Characteristics

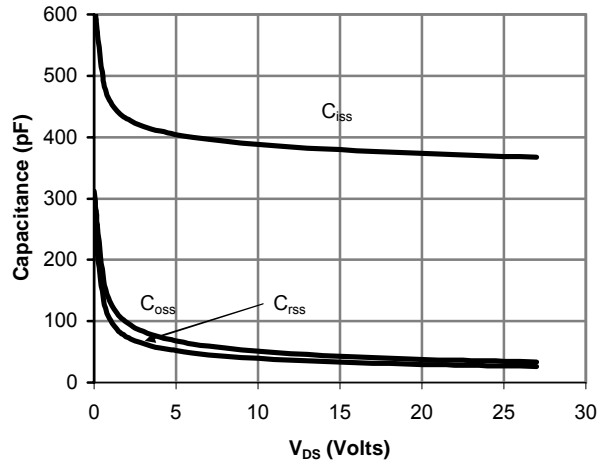


Figure 8: Capacitance Characteristics

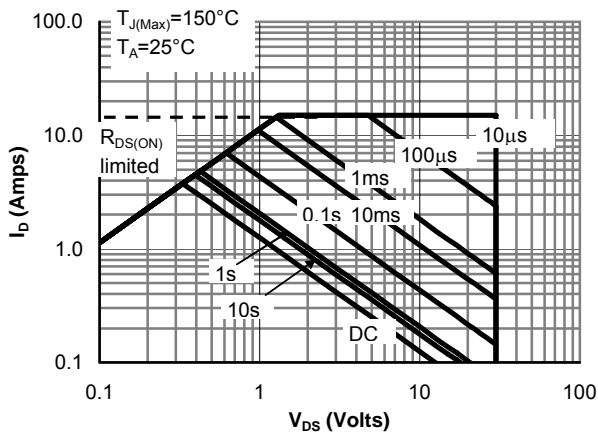


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

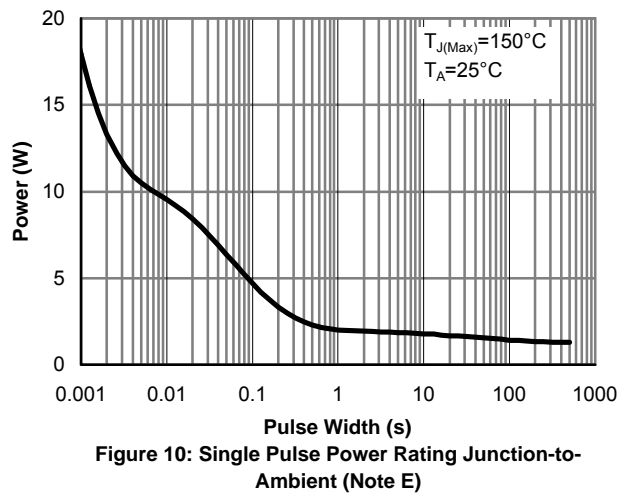


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

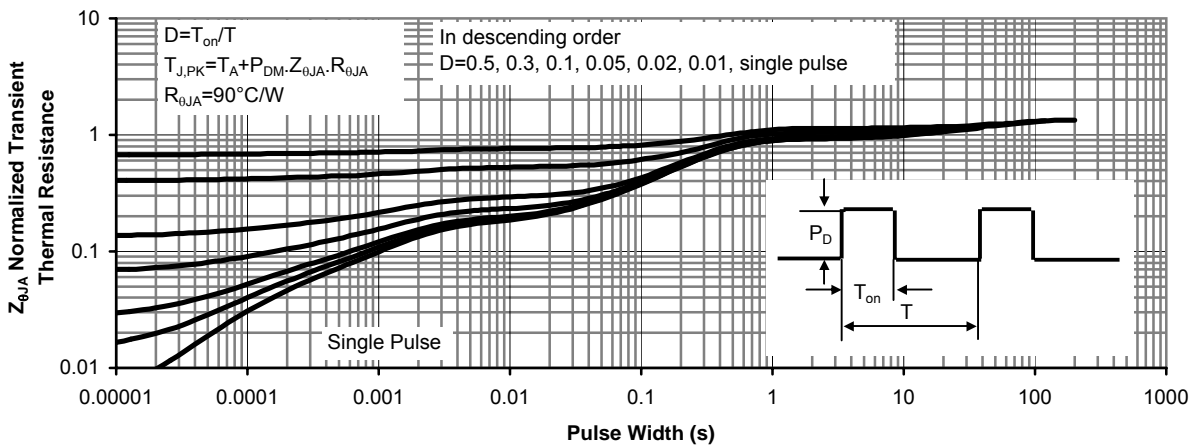


Figure 11: Normalized Maximum Transient Thermal Impedance