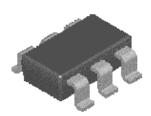
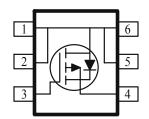
P-Channel 20-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)} m(\Omega)$	$I_{D}(A)$		
	$90 @ V_{GS} = -4.5V$	-2.9		
-20	$130 @ V_{GS} = -2.5V$	-2.5		
	$150 @ V_{GS} = -1.8V$	-2.3		

- $\hbox{-} \qquad \text{Low $r_{DS(on)}$ provides higher efficiency and} \\ \text{extends battery life}$
- Low thermal impedance copper leadframe TSOP-6 saves board space
- Fast switching speed
- High performance trench technology





ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C UNLESS OTHERWISE NOTED)						
Parameter			Maximum	Units		
Drain-Source Voltage			-20	V		
Gate-Source Voltage		V_{GS}	±12	V		
Continuous Drain Current ^a	$T_A=25^{\circ}C$	Τ	-2.9			
Continuous Drain Current	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	П	-2.4	A		
Pulsed Drain Current ^b		I_{DM}	±16			
Continuous Source Current (Diode Conduction) ^a		I_S	-1.0	A		
D Dii4ia	$T_A=25^{\circ}C$	D	2.0	W		
Power Dissipation ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	1 D	1.3	٧٧		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
M . I	t <= 5 and	D	62.5	°C/W		
Maximum Junction-to-Ambient ^a	$t \le 5 \text{ sec}$	$ m R_{ m heta JA}$	110	°C/W		

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Donomotov	Complete Treet Com Pt		Limits			T., .,
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static						
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = -250 \text{ uA}$	-0.7			
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$			-1	uA
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	C -	-5	u A	
On-State Drain Current ^A	$I_{D(on)}$	$V_{DS} = -4.5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-10			Α
		$V_{GS} = -4.5 \text{ V}, I_D = -3.3 \text{ A}$			90	
Drain-Source On-Resistance ^A	$r_{DS(on)}$	$V_{GS} = -2.5 \text{ V}, I_D = -2.9 \text{ A}$		130	130	$m\Omega$
		$V_{GS} = -1.8 \text{ V}, I_D = -2.3 \text{ A}$			150	
Forward Tranconductance ^A	g_{fs}	$V_{DS} = -10 \text{ V}, I_D = -4.9 \text{ A}$		8.0		S
Diode Forward Voltage	V_{SD}	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		-0.8		V
Dynamic ^b						
Total Gate Charge	Q_{g}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$		8		
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$ $I_{D} = -4.9 \text{ A}$		1.8		nC
Gate-Drain Charge	Q_{gd}	1 _D = -4.9 A		1.9		
Turn-On Delay Time	t _{d(on)}			22		
Rise Time	$t_{\rm r}$	$V_{DD} = -10 \text{ V}, R_L = 6 \Omega, I_D = -1 \text{ A},$		35		nS
Turn-Off Delay Time	$t_{d(off)}$	VGEN = -4.5 V		45		113
Fall-Time	t_{f}			25		

Notes

a. Pulse test: $PW \le 300us duty cycle \le 2\%$.

b. Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics $V_{GS} = -4.5V$ T_A = -55°C -3.0V -**Ib, DRAIN CURRENT (A)**2 1b, DRAIN CURRENT (A) 0 0.5 2.5 - V_{DS} , DRAIN TO SOURCE VOLTAGE (V) -V_{GS}, GATE TO SOURCE VOLTAGE (V) Figure 1. Output Characteristics Figure 2. Transfer Characteristics RDS(ON) NORMALIZED DRAIN-SOURCE ON-RESISTANCE 1200 CAPACITANCE (pF) $V_{GS} = -2.5V$ 900 600 8.0 0 10 -I_D, DIRAIN CURRENT (A) - V_{DS} , DRAIN TO SOURCE VOLTAGE (V) Figure 3. On-Resistance vs. Drain Current Figure 4. Capacitance 1.6 -10 V_{GS} = -4.5V RDS(ON), NORMALIZED DRAIN-SOURCE ON-RESISTANCE Vgs Voltage (V) 0.6 100 150 -25 0 25 50 75 125 12 T_J, JUNCTION TEMPERATURE (°C) Qg, Charge (nC) Figure 5. Gate Charge Figure 6. On-Resistance vs. Junction Temperature

Typical Electrical Characteristics

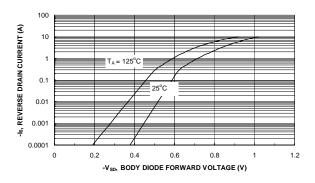


Figure 7. Source-Drain Diode Forward Voltage

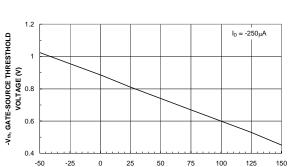


Figure 8. On-Resistance with Gate to Source Voltage

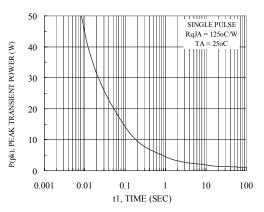


Figure 9. Vth Gate to Source Voltage Vs Temperature

T_A, AMBIENT TEMPERATURE (°C)

Figure 10. Single Pulse Maximum Power Dissipation

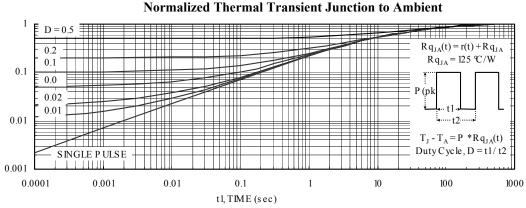
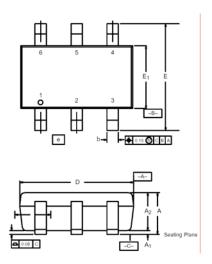
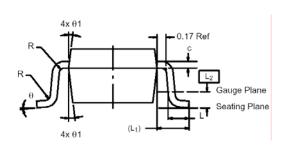


Figure 11. Transient Thermal Response Curve

Package Information

TSOP-6: 6LEAD





	MIL	LIMET	ERS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	_	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.84	_	1.00	0.033	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	1.00 BSC			0.0394 BSC			
L	0.35	_	0.50	0.014	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	_	_	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			