N-Channel 60-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- DC/DC Conversion Circuits
- Motor Drives

PRODUCT SUMMARY				
Vds (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)		
60	23 @ V _{GS} = 10V	7.7		
60	30 @ V _{GS} = 4.5V	6.7		



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)					
Parameter			Limit	Units	
Drain-Source Voltage			60	V	
Gate-Source Voltage	V _{GS}	±20	v		
Continuous Drain Current ^a	T _A =25°C	1	7.7	A	
	T _A =70°C	I _D	6.2		
Pulsed Drain Current ^b	I _{DM}	30			
Continuous Source Current (Diode Conduction) ^a	۱ _s	3.1	А		
Power Discipution ^a	T _A =25°C	P _D	2	w	
Power Dissipation ^a	T _A =70°C	U 'D	1.3	vv	
Operating Junction and Storage Temperature Range			-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	R_{\thetaJA}	62.5	°C/W		
	Steady State	Ν _{θJA}	110			

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

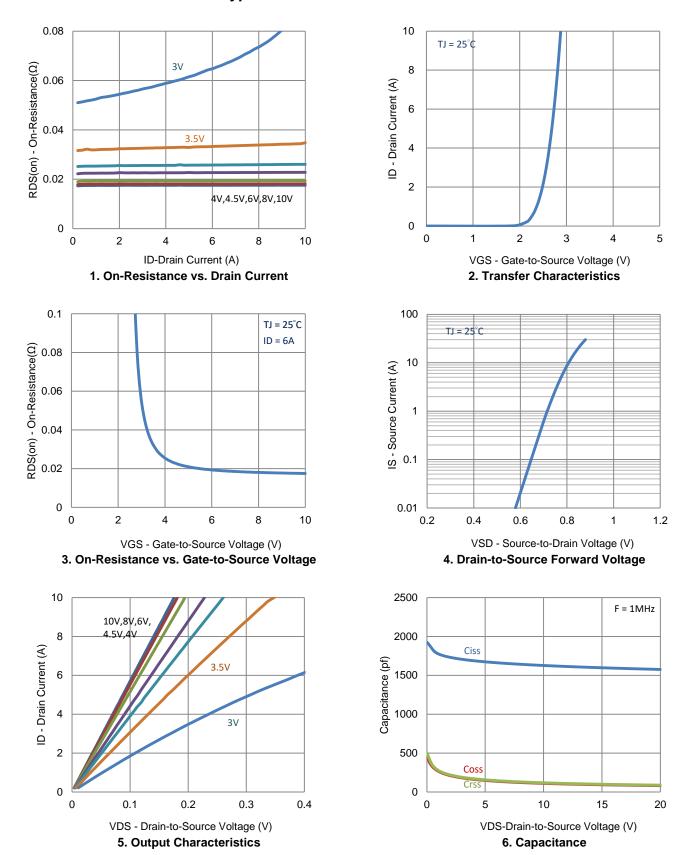
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
	IDSS	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	12			А	
Drain Course On Desistence a	r	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$			23	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5 \text{ A}$			30		
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		27		S	
Diode Forward Voltage ^a	V _{SD}	$I_{S} = 1.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.74		V	
	Dynamic ^b						
Total Gate Charge	Qg	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V},$		12			
Gate-Source Charge	Q_gs	$V_{DS} = 30 V, V_{GS} = 4.5 V,$ $I_{D} = 6 A$		4.5		nC	
Gate-Drain Charge	Q_gd	10 - 0 A		3.5			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 30 \text{ V}, \text{ R}_{1} = 5 \Omega,$		5			
Rise Time	t _r	$V_{DS} = 50 V, R_L - 5 \Omega,$ $I_D = 6 A,$		5		200	
Turn-Off Delay Time	t _{d(off)}	$V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{GEN}} = 6 \Omega$		36		ns	
Fall Time	t _f	V GEN - 10 V, 1\GEN - 0 12		11			
Input Capacitance	C _{iss}			1597			
Output Capacitance	C _{oss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 Mhz		92		pF	
Reverse Transfer Capacitance	C _{rss}			99			

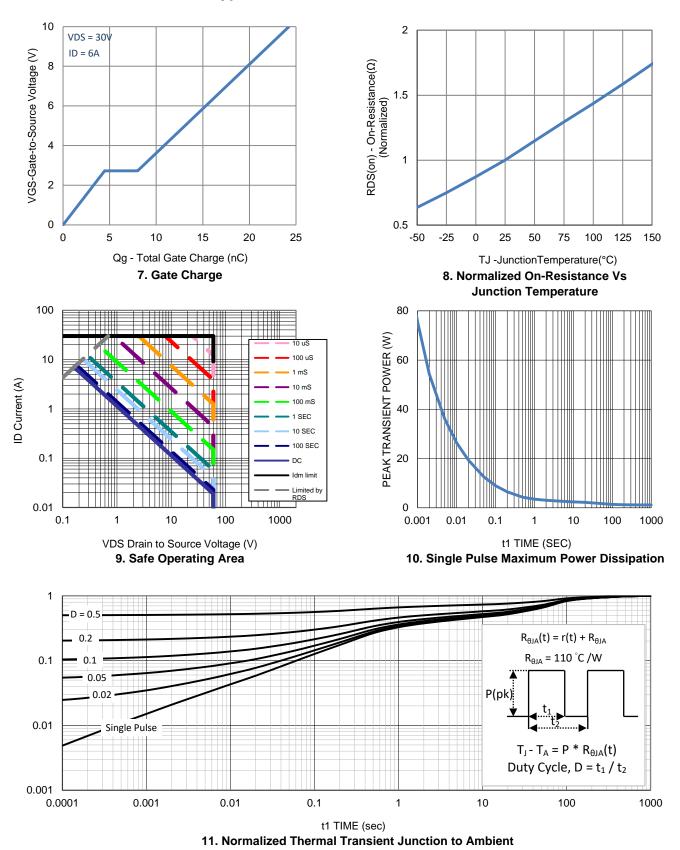
Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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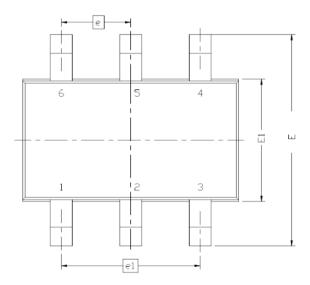


Typical Electrical Characteristics

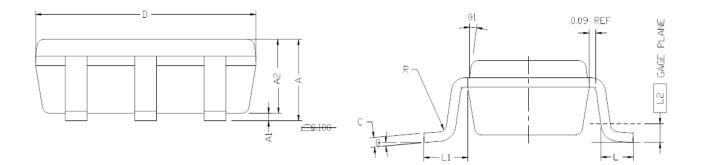


Typical Electrical Characteristics

Package Information



DIM.	MILLIMETERS				
DIM.	MIN	NDM	MAX		
Α	0.935		1.10		
A1	0.01		0.10		
A2	0.70		1.00		
b	0.25	0.32	0.40		
\subset	0.10	0.15	0.20		
D	2.95	3.05	3.10		
Ε	2.70	2.85	2.98		
E1	1.55	1.65	1.70		
e	0.95 BSC				
L	0.30		0.60		
L1	0.60REF				
L2	0.25BSC				
R	0.10				
θ	0?	4?	8?		
θ1	7? NOM				



Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- 5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.