N-Channel 100-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

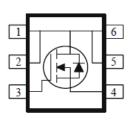
- LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I⊳(A)	
100	92 @ V _{GS} = 10V	3.9	
100	99 @ V _{GS} = 4.5V	3.7	



FREE





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter	Symbol	Limit	Units		
Drain-Source Voltage			100	V	
Gate-Source Voltage	V_{GS}	±20	V		
Continuous Drain Current ^a	T _A =25°C	· I _D	3.9		
Continuous Diain Current	T _A =70°C	טי	3.1	Α	
Pulsed Drain Current ^b	I _{DM}	15			
Continuous Source Current (Diode Conduction) a	I _S	2.8	Α		
Dower Dissipation a	T _A =25°C	P_{D}	2	W	
Power Dissipation ^a	T _A =70°C	' D	1.3] "	
Operating Junction and Storage Temperature Range			-55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter			Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W		
Maximum Junction-to-Ambient	Steady State	IXOJA	110			

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

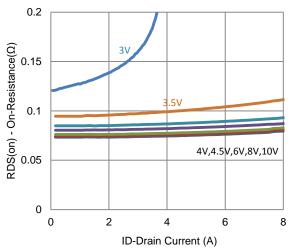
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	6			Α	
Dunin Course On Braintana a	r	$V_{GS} = 10 \text{ V}, I_D = 3.1 \text{ A}$			92	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.5 \text{ A}$			99	11177	
Forward Transconductance a	g_{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 3.1 \text{ A}$		8		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.78		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V},$		10			
Gate-Source Charge	Q_{gs}	$I_{DS} = 30 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 3.1 \text{ A}$		3.6		nC	
Gate-Drain Charge	Q_gd	1B = 3.1 A		3.4]	
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 50 \text{ V}, R_{L} = 16.2 \Omega,$		6			
Rise Time	t _r	$V_{DS} = 50 \text{ V}, K_L - 10.2 \Omega,$ $I_D = 3.1 \text{ A},$		5		nc	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		32		ns	
Fall Time	t _f	V GEN = 10 V, 1 (GEN = 0.22		8			
Input Capacitance	C _{iss}			1578			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		64		pF	
Reverse Transfer Capacitance	C _{rss}]		46			

Notes

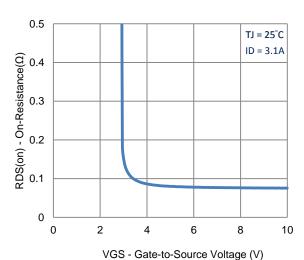
- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

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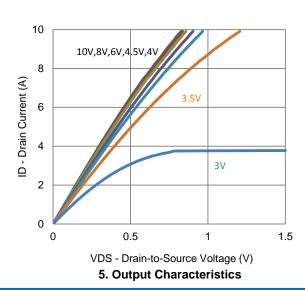
Typical Electrical Characteristics



1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage



10

TJ = 25°C

8

(Y)

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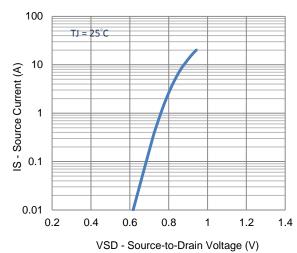
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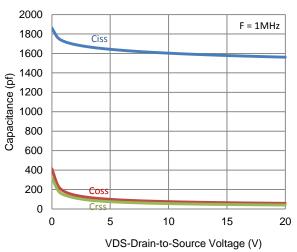
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VGS - Gate-to-Source Voltage (V)

2. Transfer Characteristics

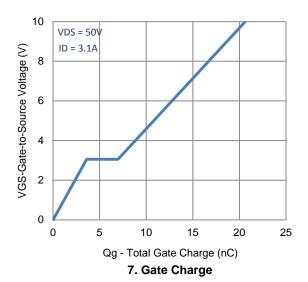


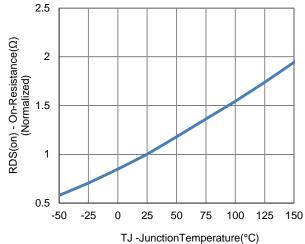
4. Drain-to-Source Forward Voltage

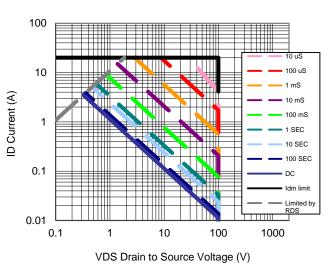


6. Capacitance

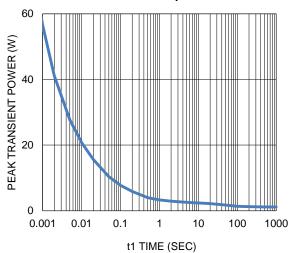
Typical Electrical Characteristics





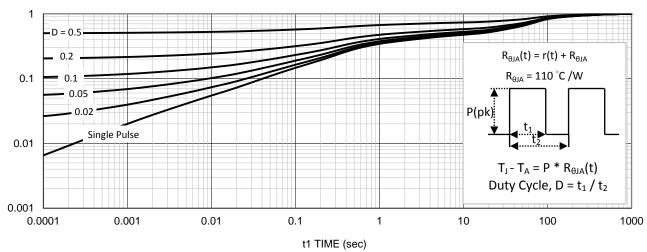






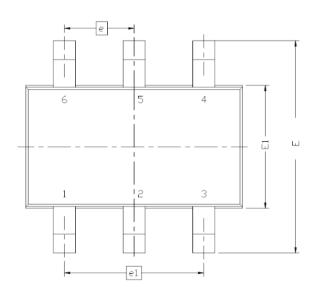
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation

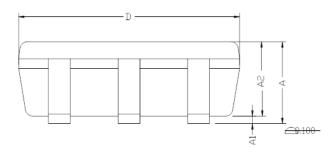


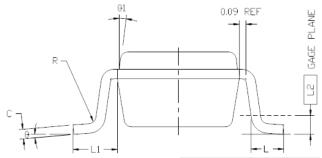
11. Normalized Thermal Transient Junction to Ambient

Package Information



DIM.	MILLIMETERS					
DIM	MIN	NDM	MAX			
Α	0.935		1.10			
A1	0.01		0.10			
A2	0.70		1.00			
b	0.25	0.32	0.40			
C	0.10	0.15	0.20			
D	2.95	3.05	3.10			
Ε	2.70	2.85	2.98			
E1	1.55	1.65	1.70			
6	0.95 BSC					
L	0.30		0.60			
L1	0.60REF					
L2	0.25BSC					
R	0.10					
θ	0?	4?	8?			
θ1	7? N□M					





Note:

- 1. All Dimension Are In mm.
- 2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.