Analog Power AM3930N

Dual N-Channel 30-V (D-S) MOSFET

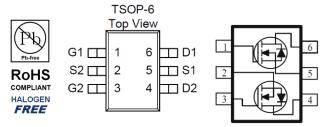
Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

- DC/DC Conversion
- · Power Routing
- Motor Drives

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I _D (A)		
30	58 @ V _{GS} = 10V	3.7		
30	82 @ V _{GS} = 4.5V	3.1		



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Units		
Drain-Source Voltage			30	V		
Gate-Source Voltage	V_{GS}	±20	V			
Continuous Brain Commental	T _A =25°C	ı	3.7	А		
Continuous Drain Current a	T _A =70°C	l _D	2.9			
Pulsed Drain Current ^b	I _{DM}	15				
Continuous Source Current (Diode Conduction) ^a		I _S	1.6	Α		
Device Discipation a	T _A =25°C	P_{D}	1.15	W		
Power Dissipation ^a	T _A =70°C	I P	0.7	VV		
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Maximum	Units		
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	110	°C/W		
Maximum Junction-to-Ambient	Steady State	ГХ⊕ЈА	150	C/VV		

1

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

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Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zara Cata Valta sa Busin Comment		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	5.5			Α	
Dunin Course On Braintain a	r	$V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$			58	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 1.6 \text{ A}$			82		
Forward Transconductance a	g_{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 3 \text{ A}$		11		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 0.8 \text{ A}, V_{GS} = 0 \text{ V}$		0.75		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		4.4			
Gate-Source Charge	Q_{gs}	$I_D = 2 A$		1.1		nC	
Gate-Drain Charge	Q_gd	10 - 2 A		1.8			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 15 \text{ V}, R_{L} = 7.5 \Omega,$		3			
Rise Time	t _r	$V_{DS} = 13 \text{ V}, K_L - 7.3 \Omega,$ $I_D = 2 \text{ A},$		6		no	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		20		ns	
Fall Time	t _f	V GEN = 10 V, 1 (GEN = 0.22		6			
Input Capacitance	C _{iss}			360			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		56		pF	
Reverse Transfer Capacitance	C _{rss}			46			

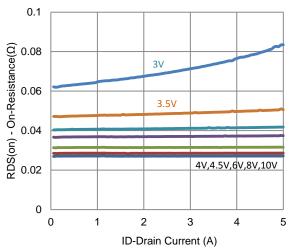
Notes

- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

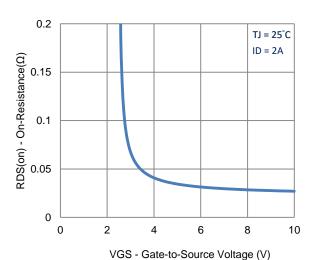
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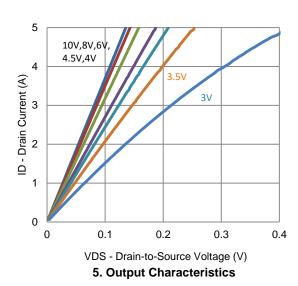
Typical Electrical Characteristics



1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage



TJ = 25°C

4

(V)

TU = 25°C

1

0

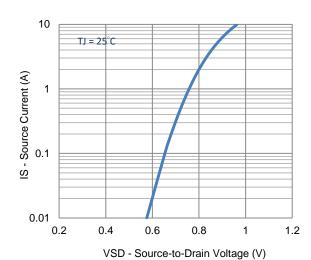
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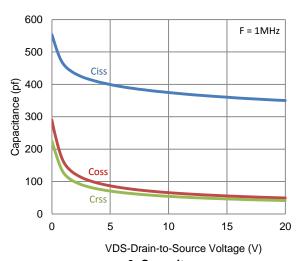
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VGS - Gate-to-Source Voltage (V)

2. Transfer Characteristics



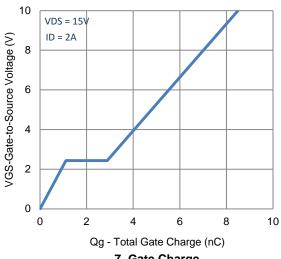
4. Drain-to-Source Forward Voltage

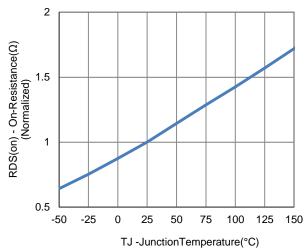


6. Capacitance

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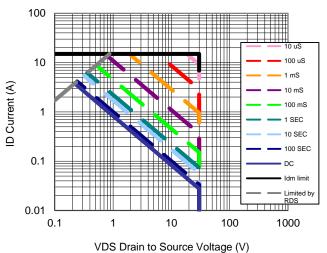
Typical Electrical Characteristics

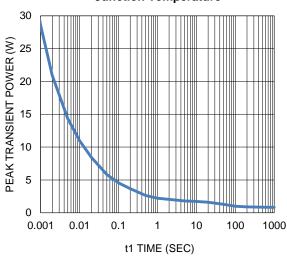




7. Gate Charge

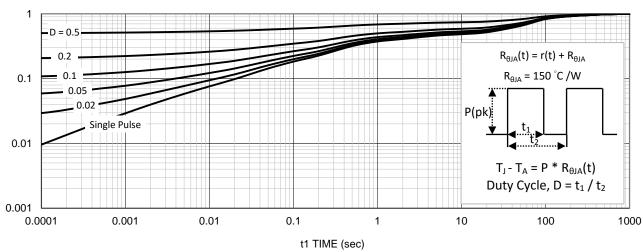






9. Safe Operating Area

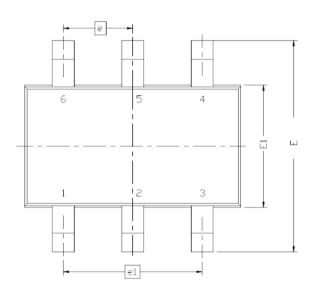
10. Single Pulse Maximum Power Dissipation



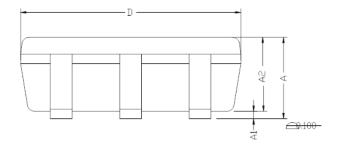
11. Normalized Thermal Transient Junction to Ambient

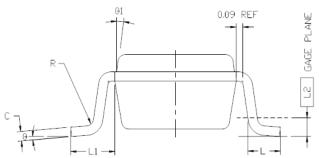
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Package Information



DIM.	MILLIMETERS					
DIN.	MIN	NDM	MAX			
Α	0.935		1.10			
A1	0.01		0.10			
A2	0.70		1.00			
b	0.25	0.32	0.40			
C	0.10	0.15	0.20			
D	2.95	3.05	3.10			
Ε	2.70	2.85	2.98			
E1	1.55	1.65	1.70			
9	0.95 BSC					
L	0.30		0.60			
L1	0.60REF					
L2	0.25BSC					
R	0.10					
θ	0?	4?	8?			
θ1	7? N□M					





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.