

N & P-Channel 60-V (D-S) MOSFET

Key Features:

- Low $r_{DS(on)}$ trench technology
- Low thermal impedance
- Fast switching speed

Typical Applications:

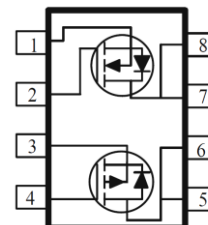
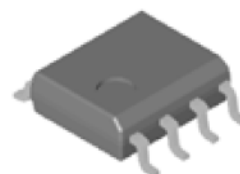
- DC/DC Conversion Circuits
- Motor Drives

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (m Ω)	I_D (A)
60	27 @ $V_{GS} = 10V$	7.2
	33 @ $V_{GS} = 4.5V$	6.5
-60	40 @ $V_{GS} = -10V$	5.9
	50 @ $V_{GS} = -4.5V$	5.3



RoHS
COMPLIANT
HALOGEN
FREE

SO-8



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Nch Limit	Pch Limit	Units
Drain-Source Voltage		V_{DS}	60	-60	V
Gate-Source Voltage		V_{GS}	± 20	± 20	
Continuous Drain Current ^a	$T_A = 25^\circ\text{C}$	I_D	7.5	-4.2	A
	$T_A = 70^\circ\text{C}$		5.9	-3.3	
Pulsed Drain Current ^b		I_{DM}	20	-20	
Continuous Source Current (Diode Conduction) ^a		I_S	2.8	-2.5	A
Power Dissipation ^a	$T_A = 25^\circ\text{C}$	P_D	2.1	2.1	W
	$T_A = 70^\circ\text{C}$		1.3	1.3	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150		$^\circ\text{C}$

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$t \leq 10 \text{ sec}$	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$
	Steady State		110	

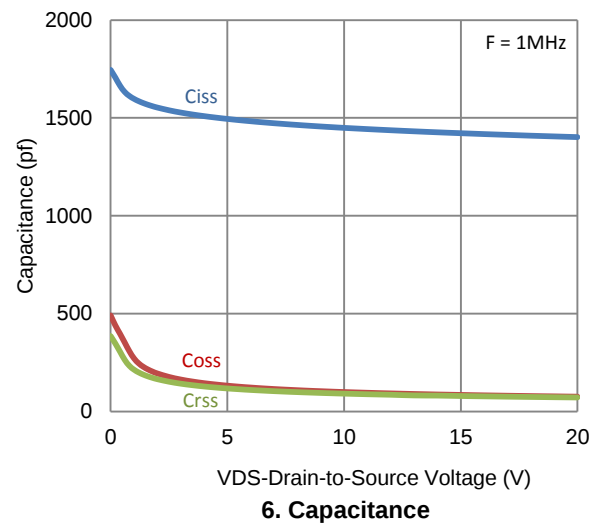
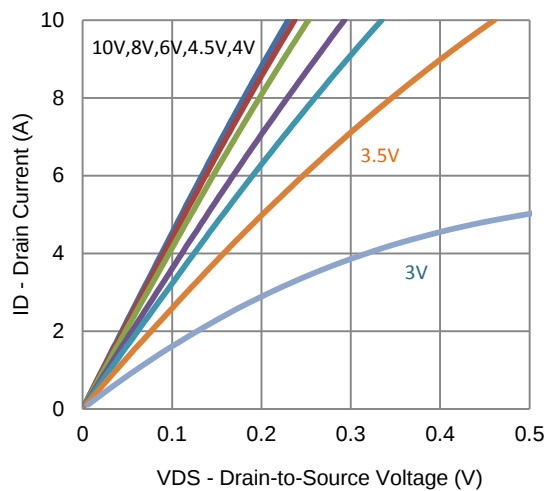
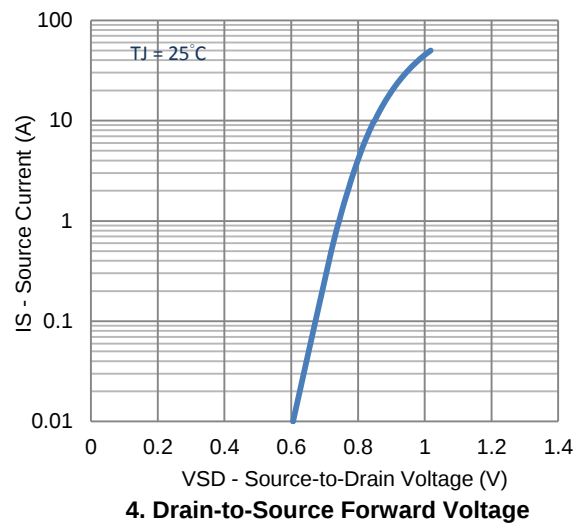
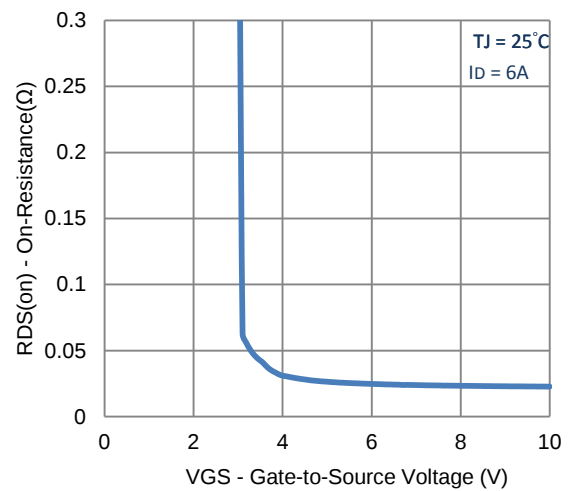
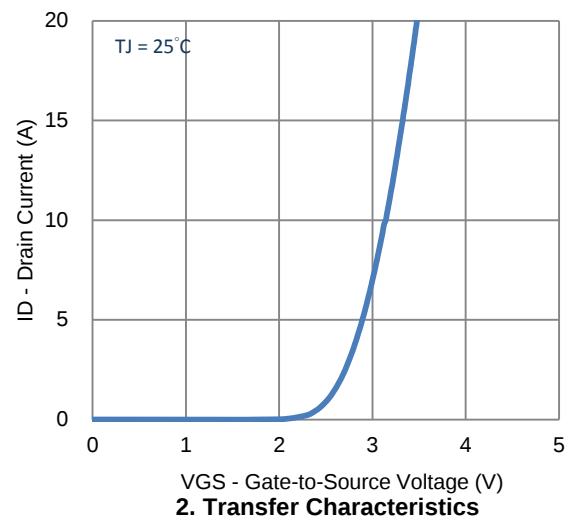
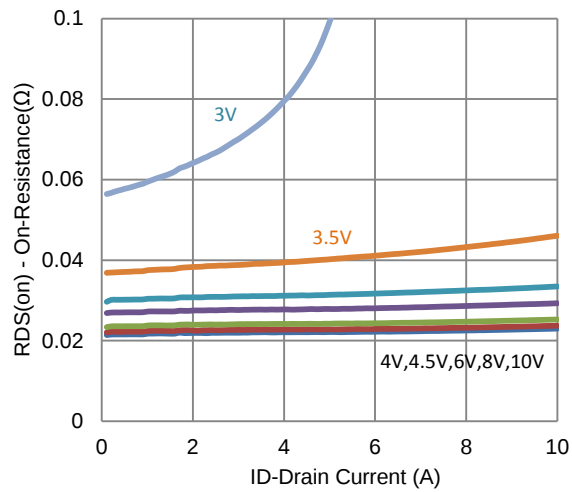
Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

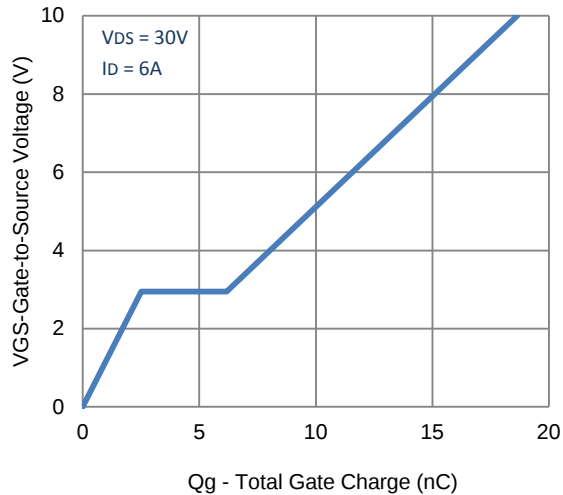
Electrical Characteristics

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 uA (Nch)	1			V
		V _{DS} = V _{GS} , I _D = -250 uA (Pch)	-1			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V (Nch)			1	uA
		V _{DS} = -48 V, V _{GS} = 0 V (Pch)			-1	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V (Nch)	9.5			A
		V _{DS} = -5 V, V _{GS} = -10 V (Pch)	-8			A
Drain-Source On-Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 4.8 A (Nch)			27	mΩ
		V _{GS} = 4.5 V, I _D = 3.8 A (Nch)			33	
		V _{GS} = -10 V, I _D = -2.6 A (Pch)			40	mΩ
		V _{GS} = -4.5 V, I _D = -2.1 A (Pch)			50	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 6 A (Nch)		6		S
		V _{DS} = -15 V, I _D = -3.3 A (Pch)		4		S
Diode Forward Voltage ^a	V _{SD}	I _S = 1.4 A, V _{GS} = 0 V (Nch)		0.74		V
		I _S = -1.25 A, V _{GS} = 0 V (Pch)		-0.79		V
Dynamic ^b						
Total Gate Charge	Q _g	N - Channel V _{DS} = 30 V, V _{GS} = 4.5 V, I _D = 6 A		8.9		nC
Gate-Source Charge	Q _{gs}			2.5		
Gate-Drain Charge	Q _{gd}			3.7		
Turn-On Delay Time	t _{d(on)}	N - Channel V _{DS} = 30 V, R _L = 5 Ω, I _D = 6 A, V _{GEN} = 10 V, R _{GEN} = 6 Ω		5		ns
Rise Time	t _r			6		
Turn-Off Delay Time	t _{d(off)}			31		
Fall Time	t _f			9		
Input Capacitance	C _{iss}	N - Channel V _{DS} = 15 V, V _{GS} = 0 V, f = 1 Mhz		1422		pF
Output Capacitance	C _{oss}			84		
Reverse Transfer Capacitance	C _{rss}			79		
Total Gate Charge	Q _g	P - Channel V _{DS} = -30 V, V _{GS} = -4.5 V, I _D = -3.3 A		18		nC
Gate-Source Charge	Q _{gs}			7.5		
Gate-Drain Charge	Q _{gd}			5.6		
Turn-On Delay Time	t _{d(on)}	P - Channel V _{DS} = -30 V, R _L = 9.1 Ω, I _D = -3.3 A, V _{GEN} = -10 V, R _{GEN} = 6 Ω		12		ns
Rise Time	t _r			9		
Turn-Off Delay Time	t _{d(off)}			69		
Fall Time	t _f			25		
Input Capacitance	C _{iss}	P - Channel V _{DS} = -15 V, V _{GS} = 0 V, f = 1 Mhz		2057		pF
Output Capacitance	C _{oss}			151		
Reverse Transfer Capacitance	C _{rss}			92		

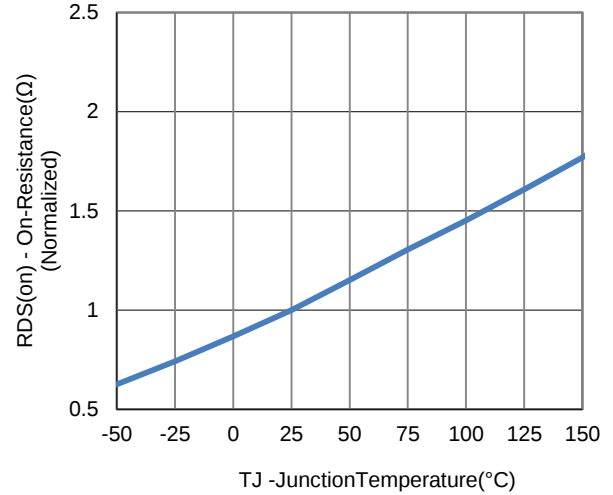
Typical Electrical Characteristics - N-channel



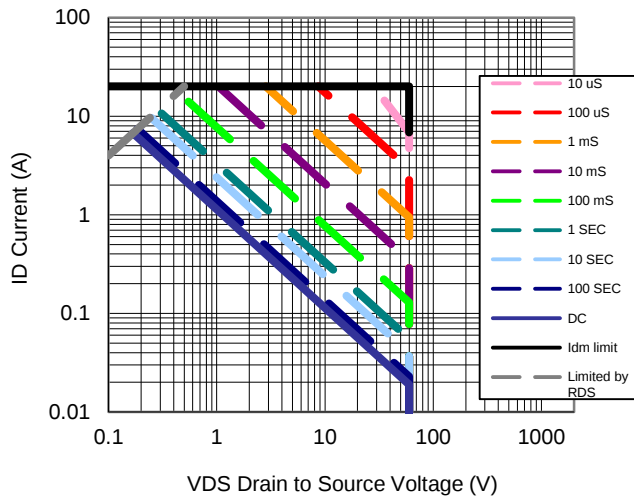
Typical Electrical Characteristics - N-channel



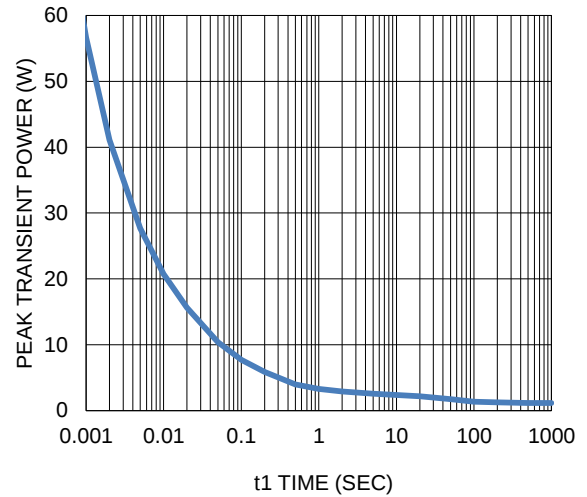
7. Gate Charge



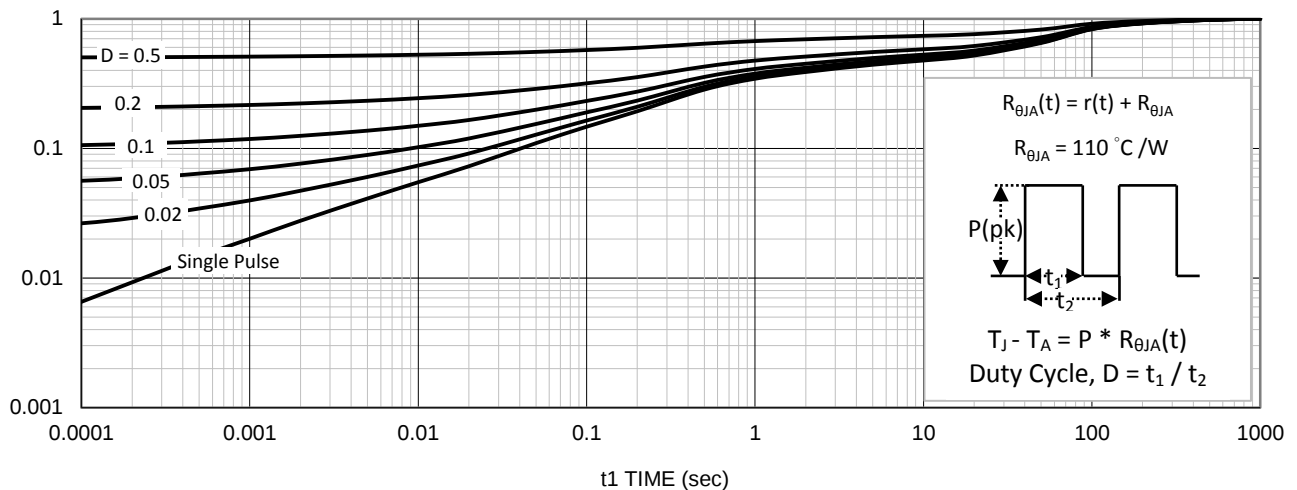
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area

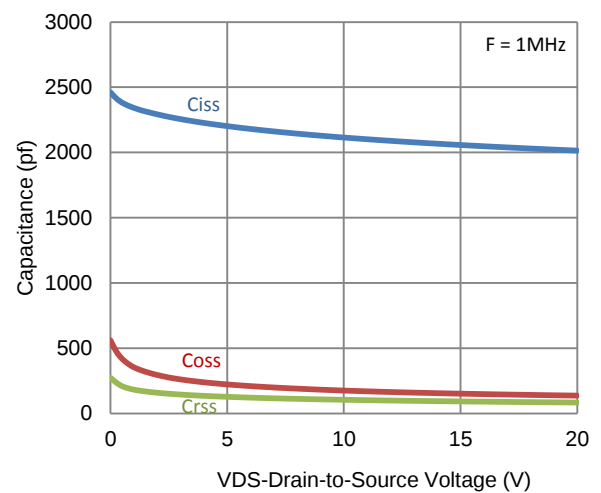
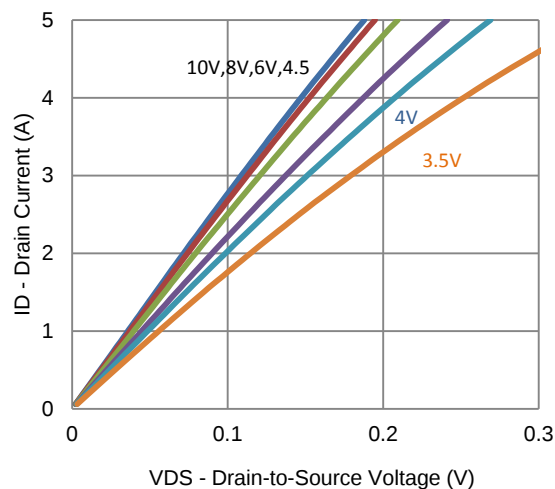
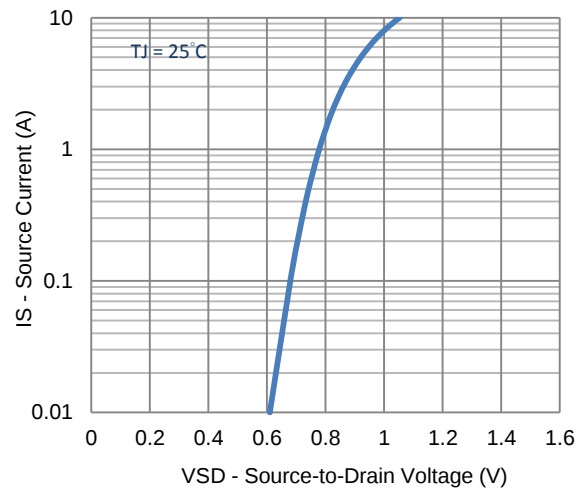
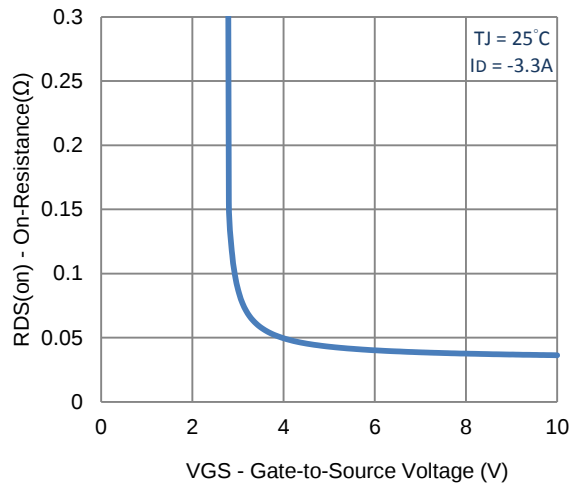
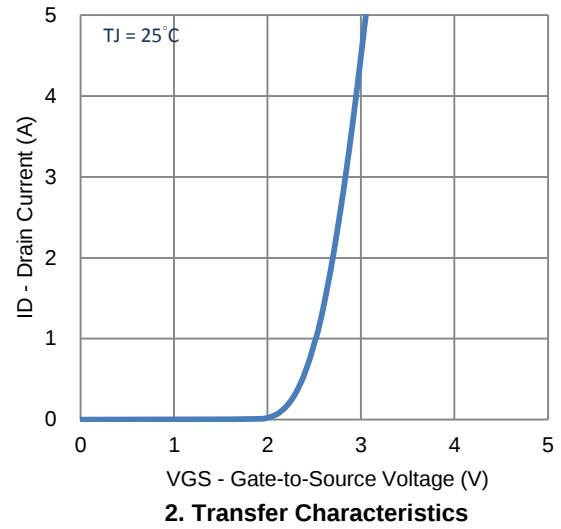
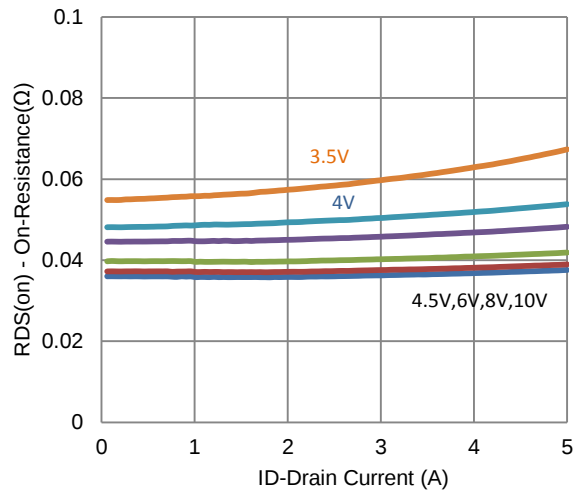


10. Single Pulse Maximum Power Dissipation

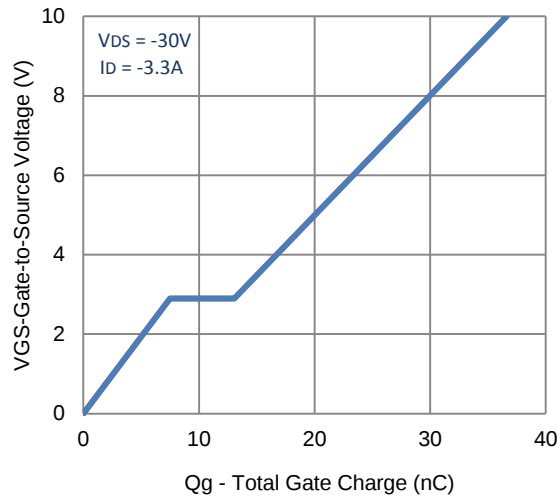


11. Normalized Thermal Transient Junction to Ambient

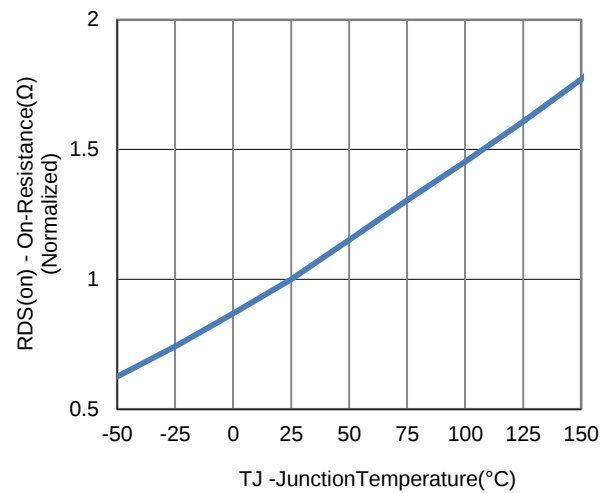
Typical Electrical Characteristics - P-channel



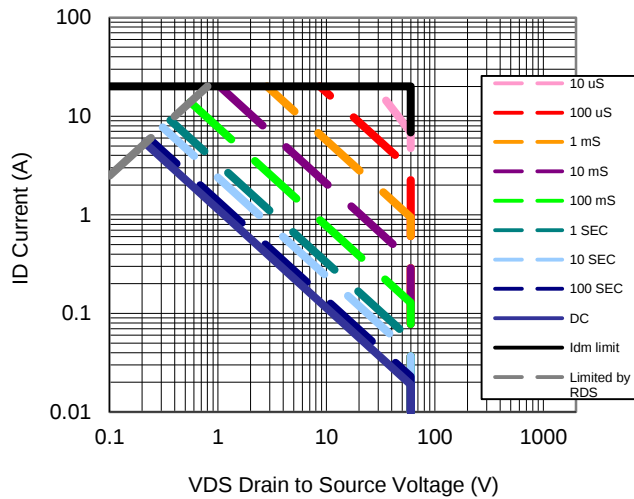
Typical Electrical Characteristics - P-channel



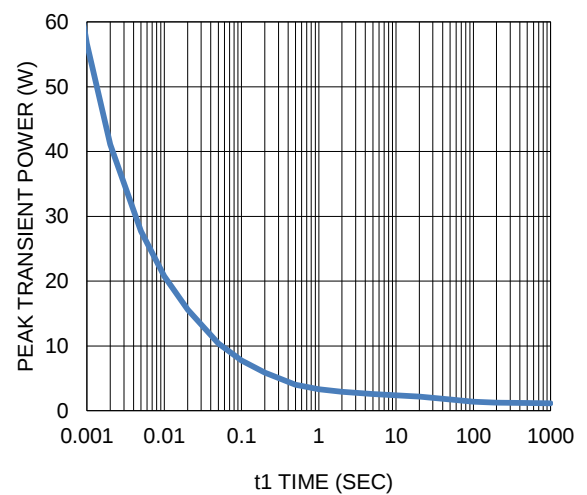
7. Gate Charge



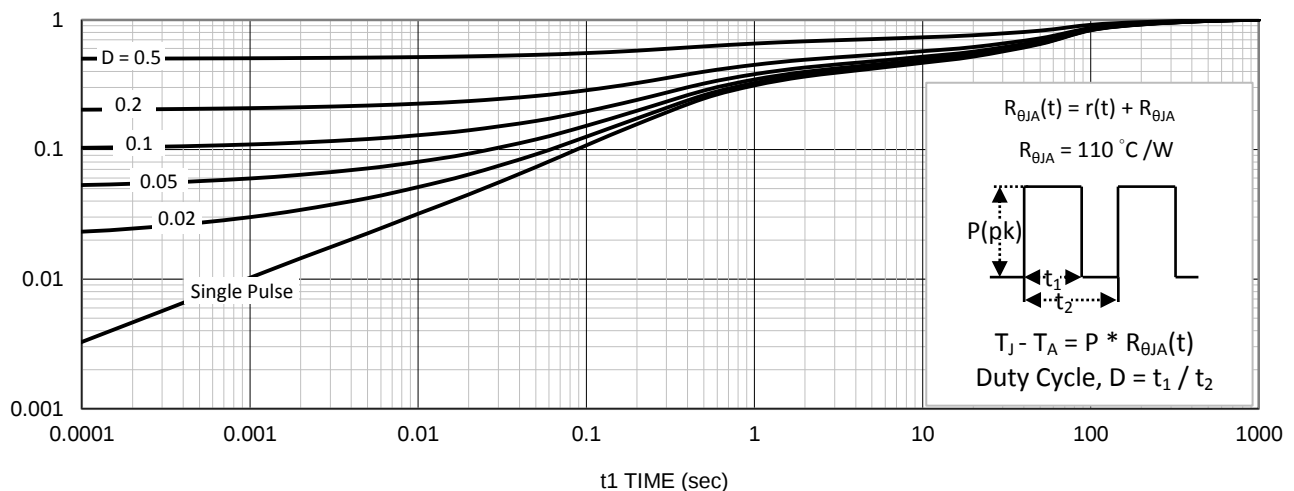
8. Normalized On-Resistance Vs Junction Temperature



9. Safe Operating Area



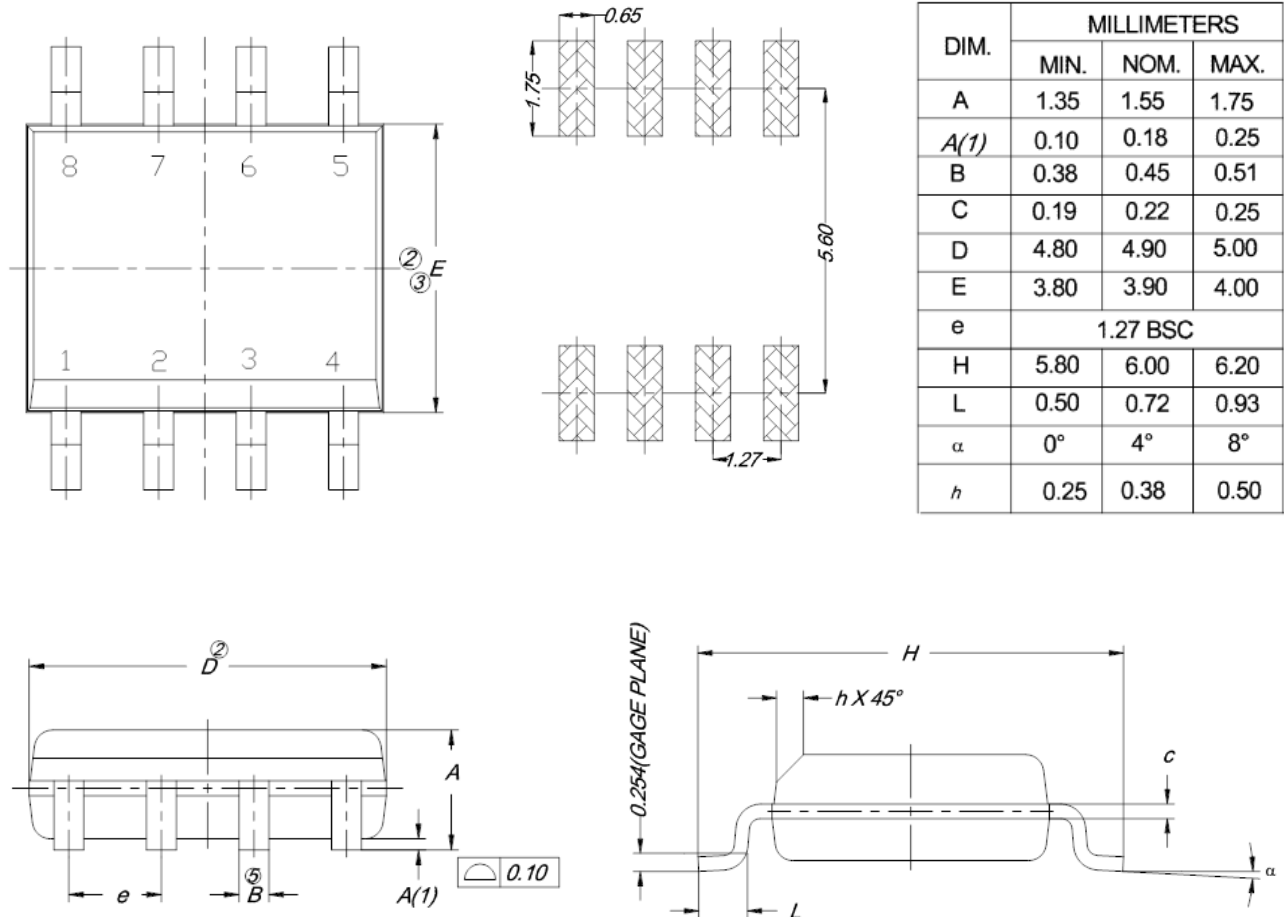
10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information

Land Pattern
(Only for Reference)



Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
4. The Package Top May Be Smaller Than The Package Bottom.
5. Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.