Dual N-Channel 60-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- Fast switching speed

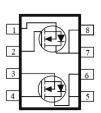
Typical	Appl	lication	s:
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- DC/DC Conversion Circuits
- Motor Drives

PRODUCT SUMMARY			
V _{DS} (V)	$r_{DS(on)}(m\Omega)$	I□ (A)	
60	28 @ V _{GS} = 10V	7.1	
	34 @ V _{GS} = 4.5V	6.5	







ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	Limit	Units	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V_{GS}	±20	V	
Continuous Davis Compant a	T _A =25°		7.1		
Continuous Drain Current ^a	T _A =70°	C I _D	5.6	Α	
Pulsed Drain Current ^b		I _{DM}	30		
Continuous Source Current (Diode Conduction) a			2.8	Α	
Device Dissinction 8	T _A =25°		2.1	W	
Power Dissipation ^a	T _A =70°	C	1.3	VV	
Operating Junction and Storage Temperature Range		T_J, T_{sta}	-55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter			Maximum	Units	
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	62.5	°C/W	
Maximum Junction-to-Ambient	Steady State	IΛθJA	110	C/VV	

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

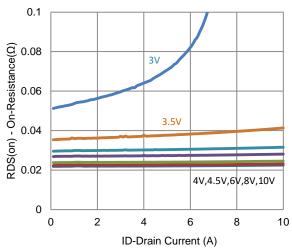
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	lana	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$			1	1 10 uA	
Zero Gate Voltage Brain Gurrent	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10		
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α	
Drain-Source On-Resistance ^a	r	$V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$			28	mΩ	
Drain-Source On-Resistance	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 4 \text{ A}$			34	11152	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 5 \text{ A}$		20		S	
Diode Forward Voltage ^a	V_{SD}	$I_{S} = 1.4 \text{ A}, V_{GS} = 0 \text{ V}$		0.77		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V},$		9			
Gate-Source Charge	Q_{gs}	$I_{DS} = 50 \text{ V}, \text{ V}_{GS} = 4.3 \text{ V},$ $I_{D} = 5 \text{ A}$		2.8		nC	
Gate-Drain Charge	Q_gd	1B = 3 A		3.1			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 30 \text{ V}, R_{L} = 6 \Omega,$		6			
Rise Time	t _r	$V_{DS} = 30 \text{ V}, K_L - 0.02,$ $I_D = 5 \text{ A},$		6		ne	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		32		ns	
Fall Time	t _f	V GEN = 10 V, 1 (GEN = 0.12		9			
Input Capacitance	C _{iss}			1422			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		84		pF	
Reverse Transfer Capacitance	C_{rss}			79			

Notes

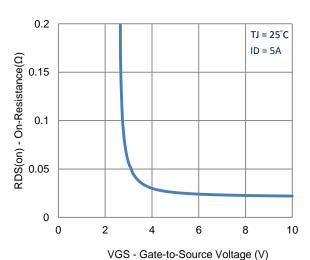
- Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing. b.

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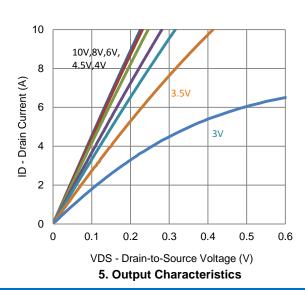
Typical Electrical Characteristics



1. On-Resistance vs. Drain Current



3. On-Resistance vs. Gate-to-Source Voltage

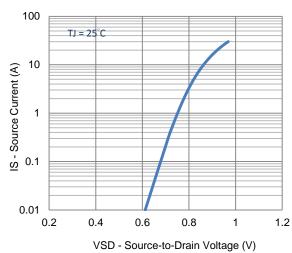


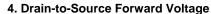
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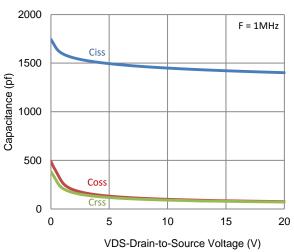
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2. Transfer Characteristics

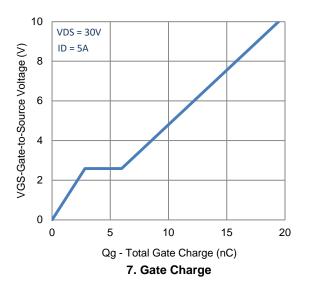


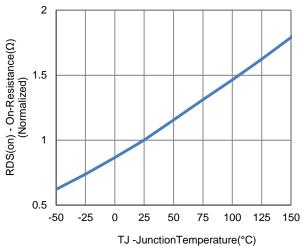


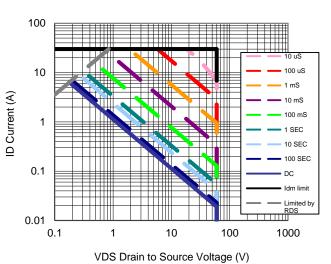


6. Capacitance

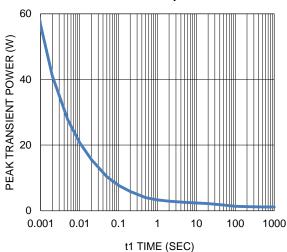
Typical Electrical Characteristics





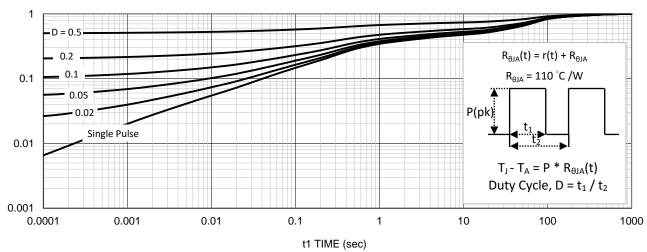






9. Safe Operating Area

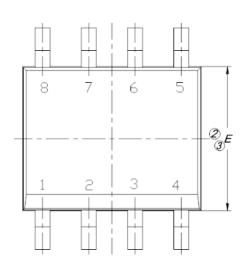
10. Single Pulse Maximum Power Dissipation

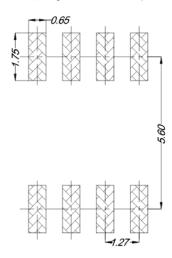


11. Normalized Thermal Transient Junction to Ambient

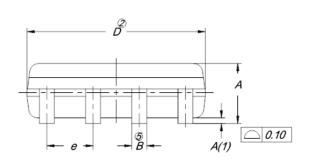
Package Information

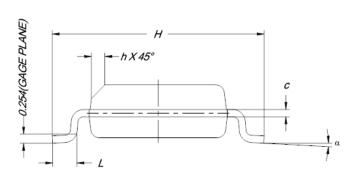
Land Pattern (Only for Reference)





DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	1.35	1.55	1.75		
A(1)	0.10	0.18	0.25		
В	0.38	0.45	0.51		
С	0.19	0.22	0.25		
D	4.80	4.90	5.00		
E	3.80	3.90	4.00		
е	1.27 BSC				
Н	5.80	6.00	6.20		
L	0.50	0.72	0.93		
α	0°	4°	8°		
h	0.25	0.38	0.50		





Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- 3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.
- Dimension "B" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.08 mm Total In Excess Of "B" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.