N-Channel 30-V (D-S) MOSFET

Key Features:

- Low r_{DS(on)} trench technology
- · Low thermal impedance
- · Fast switching speed

Typical Applications:

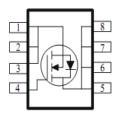
- DC/DC Conversion
- Power Routing
- Motor Drives

PRODUCT SUMMARY				
V _{DS} (V)	I□ (A)			
30	5 @ V _{GS} = 4.5V	26		
	$6.9 @ V_{GS} = 2.5V$	22		

DFN5X6-8L







ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)							
Parameter				Limit	Units		
Drain-Source Voltage				30	V		
Gate-Source Voltage	Source Voltage				V		
Continuous Drain Coursetta		Γ _A =25°C		26			
Continuous Drain Current ^a		Γ _A =70°C	I _D	21	Α		
Pulsed Drain Current ^b				100			
Continuous Source Current (Diode Conduction) a	I _S	7.3	Α				
Dawar Dissipation 8		Γ _A =25°C	P _D	5	W		
Power Dissipation ^a			ı D	3.2	V V		
Operating Junction and Storage Temperature Range	T_J, T_{sta}	-55 to 150	°C				

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Maximum	Units				
Maximum Junction-to-Ambient ^a	t <= 10 sec	$R_{\theta JA}$	25	°C/W			
IMAXIMUM JUNCTION-TO-AMBIENT	Steady State	IΛθJA	65	C/VV			

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Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

Electrical Characteristics

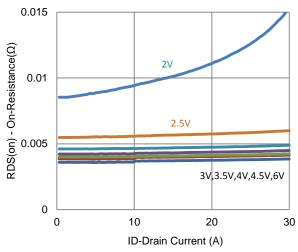
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	0.5			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1 uA		
Zero Gate Voltage Brain Guirent	I _{DSS}	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$	10		uA		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	35			Α	
	r	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$			5	mΩ	
Drain-Source On-Resistance ^a	r _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 16 \text{ A}$			6.9	11152	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 20 \text{ A}$		12		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 3.65 \text{ A}, V_{GS} = 0 \text{ V}$		0.72		V	
		Dynamic ^b					
Total Gate Charge	Q_g	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V},$		52		nC	
Gate-Source Charge	Q_{gs}	$I_{D} = 20 \text{ A}$		9			
Gate-Drain Charge	Q_gd	1D = 20 A		13			
Turn-On Delay Time	t _{d(on)}	$V_{DS} = 15 \text{ V}, R_{L} = 0.8 \Omega,$		31			
Rise Time	t _r	$V_{DS} = 13 \text{ V}, \text{ K}_{L} = 0.6 \Omega,$ $I_{D} = 20 \text{ A},$		62		ns	
Turn-Off Delay Time	$t_{d(off)}$	$V_{GEN} = 4.5 \text{ V}, R_{GEN} = 6 \Omega$		169			
Fall Time	t _f	VGEN - 4.5 V, NGEN - 0 12		55			
Input Capacitance	C _{iss}			7480			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		382		pF	
Reverse Transfer Capacitance	C_{rss}			370			

Notes

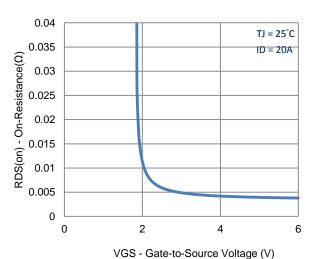
- a. Pulse test: PW <= 300us duty cycle <= 2%.
- b. Guaranteed by design, not subject to production testing.

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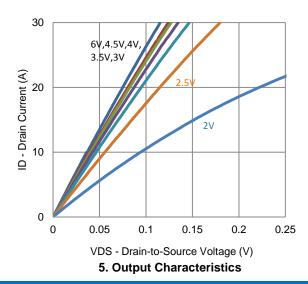
Typical Electrical Characteristics

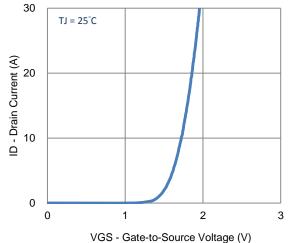


1. On-Resistance vs. Drain Current

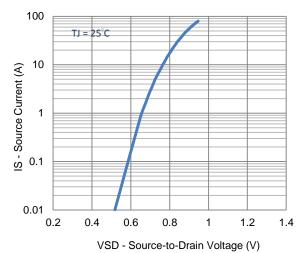


3. On-Resistance vs. Gate-to-Source Voltage

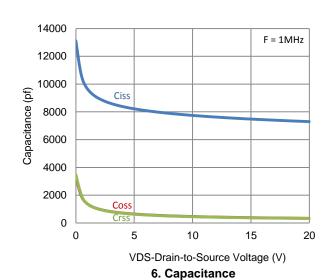




2. Transfer Characteristics



4. Drain-to-Source Forward Voltage

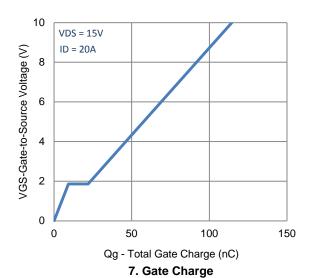


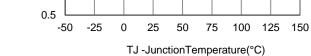
Typical Electrical Characteristics

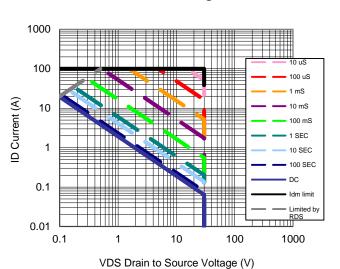
2

1.5

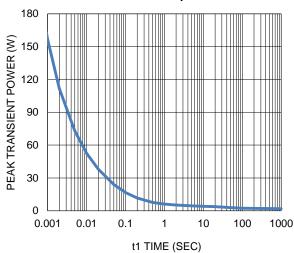
 $RDS(on) \text{ - }On\text{-}Resistance}(\Omega)$ (Normalized)





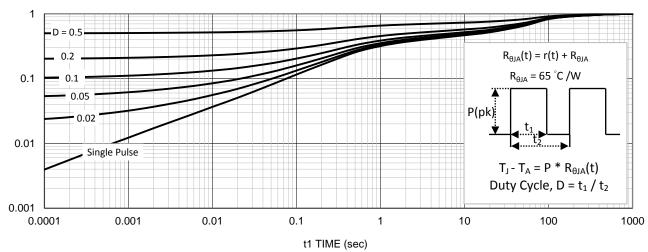






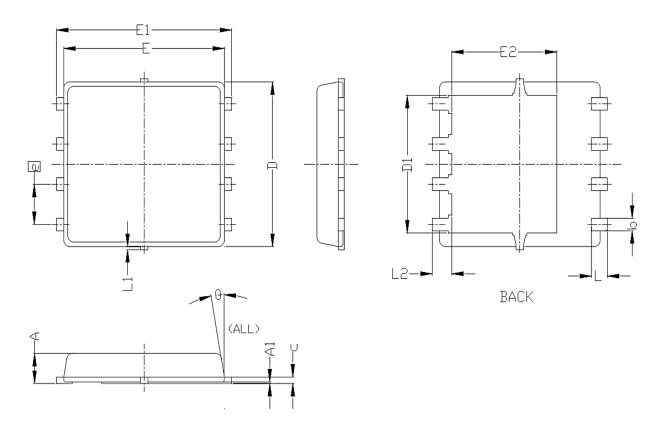
9. Safe Operating Area

10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0.95	1.00	0.033	0.037	0.039	
Al	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
С	0.15	0. 20	0.25	0.006	0.008	0.010	
D	5. 20 BSC			0. 205 BSC			
D1	4. 35 BSC			0. 171 BSC			
Е	5, 55 BSC			0. 219 BSC			
E1	6. 05 BSC			0. 238 BSC			
E2	3. 62 BSC			0. 143 BSC			
e	1. 27 BSC			0.050 BSC			
L	0.45	0.55	0.65	0.018	0.022	0.026	
L1	0		0.15	0		0.006	
L2	0.68 REF			0. 027 REF			
θ	0°		10°	0°		10°	