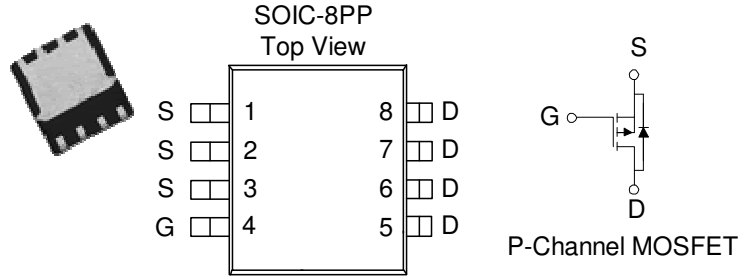


P-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ m(Ω)	I_D (A)
-30	9 @ $V_{GS} = -10V$	-20
	13 @ $V_{GS} = -4.5V$	-17

- Low $r_{DS(on)}$ provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8PP saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	I_D	$T_A=25^\circ C$	-20
		$T_A=70^\circ C$	-16
Pulsed Drain Current ^b	I_{DM}	± 50	A
Continuous Source Current (Diode Conduction) ^a	I_S	-2.1	A
Power Dissipation ^a	P_D	$T_A=25^\circ C$	5.0
		$T_A=70^\circ C$	3.2
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ C$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^a	$R_{\theta JA}$	t \leq 10 sec	25
		Steady State	65

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

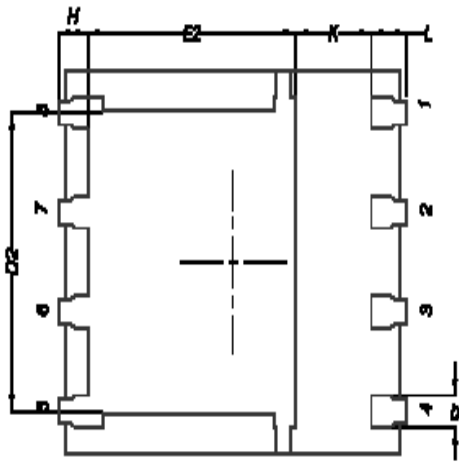
SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = -250 μA	-30			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-1			
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±25 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -24 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -24 V, V _{GS} = 0 V, T _J = 55°C			-5	
On-State Drain Current ^A	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -10 V	-50			A
Drain-Source On-Resistance ^A	r _{DS(on)}	V _{GS} = -10 V, I _D = -2 A			9	mΩ
		V _{GS} = -4.5 V, I _D = -2 A			13	
Forward Transconductance ^A	g _{fs}	V _{DS} = -15 V, I _D = -2 A		29		S
Diode Forward Voltage	V _{SD}	I _S = 2 A, V _{GS} = 0 V		-0.8		V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -15 V, V _{GS} = -5 V, I _D = -2 A		25		nC
Gate-Source Charge	Q _{gs}			11		
Gate-Drain Charge	Q _{gd}			17		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -15 V, R _L = 6 Ω, I _D = -1 A, V _{GEN} = -10 V		15		nS
Rise Time	t _r			13		
Turn-Off Delay Time	t _{d(off)}			100		
Fall-Time	t _f			54		

Notes

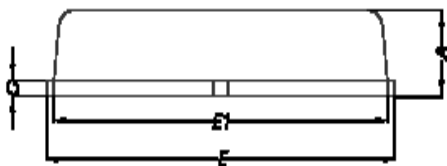
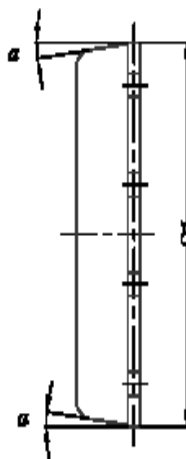
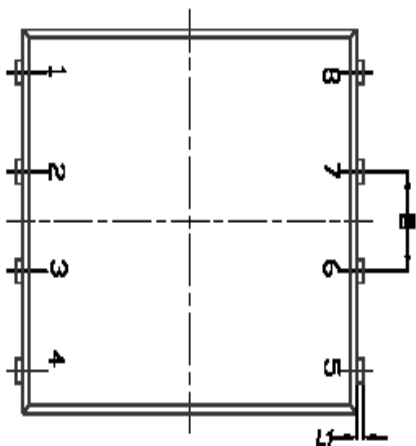
- Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

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Package Information



BACKSIDE VIEW



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.38	0.41	0.51
C	0.20	0.25	0.30
D1	4.90	4.90	5.00
D2	3.81	3.81	3.98
E	5.00	6.00	6.10
E1	5.70	6.75	5.00
E2	3.98	3.98	3.78
Ø	1.27 BSC		
H	0.41	0.51	0.51
K	1.10	-	-
L	0.51	0.51	0.71
L1	0.08	0.13	0.20
α	0°	-	12°