Analog Power AM7448N

### N-Channel 40-V (D-S) MOSFET

### **Key Features:**

- Low r<sub>DS(on)</sub> trench technology
- · Low thermal impedance
- · Fast switching speed

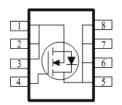
## **Typical Applications:**

- DC/DC Conversion
- Power Routing
- Motor Drives

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	$r_{DS(on)}(m\Omega)$	I <sub>□</sub> (A)	
40	$3.3 @ V_{GS} = 10V$	80 <sup>c</sup>	







ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}$ C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Limit	Units			
Drain-Source Voltage			40	V		
Gate-Source Voltage			±20	V		
	T <sub>C</sub> =25°C		80 <sup>c</sup>	A		
Continuous Drain Current	T <sub>C</sub> =70°C	l <sub>D</sub>	80 <sup>c</sup>			
Continuous Diain Current	T <sub>A</sub> =25°C		32 <sup>a</sup>			
	T <sub>A</sub> =70°C		26 <sup>a</sup>			
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	120				
Continuous Source Current (Diode Conduction) a		Is	7.1			
	T <sub>C</sub> =25°C		83	W		
Power Dissipation	T <sub>C</sub> =70°C	$P_{D}$	53			
Prower Dissipation	T <sub>A</sub> =25°C	' D	5 <sup>a</sup>			
	T <sub>A</sub> =70°C		3.2 <sup>a</sup>			
Operating Junction and Storage Temperature Range	•	$T_J,T_stg$	-55 to 150	°C		

HALOGEN FREE

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Maximum	Units					
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	$R_{\theta JA}$	25	°C/W				
IMAXIIIIUIII SUIICUOII-to-AIIISIEIIt	Steady State	IΛθΊΑ	65					
Maximum Junction-to-Case (Drain)	Steady State	$R_{\theta JC}$	1.5					

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#### Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature
- c. Package limited

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### **Electrical Characteristics**

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
Static								
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \text{ uA}$	1			V		
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA		
Zero Gate Voltage Drain Current	1	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}$			1	uA		
Zero Gate Voltage Brain Gurrent	I <sub>DSS</sub>	$V_{DS} = 32 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			10			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α		
Drain-Source On-Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_{D} = 16 \text{ A}$			3.3	mΩ		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 16 \text{ A}$		72		S		
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 3.6 \text{ A}, V_{GS} = 0 \text{ V}$		0.72		V		
		Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$	$V_{DS} = 20 \text{ V}, V_{GS} = 6.5 \text{ V},$		48				
Gate-Source Charge	$Q_gs$	$I_{DS} = 20 \text{ V}, \text{ V}_{GS} = 0.3 \text{ V},$ $I_{D} = 16 \text{ A}$		26		nC		
Gate-Drain Charge	$Q_gd$	10 - 10 / 1		14				
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DS} = 20 \text{ V}, R_{L} = 1.3 \Omega,$		48				
Rise Time	t <sub>r</sub>	$I_{DS} = 20 \text{ V}, N_{L} = 1.3 \Omega,$ $I_{D} = 16 \text{ A},$		40		ns		
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GEN} = 10 \text{ V}, R_{GEN} = 6 \Omega$		52		115		
Fall Time	t <sub>f</sub>	VGEN - 10 V, NGEN 0 12		25				
Input Capacitance	$C_{iss}$			5042				
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ Mhz}$		867		pF		
Reverse Transfer Capacitance	$C_{rss}$			462				

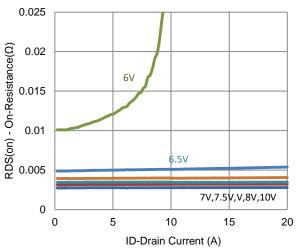
#### Notes

- a. Pulse test: PW <= 300us duty cycle <= 2%.
- Guaranteed by design, not subject to production testing.

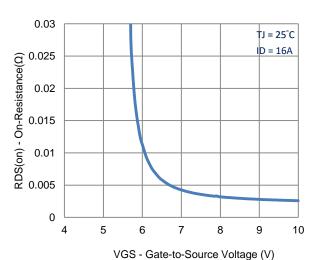
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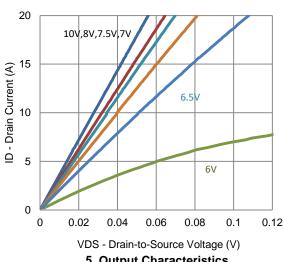
### **Typical Electrical Characteristics**



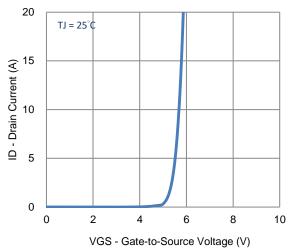
#### 1. On-Resistance vs. Drain Current



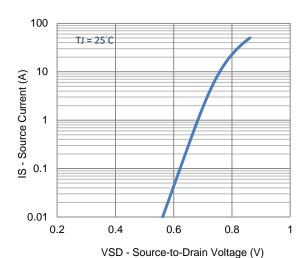
3. On-Resistance vs. Gate-to-Source Voltage



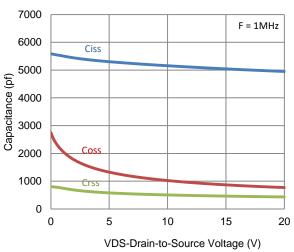
5. Output Characteristics



2. Transfer Characteristics



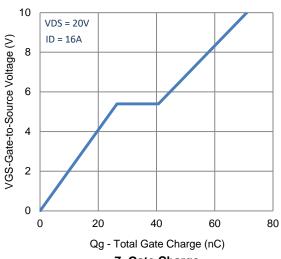
4. Drain-to-Source Forward Voltage

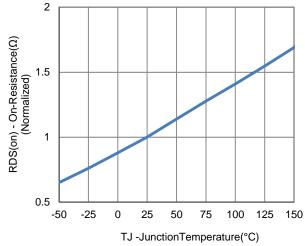


6. Capacitance

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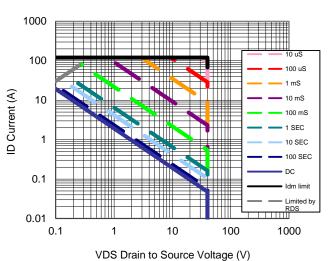
### **Typical Electrical Characteristics**

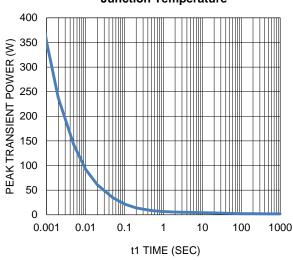




7. Gate Charge

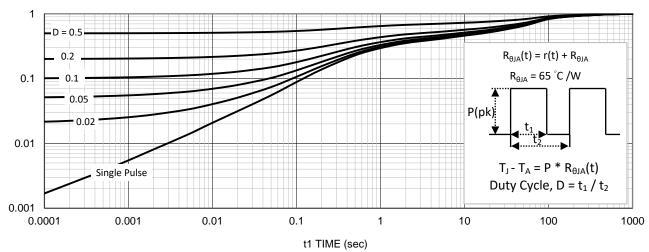






9. Safe Operating Area

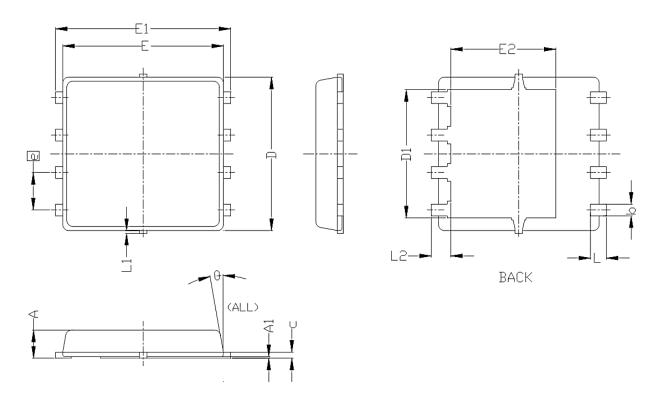
10. Single Pulse Maximum Power Dissipation



11. Normalized Thermal Transient Junction to Ambient

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# Package Information



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
STMDOLS	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00		0.05	0.000		0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
С	0. 15	0.20	0. 25	0.006	0.008	0.010
D	5. 20 BSC			0. 205 BSC		
D1	4. 35 BSC			0. 171 BSC		
E	5, 55 BSC			0. 219 BSC		
E1	6. 05 BSC			0. 238 BSC		
E2	3. 62 BSC			0. 143 BSC		
e	1. 27 BSC			0. 050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
Ll	0		0.15	0		0.006
L2	0.68 REF			0. 027 REF		
θ	0°		10°	0°		10°