

# 3-Phase Sensor-less Fan Motor Driver AM8931

The AM8931 is a 3-Phase sensor-less DC fan motor driver IC. It senses the BEMF (Back Electro-Motive Force) of the motor in rotation and provides corresponding commutation current to the motor. Rotation speed can be controlled by PWM input signal. The drivers include Lock Detection, Thermal Shutdown, and Over-current limiter. Maximum output current is 1000mA.

#### Features

- 1) Operation voltage 3.3 to 15V
- 2) Direct PWM speed control
- 3) Built-in FG & RD
- 4) Soft start function
- 5) Forward and Reverse control
- 6) Lock detection/Automatic restart function
- 7) Over current limiter
- 8) Over-voltage protection
- 9) Thermal shutdown protection
- 10) Soft switching technique to reduce acoustic noise

## ■ Absolute Maximum Ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>cc</sub>	16	V
VREG strength voltage	$V_{REG}$	6	V
Output current	lo	1000	mA
FG & RD output voltage	V <sub>FG</sub> & V <sub>RD</sub>	Vcc	V
FG & RD output current	I <sub>FG</sub> & I <sub>RD</sub>	10	mA
PWM & F/R strength voltage	V <sub>PWM</sub> &V <sub>FR</sub>	$V_{CC}$	V
CSOFT & OSC strength voltage	Vcsoft & Vosc	$V_{REG}$	V
Power dissipation (JEDEC 2S2P)	Pd	3830*	mW
Operate temperature range	T <sub>opr</sub>	<b>-40</b> ∼+95	$^{\circ}\! \mathbb{C}$
Storage temperature range	T <sub>stg</sub>	-55∼+150	$^{\circ}\!\mathbb{C}$
Junction temperature	Tjmax	150	$^{\circ}\!\mathbb{C}$

<sup>\*</sup> Pd de-rated by 30.64mW/°C over 25°C (based on JEDEC 2S2P board)

Those are stress rating only and functional operating at those conditions for extended periods may damage to the device.

## Recommended operating conditions

(Set the power supply voltage taking allowable dissipation into considering)

Parameter	Symbol	Min	Тур	Max	Unit
Operating supply voltage range	Vcc		3.3~15		V

## Storage Condition

Parameter	Value	Unit
Temperature condition Before Opening	5~40	$^{\circ}\!\mathbb{C}$
Humidity condition Before Opening	30~80%	RH
Temperature condition after Opening	<30	$^{\circ}$
Humidity condition after Opening	<60%	RH



#### Electrical Characteristics

(Unless otherwise specified, Ta =  $25^{\circ}$ C, VCC = 12.0V)

Damamatan	Course le sel	Limit		11	Conditions		
Parameter	Symbol	Min	Тур	Max	Unit	Conditions	
Supply current	I <sub>cc</sub>	_	1.8	3	mA	PWM= V <sub>REG</sub>	
Stand-by current	I <sub>SC</sub>	_	0.6	1	mA	PWM= 0V	
Regulator voltage	$V_{REG}$	2.85	3.0	3.15	V		
Oscillator							
OSC pin charge current	I <sub>oscc</sub>	_	-12	_	μA	OSC pin= 0.3V	
OSC pin discharge current	I <sub>OSCD</sub>	_	12	_	μA	OSC pin= 1.5V	
PWM, F/R input							
Input H level	V <sub>PWMH</sub> / V <sub>FRH</sub>	2.5	_	V <sub>CC</sub>	V		
Input L level	V <sub>PWML</sub> / V <sub>FRL</sub>	0	_	0.8	V		
PWM input frequency	F <sub>PWM</sub>	20	_	50	kHz		
Output							
Output ON resistance	R <sub>on</sub> (H+L)	_	1.5	2.1	Ω	I <sub>0</sub> =300mA High and Low total	
FG/RD low voltage	$V_{FGL}/V_{RDL}$	_	_	0.4	V	$I_{FG/RD} = 3mA$	
FG/RD leakage current	I <sub>FGH</sub> / I <sub>RDH</sub>	_	_	10	μA	$V_{FG/RD} = 15V$	
Lock protection							
Lock detection ON time	T <sub>ON</sub>	0.7	1	1.3	sec	T <sub>ON</sub> = start time + lock detect	
Lock detection OFF time	T <sub>OFF</sub>	3.5	5	6.5	sec		
Soft start							
Soft start release voltage	Vcsoft	1.5	2.0	2.5	V		
Soft start charge current	Icsoft	_	0.5	_	μA		
Current limiter							
Current limit voltage	$V_{RNF}$	0.2	0.25	0.3	V		
Thermal							
Thermal shutdown	ThSD	150	170	_	$^{\circ}\!\mathbb{C}$	*1	
Thermal shutdown hysteresis	$\Delta$ ThSD	_	25	_	$^{\circ}$ C	*1	

<sup>\*1:</sup> It is design target, not to be measured at production test.



## Block Diagram

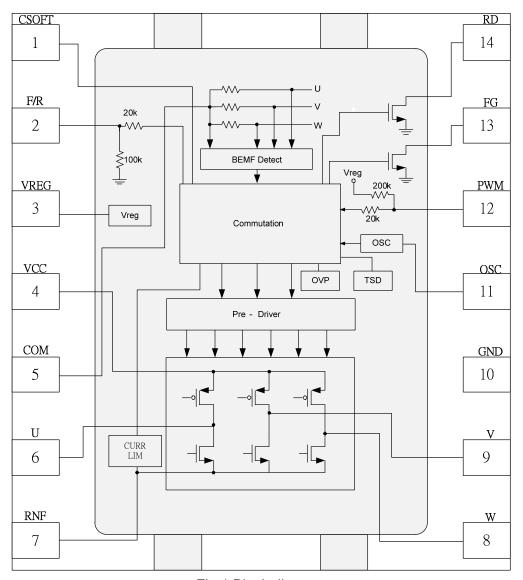


Fig.1 Block diagram

## Pin Description

PIN No	Pin Name	Function	PIN No	Pin Name	Function
1	CSOFT	Soft start time setting terminal	8	W	W phase output terminal
2	F/R	Forward and Reverse control terminal	9	V	V phase output terminal
3	VREG	Regulator voltage output terminal	10	GND	Ground terminal
4	VCC	Power supply terminal	11	OSC	Start-up frequency output terminal
5	COM	Motor center tap voltage input terminal	12	PWM	PWM signal input terminal
6	U	U phase output terminal	13	FG	FG signal output terminal
7	RNF	output current detection terminal	14	RD	RD signal output terminal
E-pad	PG	Power ground terminal			



## Application circuit

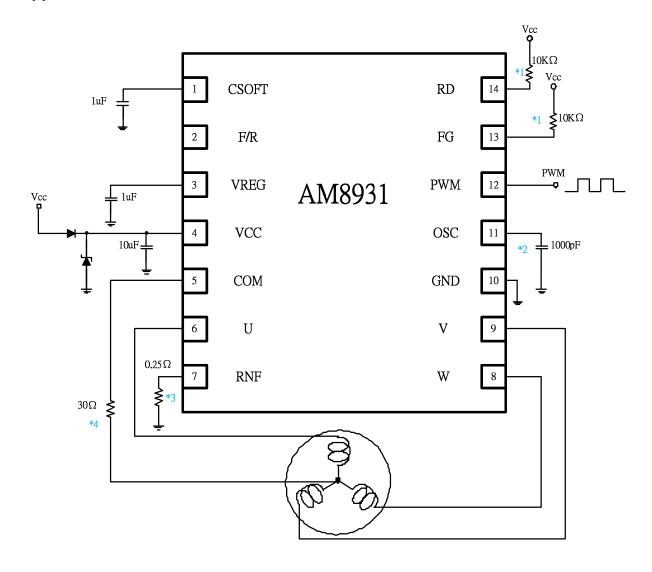


Fig.2 Application circuit

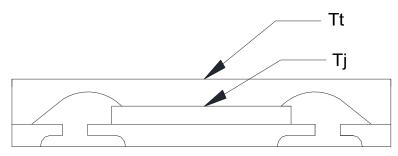
- \*1 Open drain output. A pull-up resistances of  $10k\Omega$  should be inserted.
- \*2 This Capacitor 1000pF is only for reference. Variable Motors should select suitable capacitor for optimum start-up characteristics.
- \*3 Current limiter voltage setting is 0.25V(Typ). The formula is RNF=0.25V/current limit target.  $(0.25\Omega=0.25V/1A)$
- \*4 Need to add a  $30\Omega$  resistor between Fan and IC Com Pin.



#### Thermal Information

Θја	junction-to-ambient thermal resistance	32.64°C/W
Ψjt	junction-to-top characterization parameter	0.73°C/W

- ▶ **Oja** is obtained in a simulation on a JEDEC-standard 2s2p board as specified in JESD-51.
- The **Oja** number listed above gives an estimate of how much temperature rise is expected if the device was mounted on a standard JEDEC board.
- When mounted on the actual PCB, the **Oja** value of JEDEC board is totally different than the **Oja** value of actual PCB.
- Ψjt is extracted from the simulation data to obtain Oja using a procedure described in JESD-51, which estimates the junction temperature of a device in an actual PCB.
- > The thermal characterization parameter, Ψjt, is proportional to the temperature difference between the top of the package and the junction temperature. Hence, it is useful value for an engineer verifying device temperature in an actual PCB environment as described in JEDEC JESD-51-12.
- > When Greek letters are not available, Ψjt is written Psi-jt.
- Definition:



DFEINITION:
$$\psi_{jt}$$
 = ( $T_j - T_t$ )/  $P_d$ 

Where:

Ψjt (Psi-jt) = Junction-to-Top(of the package) °C/W

Tj= Die Junction Temp. °C

Tt= Top of package Temp at center. °C

Pd= Power dissipation. Watts

- Practically, most of the device heat goes into the PCB, there is a very low heat flow through top of the package, So the temperature difference between Tj and Tt shall be small, that is any error caused by PCB variation is small.
- This constant represents that Ψjt is completely PCB independent and could be used to predict the Tj in the environment of the actual PCB if Tt is measured properly.



## How to predict Tj in the environment of the actual PCB

Step 1 : Used the simulated Ψjt value listed above.

Step 2 : Measure Tt value by using

#### > Thermocouple Method

We recommend use of a small ~40 gauge(3.15mil diameter) thermocouple. The bead and thermocouples wires should touch the top of the package and be covered with a minimal amount of thermally conductive epoxy. The wires should be heat-insulated to prevent cooling of the bead due to heat loss into wires. This is important towards preventing "too cool" **Tt** measurements, which would lead to the calculated **Tj** also being too cool.

#### > IR Spot Method

An IR Spot method should be utilized only when using a tool with a small enough spot area to acquire the true top center "hot spot".

Many so-called "small spot size" tools still have a measurement area of 0~100+mils at "zero" distance of the tool from the surface. This spot area is too big for many smaller packages and likely would result in cooler readings than the small — thermocouple method. Consequently, to match between spot area and package surface size is important while measuring **Tt** with IR sport method.

Step 3 : calculating power dissipation by

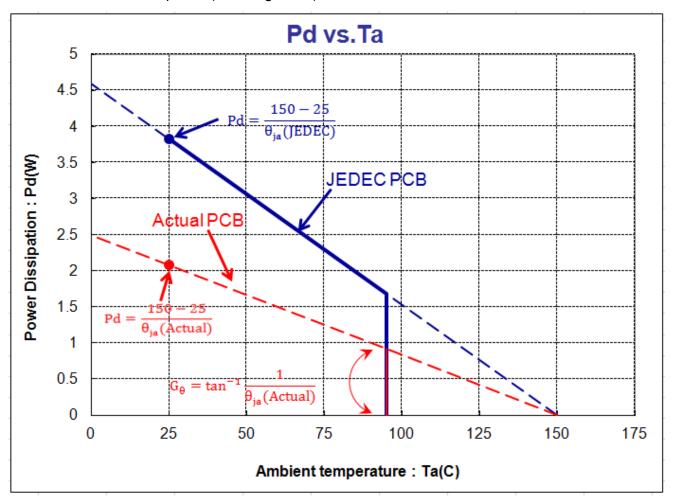
$$P \cong (VCC-|Vo_{Hi}-Vo_{Lo}|) \times I_{out} + VCC \times Icc$$

Step 4 : Estimate **Tj** value by

Step 5: Calculated Oja value of actual PCB by the known Tj



## Maximum Power Dissipation (de-rating curve) under JEDEC PCB & actual PCB





#### Operation notes

#### 1) VCC power supply line

The BEMF causes re-circulate current to power supply. Please connect a capacitor between power supply VCC pin and ground as a route of re-circulate current. And please determine the capacitance after confirmation that the capacitance does not causes any problems.

#### 2) VREG regulator

VREG is voltage regulator output and internal circuits used. Connect capacitors to ground for stable operation.

#### 3) Ground potential

Ground potential E-PAD and GND pin connect the lowest voltage on the chip and short the path as possible.

#### 4) PWM speed control

This IC offer PWM pin direct control output transistors for motor speed control. Higher frequency will reduce output current noise. The control input frequency recommended operation between 20 KHz to 50 KHz. If frequency is slower than 7 KHz (typ.), it will go into stand-by mode. This pin connects internal pull-high resistance of 200K ohm to VREG. When connect to VREG or floating. The motor will rotate in the full speed.

#### 5) Soft Switching Circuits

This IC use duty-variable switching for low acoustic noise and vibration.

#### 6) Start-up Circuits

The OSC pin is defined a sensor-less start-up commutation frequency. The connecting capacitor is between the OSC pin and ground. Variable Motors start-up characteristic are variable with different capacitors. Variable Motors should select suitable capacitor for optimum start-up characteristics. If the capacitance value is larger, the variation start-up time is longer. Also, if the capacitance value is smaller, the motor start-up time is shorter and might cause start-up failed by fan friction.

#### 7) FG / RD function

This FG or RD pin is made up with an open drain output.

Recommend connect a resistance of 10k ohm to supply.

#### 8) Thermal design and Thermal shutdown

The thermal design should allow enough margins for actual power dissipation. In case the IC is left running over the allowable loss, the junction temperature rises, and the thermal-shutdown circuit works at the junction temperature of 170°C (typ.) (the outputs of all the channels are turned off). When the junction temperature drops to 145°C (typ.), the IC start operating again.

#### 9) F/R (Forward and Reverse) function

Motor direction can be forward or reverse by switching F/R voltage level.

F/R high (VREG): U -> V-> W and F/R low (ground): U->W->V.

Internal pull low resistor is 100k ohms.



#### 10) Current limiter

Current limiter voltage setting is 0.25V. Connect resistance to ground to determine the current limit value. The resistance path needs wider and ground side make shorter to GND. The formula is RNF=0.25V/current limit target.  $(0.25\Omega=0.25V/1A)$ 

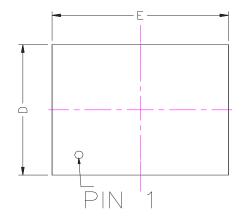
#### 11) Soft start function

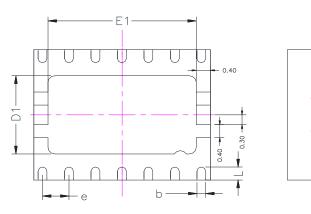
The motor could be smoothly start-up when Soft start pin connecting a capacitor to ground. The function release when the voltage reaches 2.0V or more. If the soft start function is not used, keeps this pin floating.

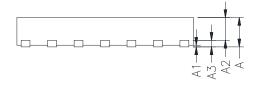
Unit: mm



## • Packaging outline --- DFN 5X4-14L







#### reference JEDEC MO229(D)VJGD-2

SYMBOL	MILLIN	<b>METERS</b>	INCHES		
STNIBUL	Min.	Max.	Min.	Max.	
A	-	0.90	-	0.035	
A1	-	0.05	-	0.002	
<b>A2</b>	-	0.70	-	0.028	
A3	0.20 REF		0.008 REF		
b	0.20	0.30	0.008	0.012	
D	4.00 BSC		0.157 BSC		
E	5.00 BSC		0.197 BSC		
D1	2.35	2.45	0.093	0.096	
E1	4.15	4.25	0.163	0.167	
L	0.30	0.50	0.012	0.020	
e	0.75 BSC		0.030	BSC	



## Condition of Soldering

1). Manual Soldering

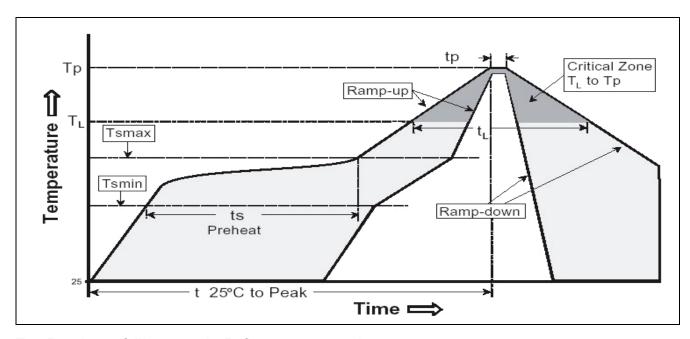
Time / Temperature  $\leq 3 \sec / 400 \pm 10 \, ^{\circ}\text{C}$  (1 Cycle)

Test Results: 0 fail/ 22 tested Manual Soldering count: 2 Times

2).Re-flow Soldering (follow IPC/JEDEC J-STD-020D)

Classification Reflow Profile

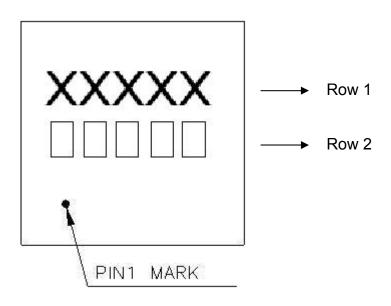
Profile Feature	Pb-Free Assembly	
Average ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second max.	
Preheat		
- Temperature Min (Ts min)	150°C	
- Temperature Max (Ts max)	200°C	
- Time (ts) from (Tsmin to Tsmax)	60-120 seconds	
Ts max to T <sub>∟</sub>		
- Temperature Min (Ts min)	3°C/second max.	
Time maintained above:		
- Liquid us temperature (T <sub>L</sub> )	217°C	
- Time (t <sub>L</sub> ) maintained above TL	60-150 seconds	
Peak package body temperature (Tp)	260 +0/-5°C	
Time with 5°C of actual Peak	30 seconds	
- Temperature (tp)		
Ramp-down Rate	6°C/second max.	
Time 25°C to Peak Temperature	8 minutes max.	



Test Results: 0 fail/ 32 tested Reflow count: 3 cycles



## Marking Identification



Row 1 A8931

Row 2 Lot number & Date

